INDEX

SPECIFICATIONS

KEYBOARD
61 keys C1 ~ c4 (5 octaves)

TONE SELECTORS

Channel I	Channel II
STRING 1	STRING 2
STRING 3	STRING 4
BRASS 1	BRASS 2
FLUTE	BRASS 3
ELECTRIC PIANO	BASS
CLAVICHORD 1	CLAVICHORD 2
HARPSICHORD 1	HARPSICHORD 2
ORGAN 1	ORGAN 2
GUITAR 1	GUITAR 2
FUNKY 1	FUNKY 2
FUNKY 3	FUNKY 4
MEMORY 1	MEMORY 2
MEMORY 3	MEMORY 4
PANEL	PANEL

TONE SLIDE CONTROLS
- VCO (Voltage Controlled Oscillator) Section
  - SPEED
  - PWM (Pulse Width Modulation)
  - PW (Pulse Width)
  - ⊓ (Square Wave) Switch (On/Off)
  - ⋀ (Sawtooth Wave) Switch (On/Off)
  - NOISE
- VCF (Voltage Controlled Filter) Section
  - HPF (High Pass Filter)
  - RES_H (Resonance High)
  - LPF (Low Pass Filter)
  - RES_L (Resonance High)
  - IL (Initial Level)
  - AL (Attack Level)
  - A (Attack Time)
  - D (Decay Time)
  - R (Release Time)
- VCA (Voltage Controlled Oscillator) Section
  - VCF LEVEL
  - ~ (Sine Wave)
  - A (Attack Time)
  - D (Decay Time)
  - S (Sustain Level)
  - R (Release Time)
  - LEVEL
- TOUCH RESPONSE Section
  - INITIAL Controls
    - BRILLIANCE
    - LEVEL
  - AFTER Controls
    - BRILLIANCE
    - LEVEL
- MEMORY BANKS (VCO, VCF, VCA, TOUCH RESPONSE)
  - Memory 1
  - Memory 2
  - Memory 3
  - Memory 4

EFFECT CONTROLS
- RING MODULATOR
  - ATTACK TIME
  - DECAY TIME
  - DEPTH
  - SPEED
  - MODULATION
- TOUCH RESPONSE
  - INITIAL Control
    - PITCH BEND
  - AFTER Controls
    - SUB. OSCILLATOR
      - SPEED
      - VCO
      - VCF
- SUB. OSCILLATOR
  - FUNCTION Selector (~, ⋀, ⋁, ⊓ NOISE, EXTERNAL)
  - SPEED
  - VCO
  - VCF
  - VCA
- SUSTAIN
  - Select Switch (I/II)
  - SUSTAIN Time
- PORTAMENTO/GLISSANDO
  - Select Switch (PORTAMENTO/GLISSANDO)
  - PORTAMENTO/GLISSANDO Time
- FINGER BOARD
- RESONANCE
- BRILLIANCE
- KEYBOARD CONTROLS
  - BRILLIANCE
    - LOW
    - HIGH
  - LEVEL
    - LOW
    - HIGH
- ORCHESTRA EFFECT
  - Voice Selector (ON/OFF)
  - TREMOLO
  - CHORUS
  - SPEED
  - DEPTH
- FEET levers (Channels I & II)
  (16', 8', 5-1/3', 4', 2-2/3', 2')

OTHER CONTROLS
- PITCH (Coarse & Fine Controls)
- DETUNE (Channel II only)
- CH. I/CH. II MIX
- Master VOLUME
- FOOT CONTROLLER (Pedal)
- EXTERNAL IN LEVEL
- FOOT PEDAL Selectors
  (EXP./EXP. & WAH)
- FOOT SWITCH Selectors (On/Off)
  - SUSTAIN
  - PORTAMENTO/GLISSANDO
- POWER SWITCH (with Indicator)

OTHER FITTINGS
- EXTERNAL IN Jack
- OUTPUT Jacks with Selector (HIGH/LOW)
- Headphone Jack
- FOOT CONTROLLER Jack
- FOOT SWITCH Jack
- Carrying Straps
- Metal Corners
- Slip Fittings
- Power Cord Storage Space

OUTPUT SPECIFICATIONS
- Nominal Levels
- High: 0 dBm (800 mVrms)
- Low: −20 dBm (80 mVrms)
- Headphones: −10 dBm (250 mVrms)
- Nominal Impedance
  - Output Jacks: 600 ohms
  - Headphone Jack: 8 ohms

CIRCUITRY
- Solid State
- Power Consumption: 180 Watts
- Power Source: AC. 50/60 Hz

DIMENSIONS (W x H x D)
- Assembled: 1,205 x 945 x 702 mm (47-1/2 x 37-1/4 x 27-5/8")
- In Case: 1,205 x 290 x 702 mm (47-1/2 x 11-3/8 x 27-5/8")
- Height to Keyboard: 842 mm (33-1/8")

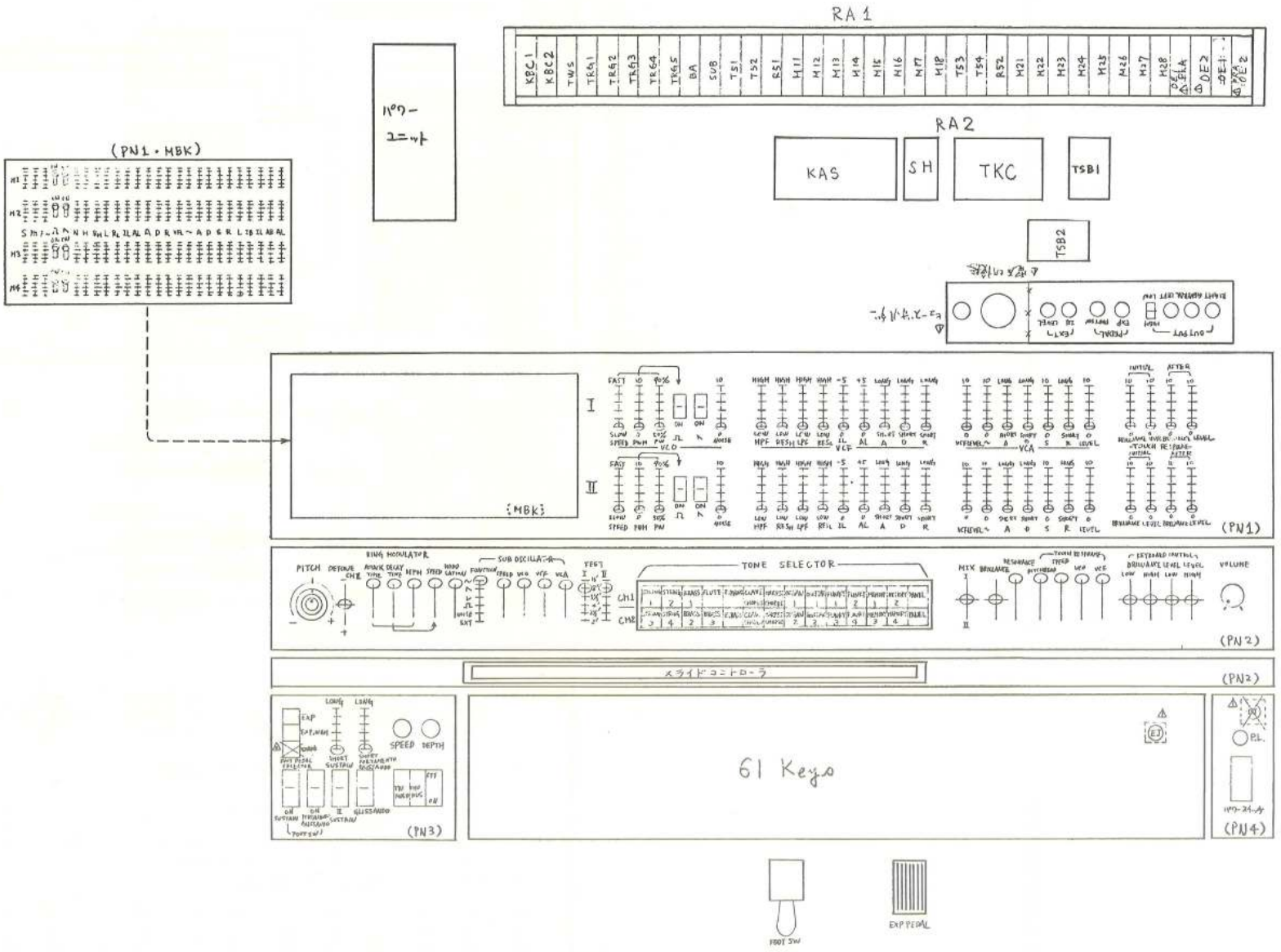
WEIGHT
100 kg (220.5 lbs) Including Standard Accessories

FINISH
Black Leatherette with Walnut Veneer Panel Siding

STANDARD ACCESSORIES
- Removable Cover
- Vinyl Carrying Bag
- Foot Controller (Expression Pedal)
- Foot Switch Pedal
- Music Rest
- Detachable Casters

Specification subject to change without notice.

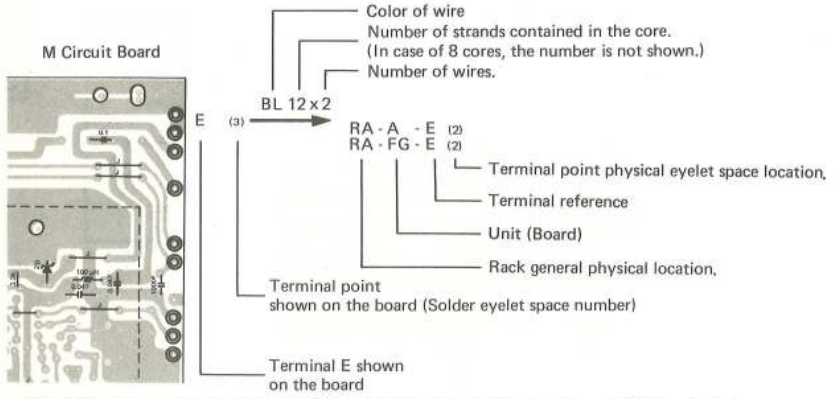
PANEL LAYOUT



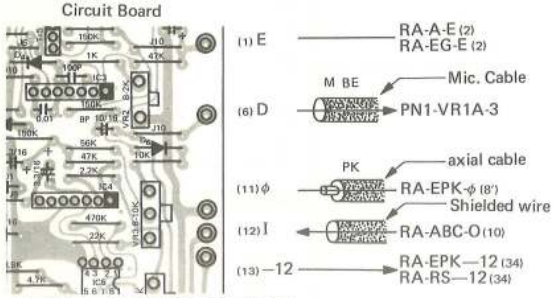
CORDING GUIDE

1) CIRCUIT BOARD AND WIRING

The coding system is as follows

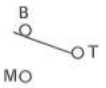


Two (2) black wires are connected to "E" on M circuit board. One goes to each "E" terminal of A and FG boards.



(View from the side points mounted)

2) SWITCH



B : Break
T : Transfer
M : Make

3) Transistor and FET

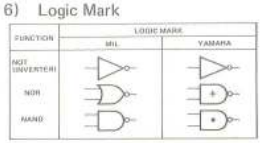
4) ABBREVIATIONS OF WIRE COLOR IN ELECTONE

BL ..... BLACK	BR ..... BROWN	RE ..... RED	OR ..... ORANGE
YE ..... YELLOW	GR ..... GREEN	BE ..... BLUE	VI ..... VIOLET
GY ..... GRAY	WH ..... WHITE	GG ..... GRASS GREEN	SB ..... SKY BLUE
PK ..... PINK	TR ..... TRANSPARENT	TP ..... TIN PLATED WIRE	

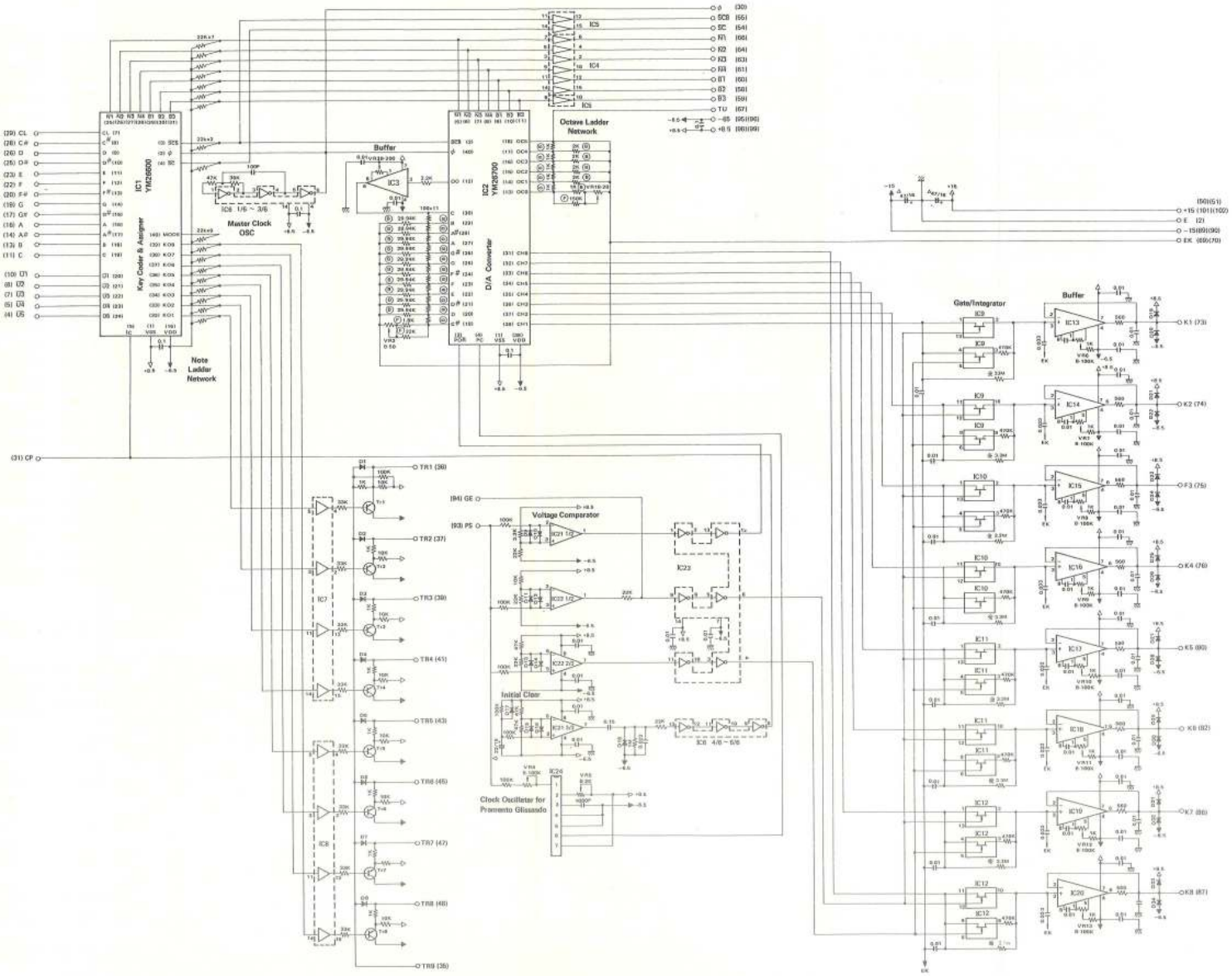
5) WIRE COLOR — Musical Note indication

C	C#	D	D#	E	F	F#	G	G#	A	A#	B
BR	RE	OR	YE	GR	BE	VI	GY	WH	GG	SB	PK

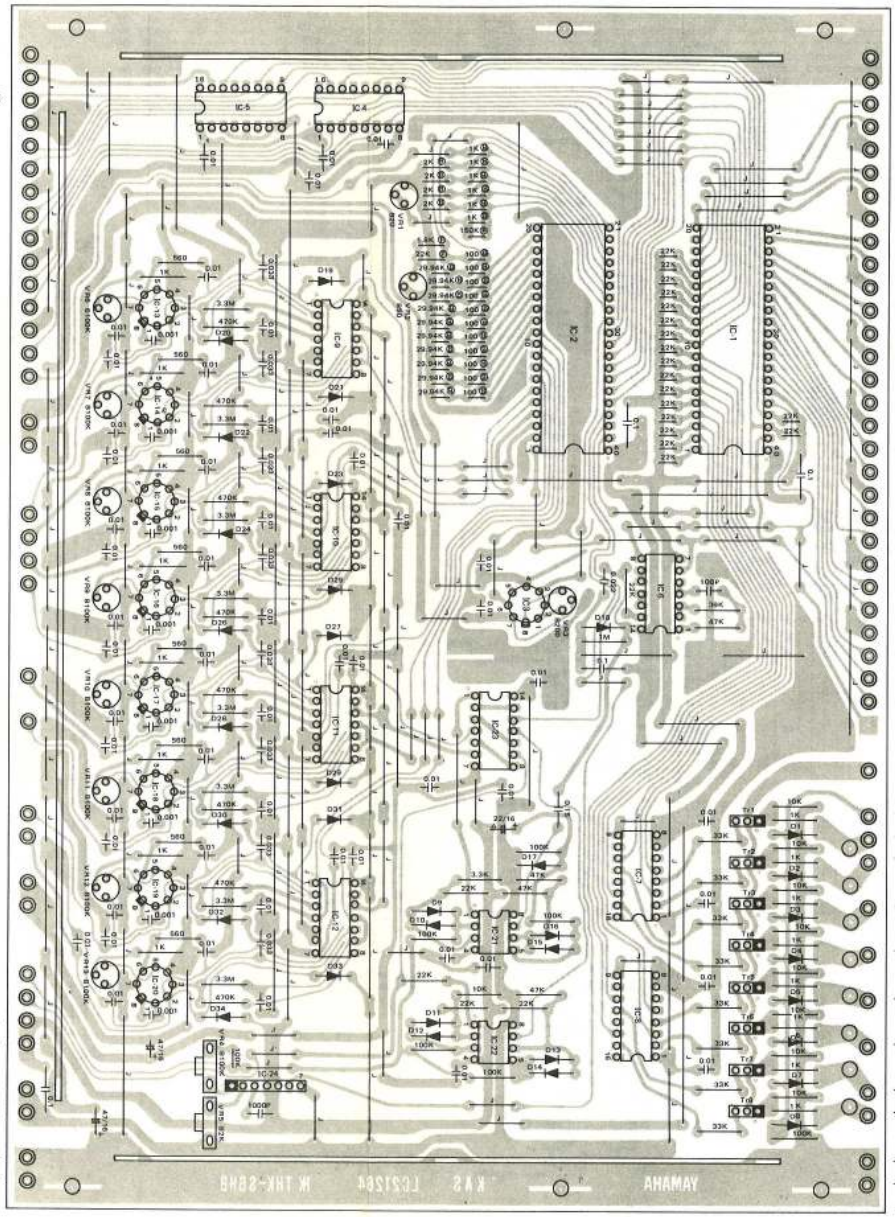
6) Logic Mark



KAS Circuit Diagram



KAS Circuit Board & Wiring

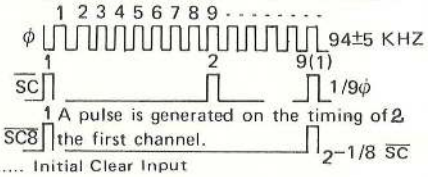


KEY CODER & KEY ASSIGNER LSI (YM26600)

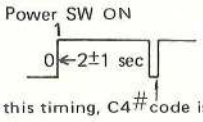
The LSI detects what keys are held down by judging the pulse combination of the octave and note.
It also generates the seven bit key code, which is processed by time sharing, in accordance with the key held down.

Pin. No. Pin Name

1. VSS .....+8.5V Power Supply
2. ϕ ..... Master Clock Input
3. $\overline{SC}$ ..... Synchro-clock Output
4. $\overline{SC8}$ ..... Synchro-clock Output on the first channel.



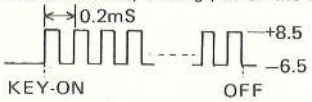
5. IC ..... Initial Clear Input



On this timing, C4# code is memoried.

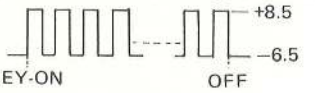
6. VDD .....–6.5V Power Supply Input
7. CL
8. C#
19. C .... Note on data input

When the key is depressed, the pulse is supplied the corresponding pin of the note.



20. V1
24. V5 .... Octave on data input

When the key is depressed, the pulse is supplied to the corresponding pin of the octave.



25. $\overline{N1}$
28. $\overline{N4}$ .... Note code data output

1: –6.5V 0: +8.5V

	C#	D	D#	E	F	F#	G	G#	A	A#	B	C
N1	1	0	1	1	0	1	1	0	1	1	0	1
N2	1	1	0	1	1	0	1	1	0	1	1	0
N3	1	1	1	0	0	0	1	1	1	0	0	0
N4	1	1	1	1	1	1	0	0	0	0	0	0

29. B1
31. B3 .... Octave Code Data Output

1: –6.5V 0: +8.5V

	C2	C2#~C3	C3#~C4	C4#~C5	C5#~C6
B1	0	1	0	1	0
B2	1	0	0	1	1
B3	1	1	1	0	0

32. KO1
39. KO8 .... Key on Data Output

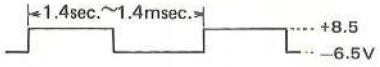
The number of note sounded is variable by using this pin.
i.e.) Up to 4 notes: Supply –6.5V to $\overline{KO5}$.
Up to 3 notes: Supply –6.5V to $\overline{KO4}$.

40. Mode .... Switching output for sound model
For 8 notes .... Supply –6.5V (1)
For 7 notes .... Supply +8.5V (0)

D-A CONVERTER LSI (YM26700)

The time shared key data is supplied to the LSI. Analog DC voltage is produced in corporation with key by the data and supplied to each channel.

1. VSS ..... +8.5V Power Supply
2. $\overline{SC8}$ ..... Synchro-clock input on the first channel.
3. $\overline{POR}$ ..... Portamento and Glissando operation. When the portament VR is turned on, +8.5V is supplied to the pin and actuate.
4. PC ..... Clock input for Portament and Glissando operation.



The frequency is variable by Changing the portamento VR.

5. $\overline{N1}$
8. $\overline{N4}$ ..... Note code data input
Note code data is supplied to the pins from key coder LSI.
9. $\overline{B1}$
11. $\overline{B3}$ ..... Octave code data input
Octave code data is supplied to the pins from key coder LSI.
12. OO ..... Output for octave key voltage. (8ch time sharing)
Provided the output key voltage for the octave selected from octave code.
13. OCT0
18. OCT5 ..... Input for octave key voltage.

* TU pin: 4.0V

	OCT0	OCT1	OCT2	OCT3	OCT4	OCT5
Voltage	0.25V	0.5V	1.0V	2.0V	4.0V	4.0V

The voltage of TU line is divided by the ladder composed resistors and supplied to each pin constantly.

19. C#
30. C .... Input for note key voltage

OO pin: 4.0V

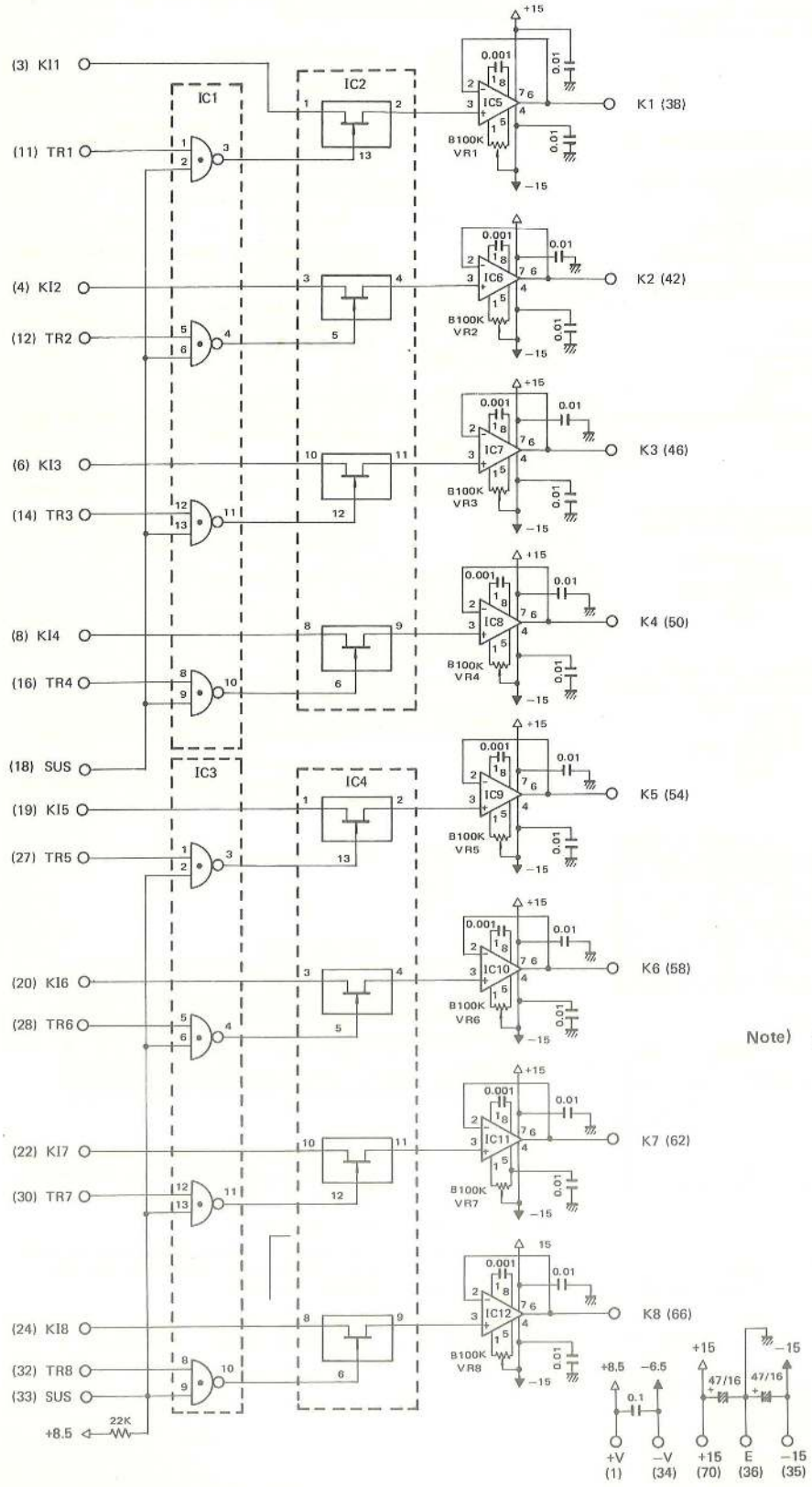
	C#	D	D#	E	F	F#
Voltage	2.119	2.245	2.378	2.520	2.670	2.828

	G	G#	A	A#	B	C
Voltage	2.997	3.175	3.364	3.564	3.775	4.0V

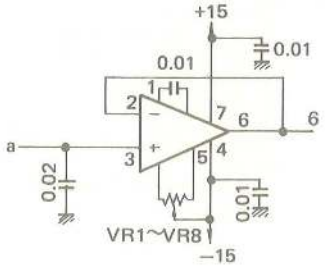
The voltage of OO line is divided by the ladder composed resistors and supplied to each pin constantly.

31. CH8
38. CH1 .... Key voltage output
The output of voltage determined by each key is provided in accordance with the channel key code.
39. VDD .... –6.5V Power Supply, Input
40. ϕ ..... Master Clock Input f=94±5KHz

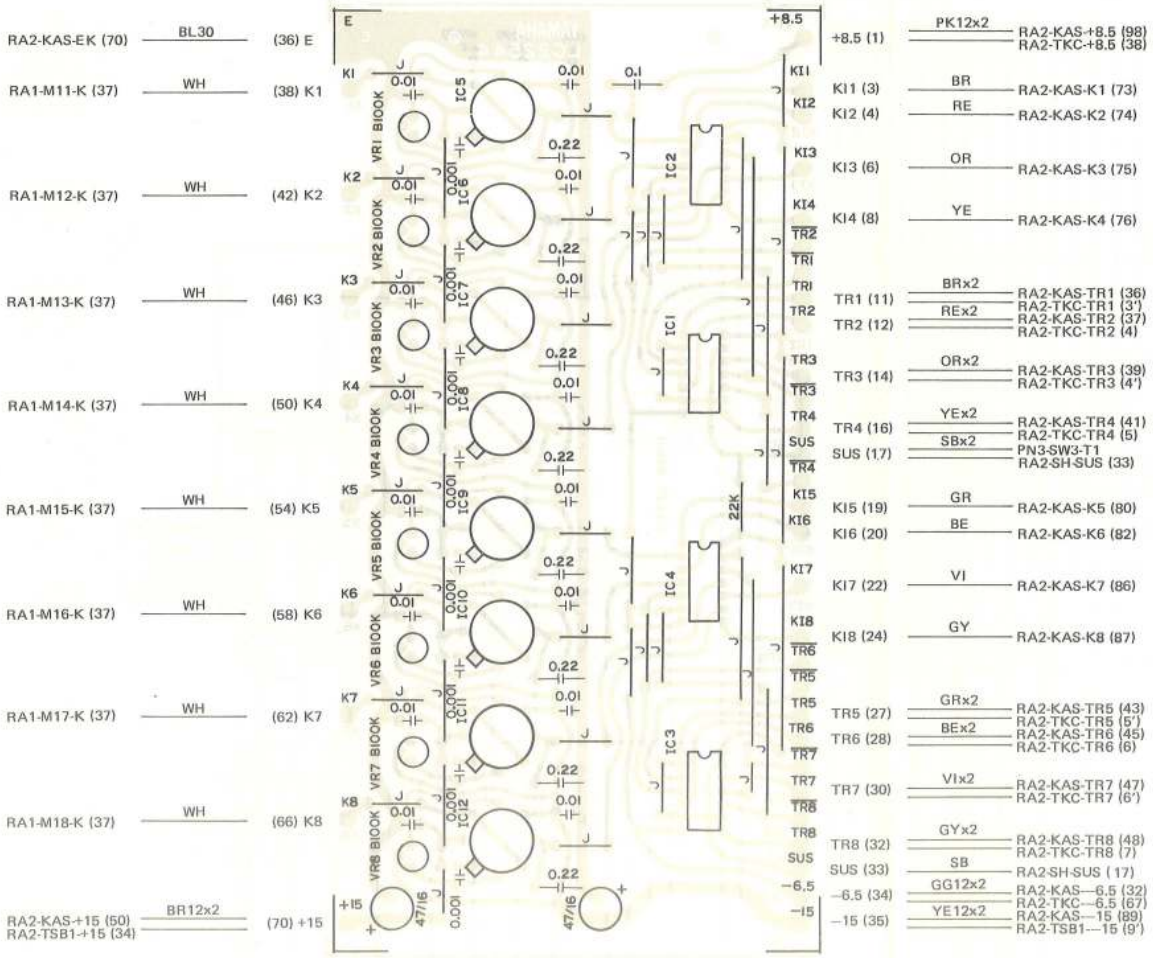
SH Circuit Diagram



Note) 1. IC5~IC12 :CA4016P

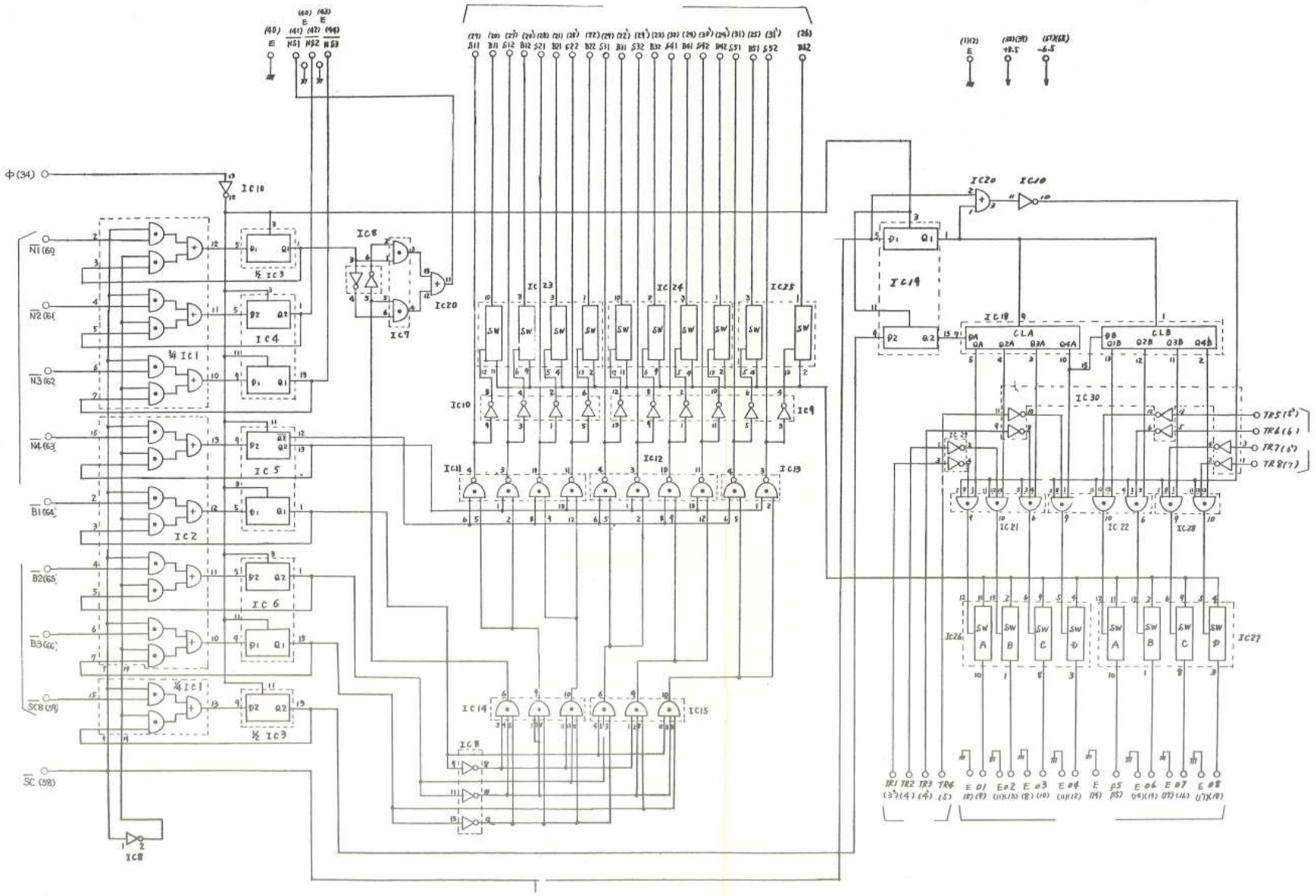


SH Circuit Board & Wiring

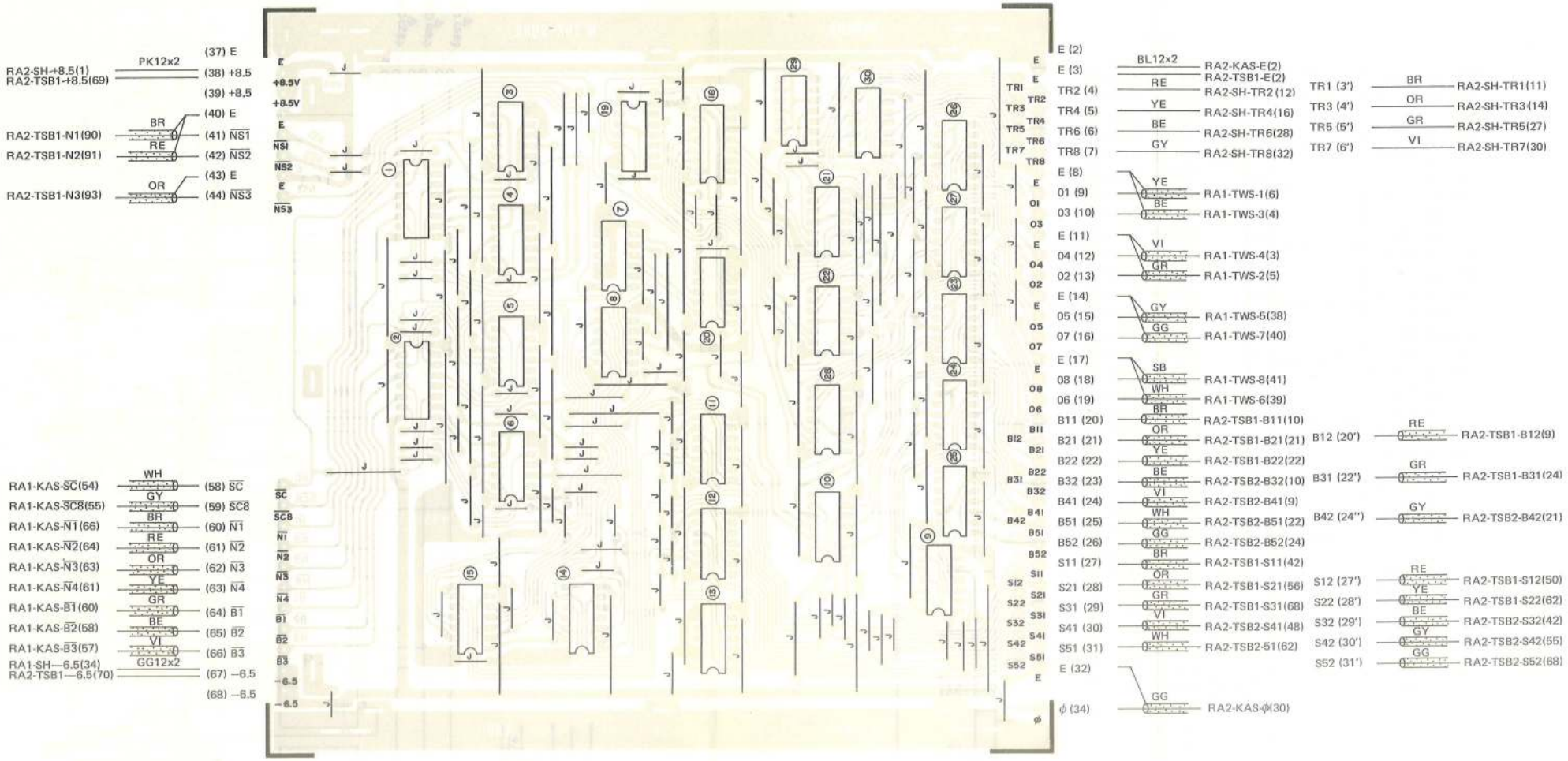


Note) 1. ※ Mark : Ceramic Capacitor 0.1
2. Volume : 332H
3. IC1,3 : TC4011P
IC2,4 : TC4016P
IC5~12 : CA3140T
4. Print Board #22544

TKC Circuit Diagram

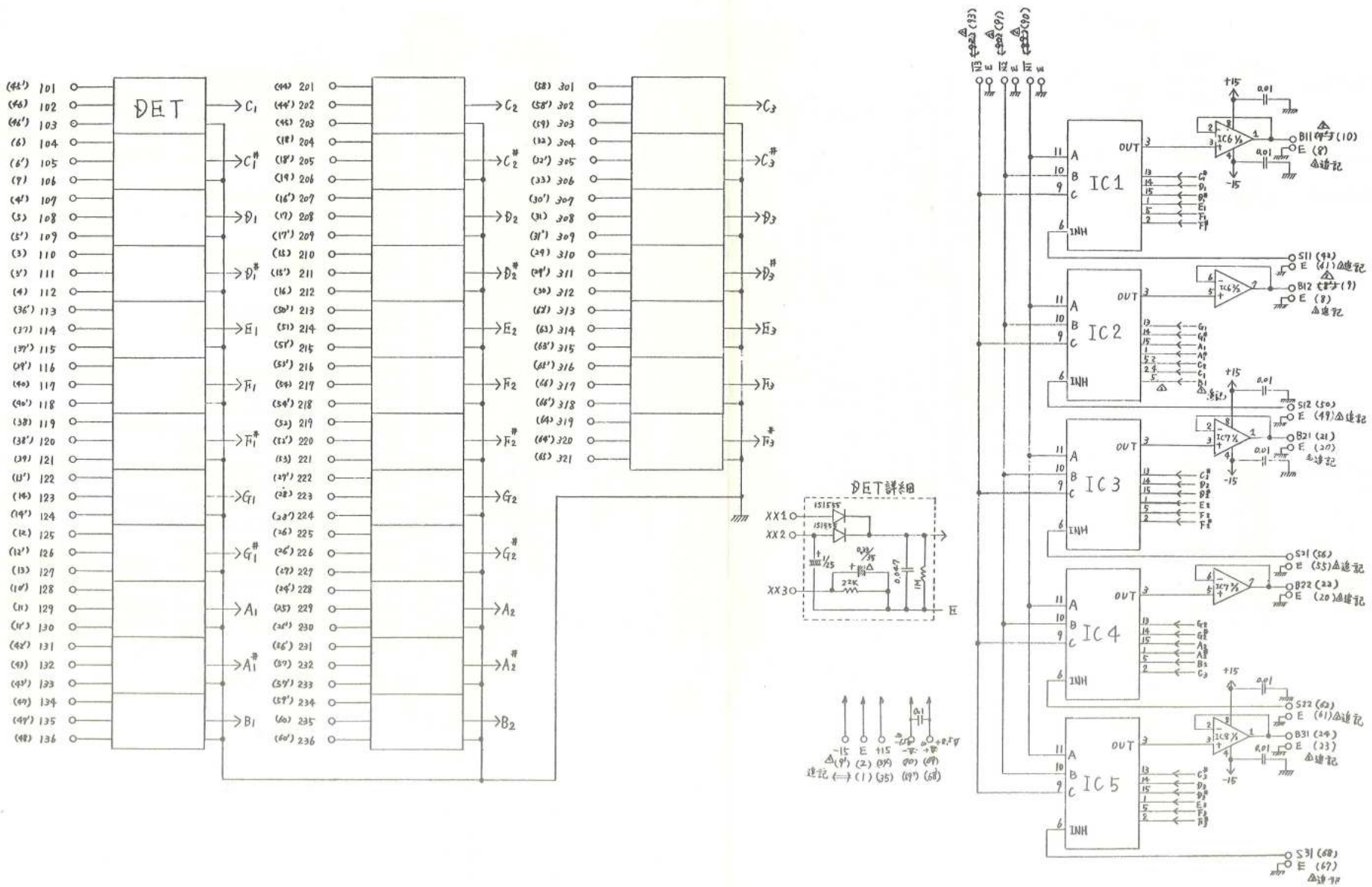


TKC Circuit Board & Wiring

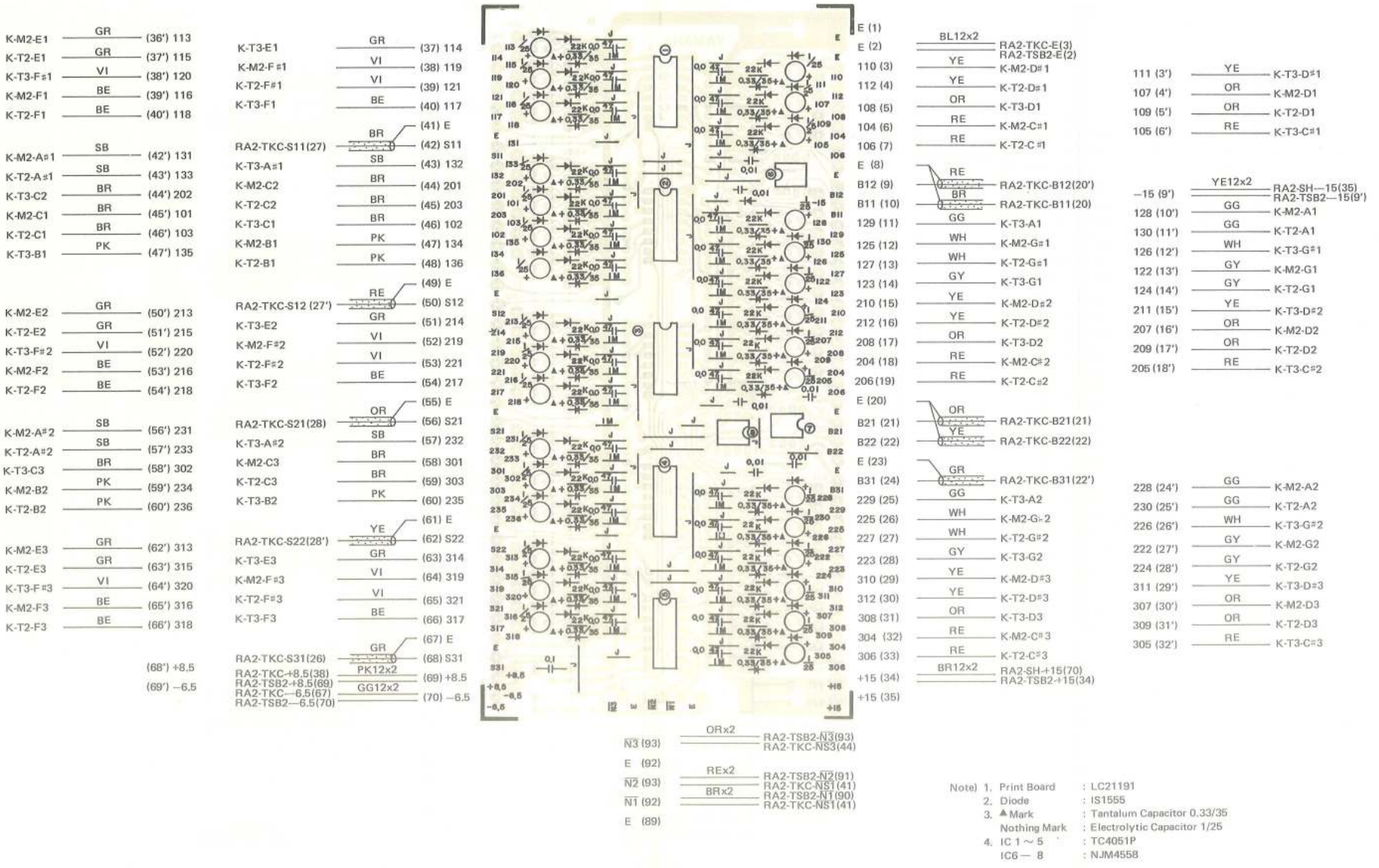


Note) 1. Print Board : LC21053
2. IC 1, 2 : TC4019P
IC 3 ~ 6, 19 : TC4013P
IC7 : TC4081P
IC 8 ~ 10, 29, 30 : TC4069P
IC 11 ~ 13 : TC4011P
IC 14, 15, 21, 22, 28 : TC4073P
IC 18 : TC4015P
IC 20 : TC4071P
IC 23 ~ 27 : TC4016P
3. Camber-stop Hardware : #03992

TSB 1 Circuit Diagram

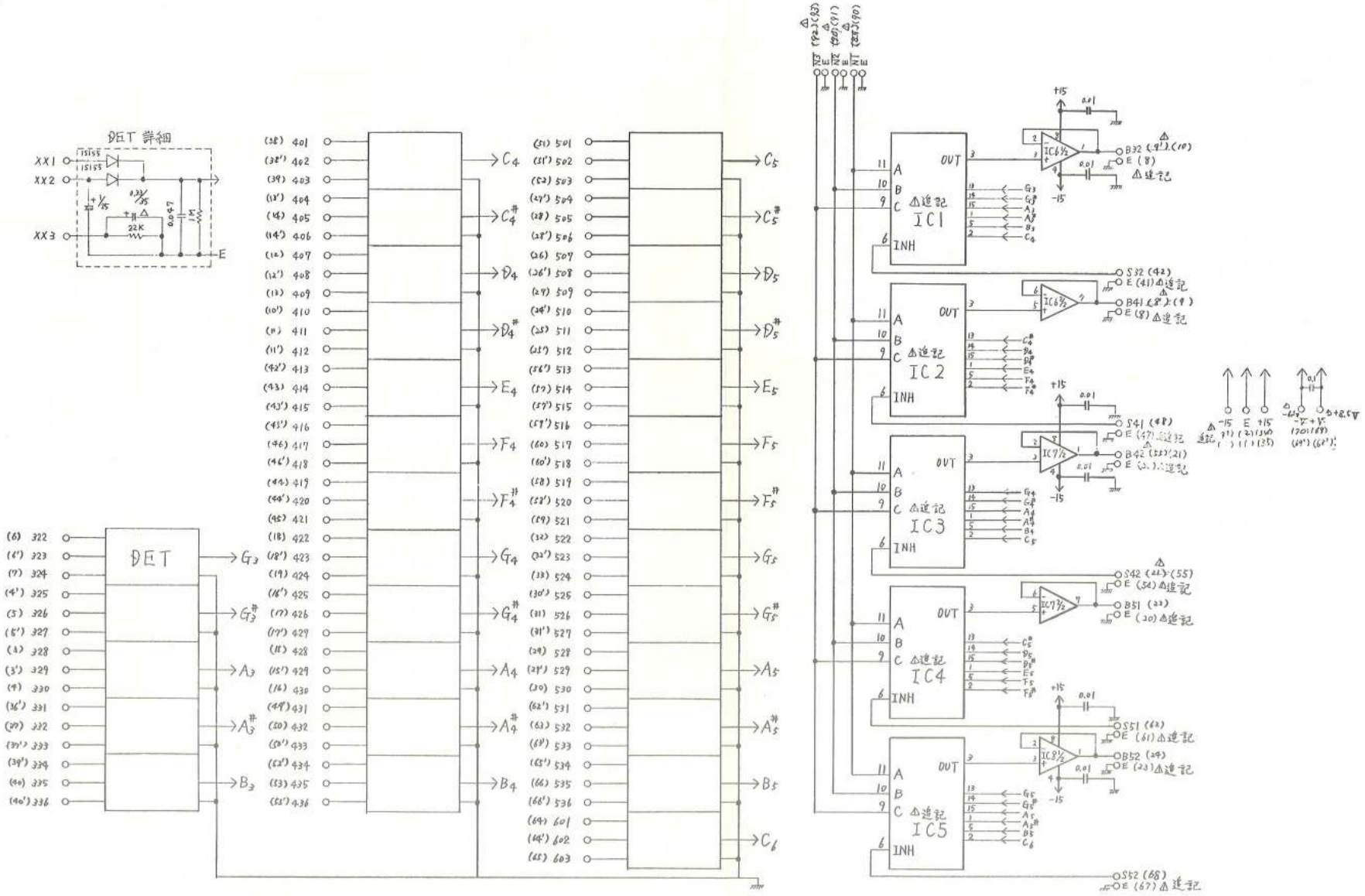


TSB 1 Circuit Board & Wiring

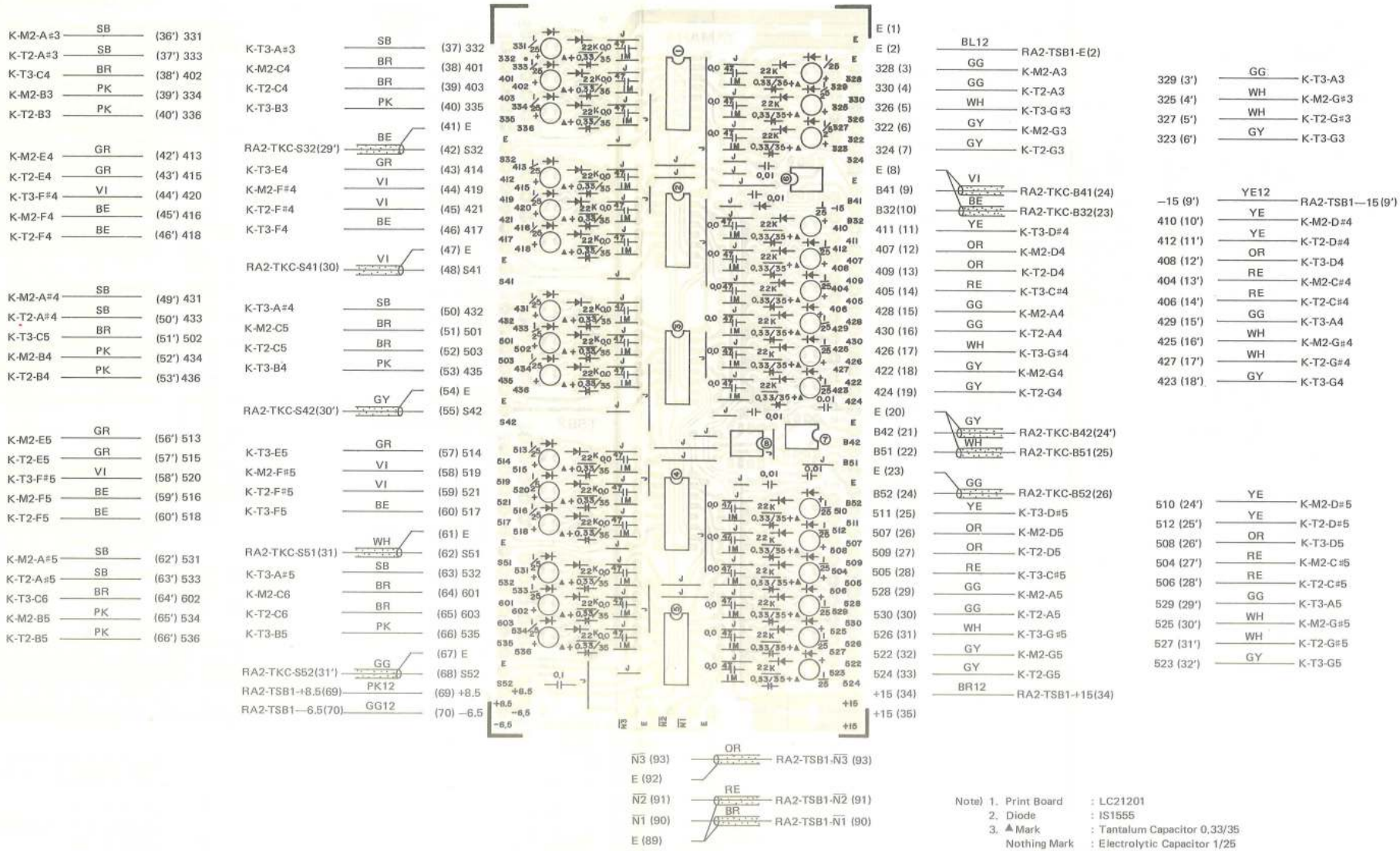


Note) 1. Print Board : LC21191
2. Diode : IS1555
3. ▲Mark : Tantalum Capacitor 0.33/35
Nothing Mark : Electrolytic Capacitor 1/25
4. IC 1 ～ 5 : TC4051P
IC6 — 8 : NJM4558

TSB 2 Circuit Diagram

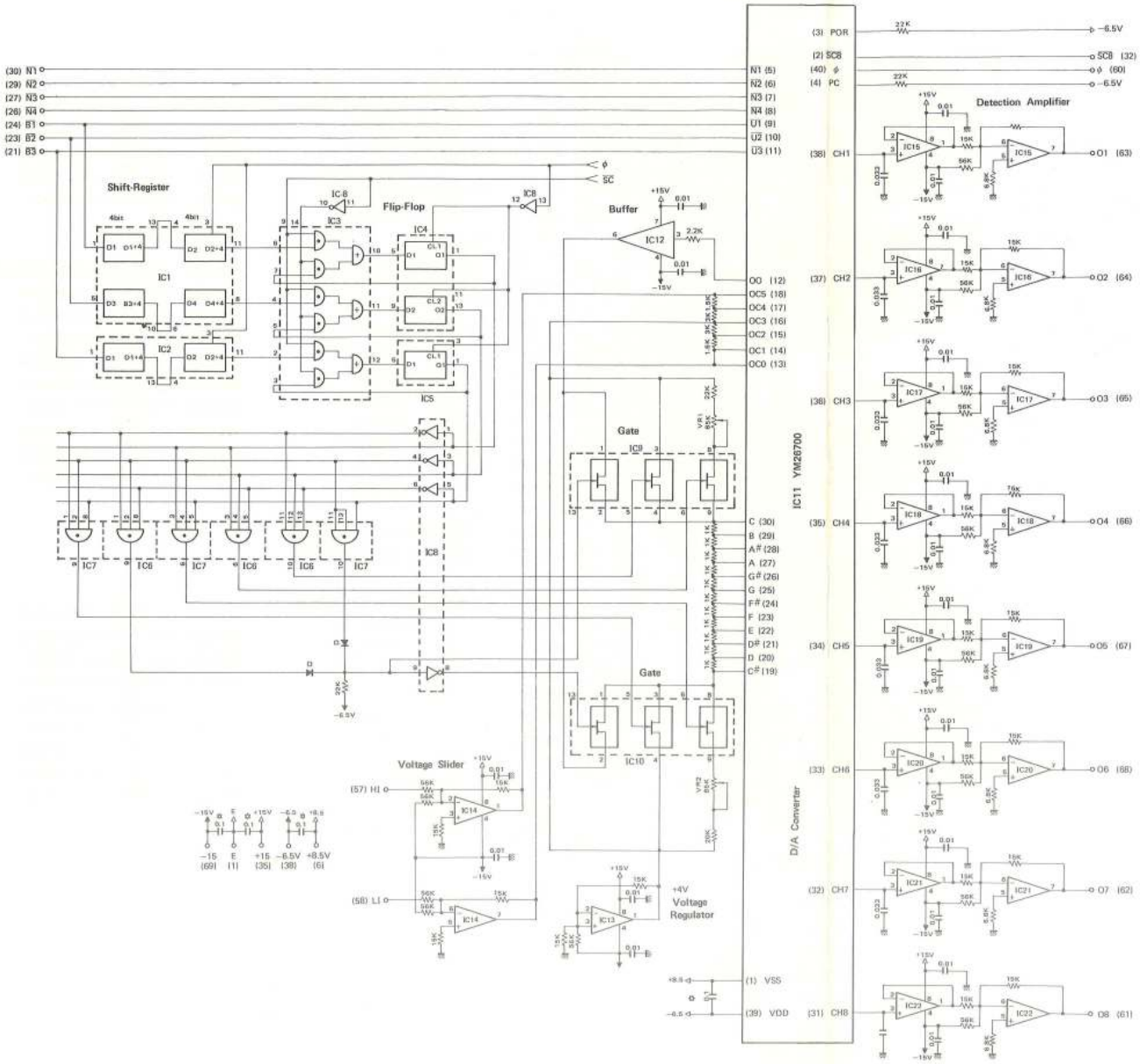


TSB 2 Circuit Board & Wiring



Note) 1. Print Board : LC21201
2. Diode : IS1555
3. ▲ Mark : Tantalum Capacitor 0,33/35
Nothing Mark : Electrolytic Capacitor 1/25
4. IC 1 ~ 5 : TC4051P
IC 6 ~ 8 : NJM4558

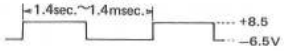
KBC 1 · 2 Circuit Diagram



D-A CONVERTER LSI (YM26700)

The time shared key data is supplied to the LSI. Analog DC voltage is produced in corporation with key by the data and supplied to each channel.

1. VSS ..... +8.5V Power Supply
2. $\overline{SCB}$ ..... Synchro-clock input on the first channel.
3. $\overline{POR}$ ..... Portamento and Glissando operation. When the portament VR is turned on, +8.5V is supplied to the pin and actuate.
4. PC ..... Clock input for Portament and Glissando operation.



The frequency is variable by Changing the portamento VR.

5. $\overline{N1}$ ..... Note code data input
8. $\overline{N4}$ Note code data is supplied to the pins from key coder LSI.

9. $\overline{B1}$ ..... Octave code data input
11. $\overline{B3}$ Octave code data is supplied to the pins from key coder LSI.

12. OO ..... Output for octave key voltage. (8ch time sharing)
Provided the output key voltage for the octave selected from octave code.

13. OCT0 ..... Input for octave key voltage.
18. OCT5

* TU pin: 4.0V

	OCT0	OCT1	OCT2	OCT3	OCT4	OCT5
Voltage	0.25V	0.5V	1.0V	2.0V	4.0V	4.0V

The voltage of TU line is divided by the ladder composed resistors and supplied to each pin constantly.

19. $C^{\#}$ .... Input for note key voltage
30. C

OO pin: 4.0V

	$C^{\#}$	D	$D^{\#}$	E	F	$F^{\#}$
Voltage	2.119	2.245	2.378	2.520	2.670	2.828

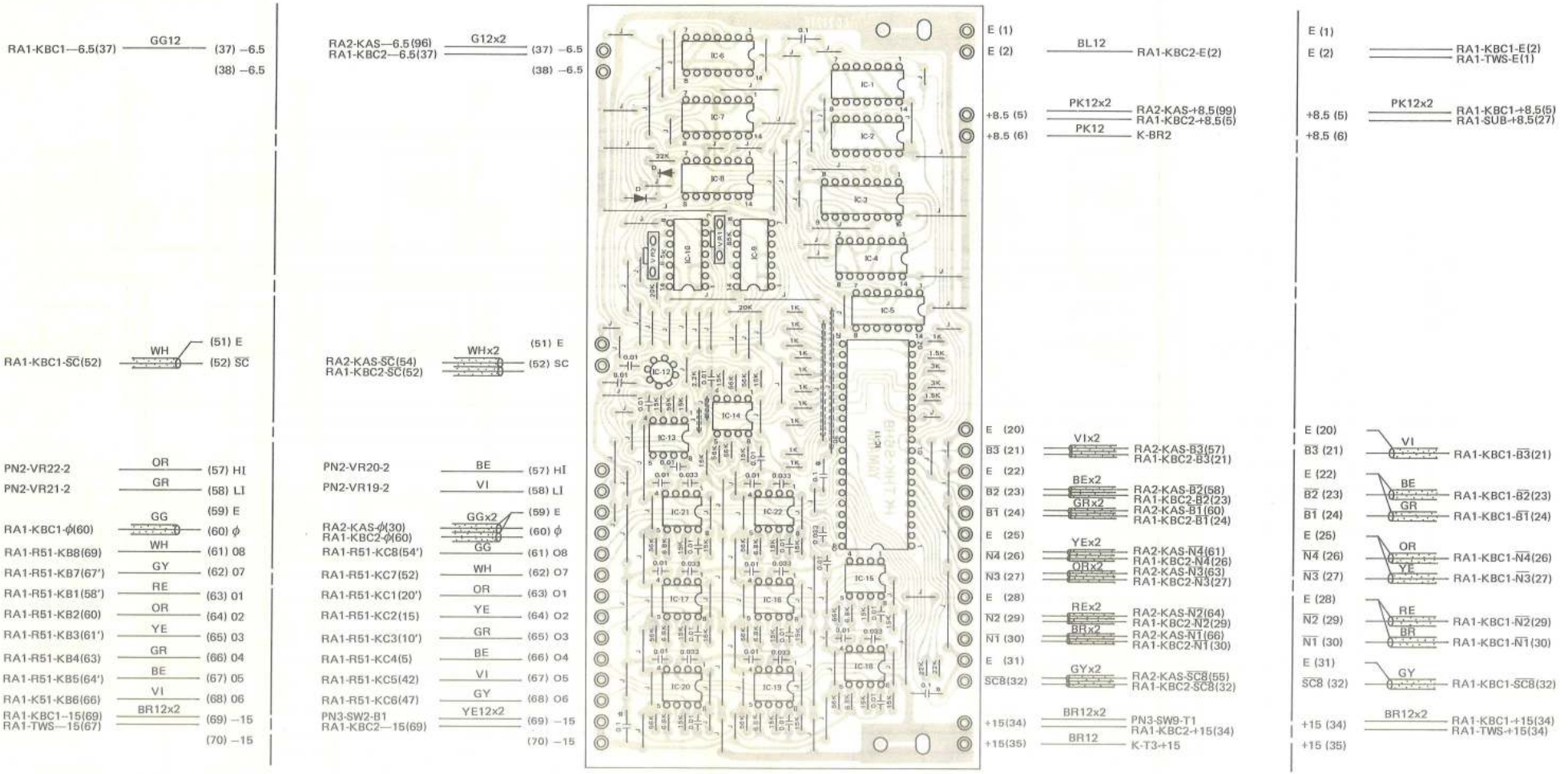
	G	$G^{\#}$	A	$A^{\#}$	B	C
Voltage	2.997	3.175	3.364	3.564	3.775	4.0V

The voltage of OO line is divided by the ladder composed resistors and supplied to each pin constantly.

31. CH8 ..... Key voltage output
38. CH1 The output of voltage determined by each key is provided in accordance with the channel key code.

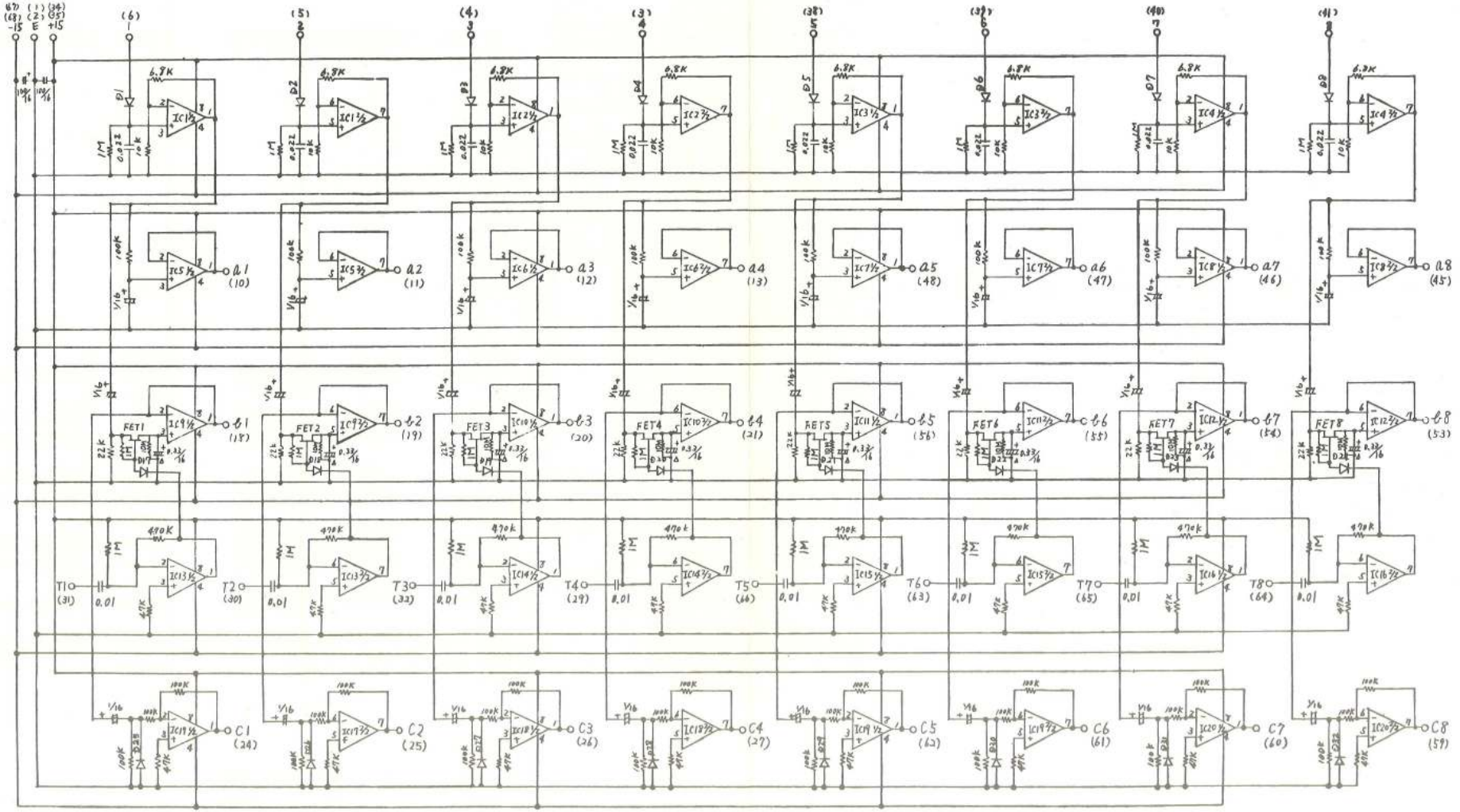
39. VDD .... −6.5V Power Supply, Input
40. φ ..... Master Clock Input f=94±5KHz

KBC 1 · 2 Circuit Board & Wiring

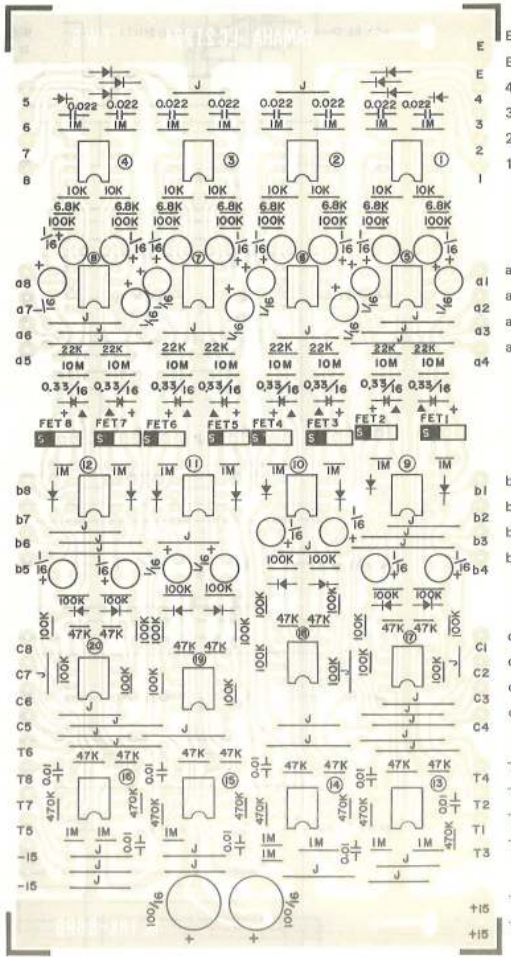


Note) 1. Print Board LC21216
2. Resistor
22K : ±5%
Other : ±1%
3. Diode 1S1555
4. VR1,2 (V10K4A-5-2)
5. IC1, 2 : CD4006AE
IC3 : TC4019P
IC4,5 : TC4013P
IC6,7 : TC4073P
IC8 : TC4069P
IC9,10 : TC4016P
IC11 : YM26700
IC12 : LM310
IC13~22 : NJM4558
6. ☆Mark : Ceramic Capacitor
Other : Mylar Capacitor

TWS Circuit Diagram



TWS Circuit Board & Wiring



RA2-TKC-05(15) — GY — (38) 5
RA2-TKC-06(19) — WH — (39) 6
RA2-TKC-07(16) — GG — (40) 7
RA2-TKC-08(18) — SB — (41) 8

RA1-TRG2-a8(31) — PK — (45) a8
RA1-TRG2-a7(28) — SB — (46) a7
RA1-TRG2-a6(24) — GG — (47) a6
RA1-TRG2-a5(21) — WH — (48) a5

RA1-TRG1-b8(31) — BR — (53) b8
RA1-TRG1-b7(28) — PK — (54) b7
RA1-TRG1-b6(24) — SB — (55) b6
RA1-TRG1-b5(21) — GG — (56) b5

RA1-TRG4-c8(65) — OR — (59) c8
RA1-TRG4-c7(63) — RE — (60) c7
RA1-TRG4-c6(59) — BR — (61) c6
RA1-TRG4-c5(55) — PK — (62) c5
RA2-KAS-TR6(45) / RA1-M16-TR(63) — BEx2 — (63) T6
RA2-KAS-TR8(48) / RA1-M18-TR(63) — GYx2 — (64) T8
RA2-KAS-TR7(47) / RA1-M17-TR(63) — VIx2 — (65) T7
RA2-KAS-TR5(43) / RA1-M15-TR(63) — GRx2 — (66) T5
RA1-KBC1—15(69) — YEx2 — (67) –15
RA1-TRG1—15(67) — (68) –15

E (1) — BL12x2 — RA1-KBC2-E(2) / RA1-TRG1-E(1)
E (2)
4 (3) — VI — RA2-TKC-04(12)
3 (4) — BE — RA2-TKC-03(10)
2 (5) — GR — RA2-TKC-02(13)
1 (6) — YE — RA2-TKC-01(9)

a1 (10) — GR — RA1-TRG2-a1(6)
a2(11) — BE — RA1-TRG2-a2(9)
a3 (12) — VI — RA1-TRG2-a3(13)
a4 (13) — GY — RA1-TRG2-04(16)

b1(18) — BE — RA1-TRG1-b1(6)
b2 (19) — VI — RA1-TRG1-b2(9)
b3 (20) — GY — RA1-TRG1-b3(13)
b4 (21) — WH — RA1-TRG1-b4(16)

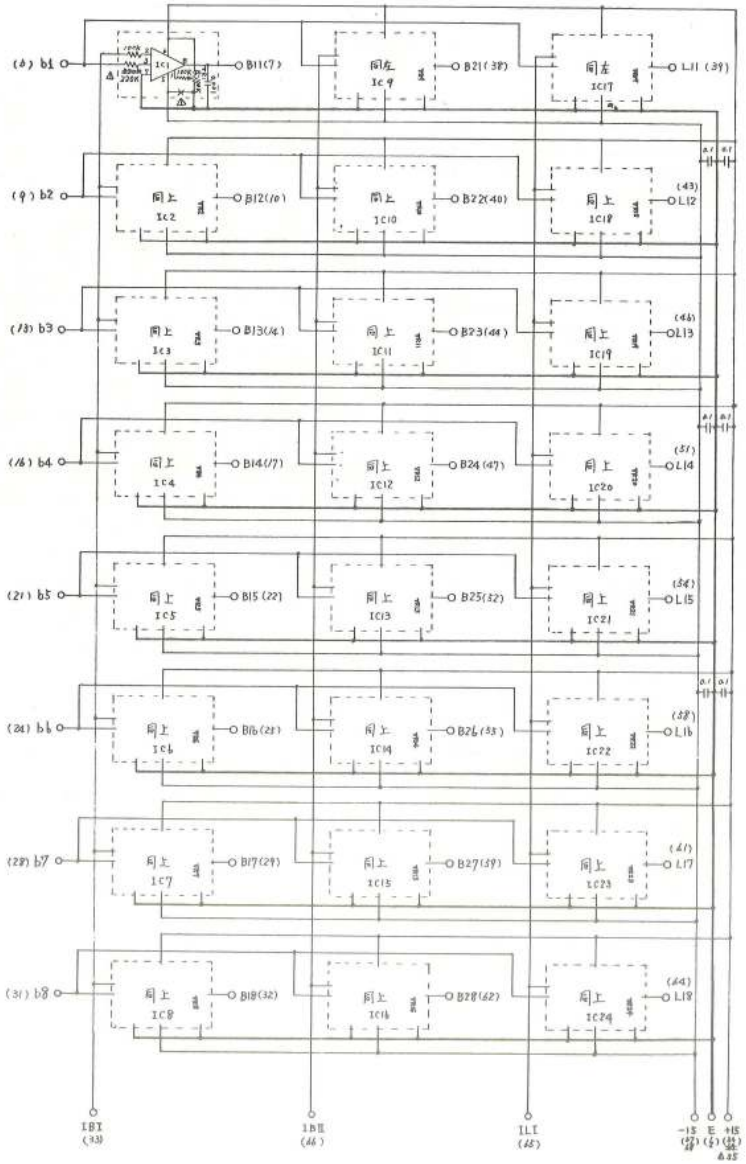
c1 (24) — GY — RA1-TRG4-c1(40)
c2 (25) — WH — RA1-TRG4-c2(44)
c3 (26) — GG — RA1-TRG4-c3(47)
c4 (27) — SB — RA1-TRG4-c4(51)

T4 (29) — YEx2 — RA2-KAS-TR4(41) / RA1-M14-TR(63)
T2 (30) — REx2 — RA2-KAS-TR2(37) / RA1-M12-TR(63)
T1 (31) — BRx2 — RA2-KAS-TR1(36) / RA1-M11-TR(63)
T3 (32) — ORx2 — RA2-KAS-TR3(39) / RA1-M13-TR(63)

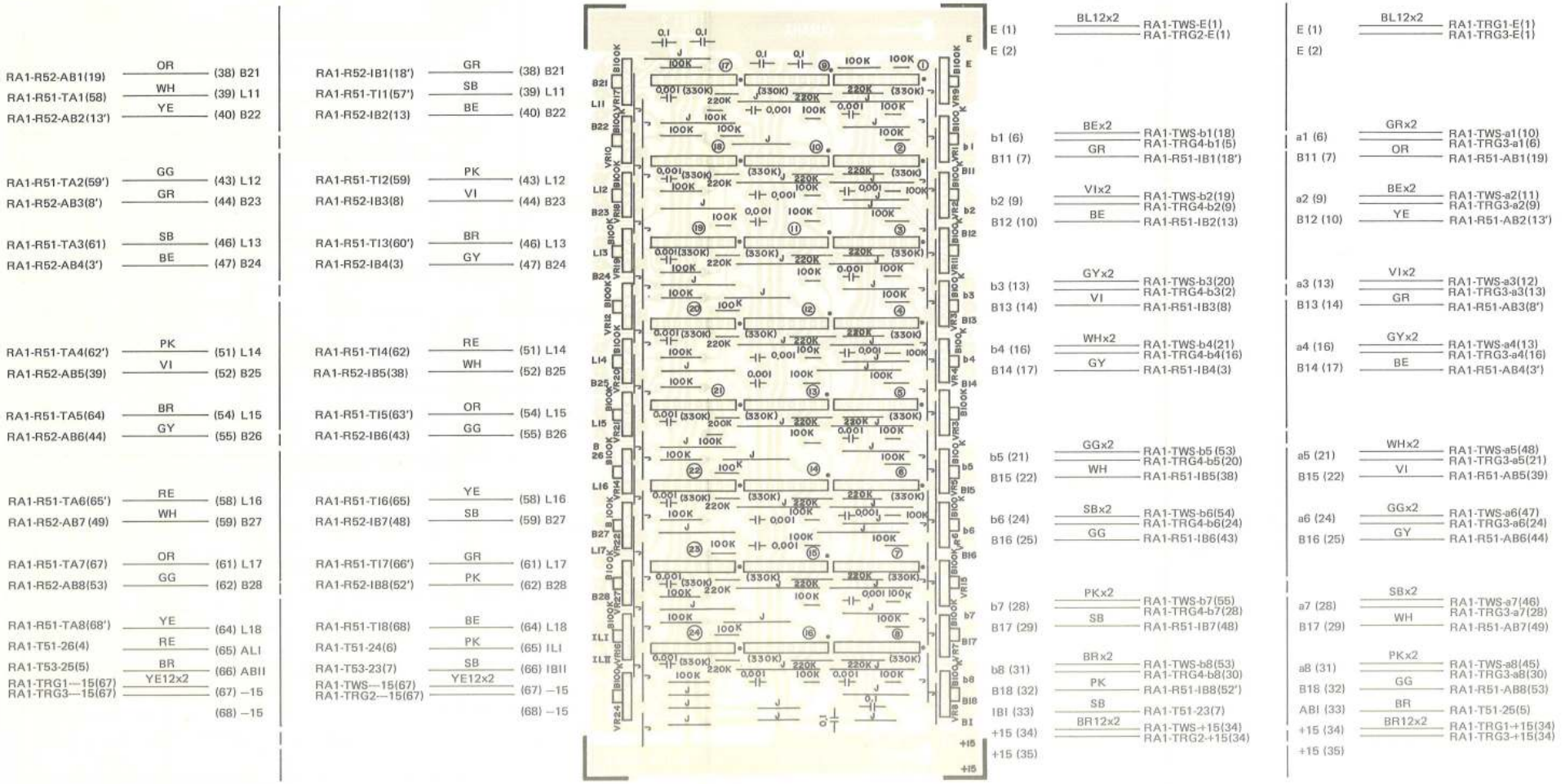
+15 (34) — BR12x2 — RA1-KBC2-+15(34) / RA1-TRG1-+15(34)
+15 (35)

Note) 1. Print Board : LC21223
2. FET 1 ~ 8 : 2SK30
3. Diode 1 ~ 32 : IS1555
4. IC 1 ~ 20 : NJM4558
5. △Mark : Tantalum Capacitor

TRG 1·2 Circuit Diagram

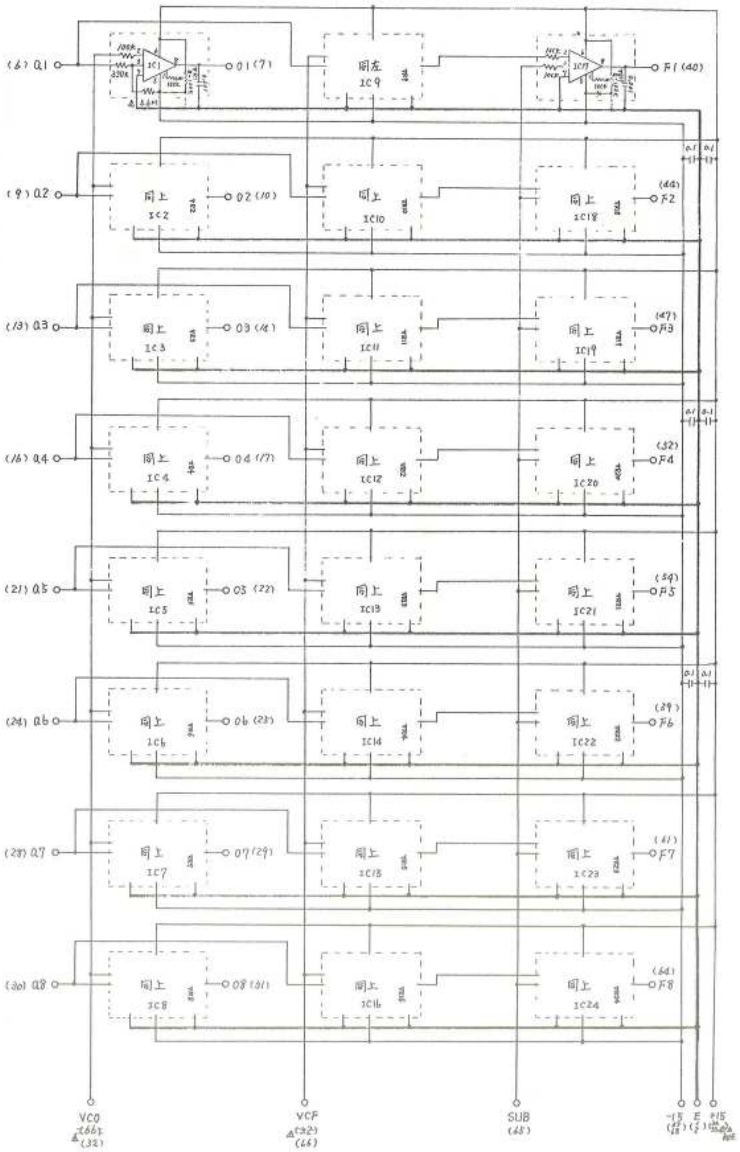


TRG 1·2 Circuit Board & Wiring

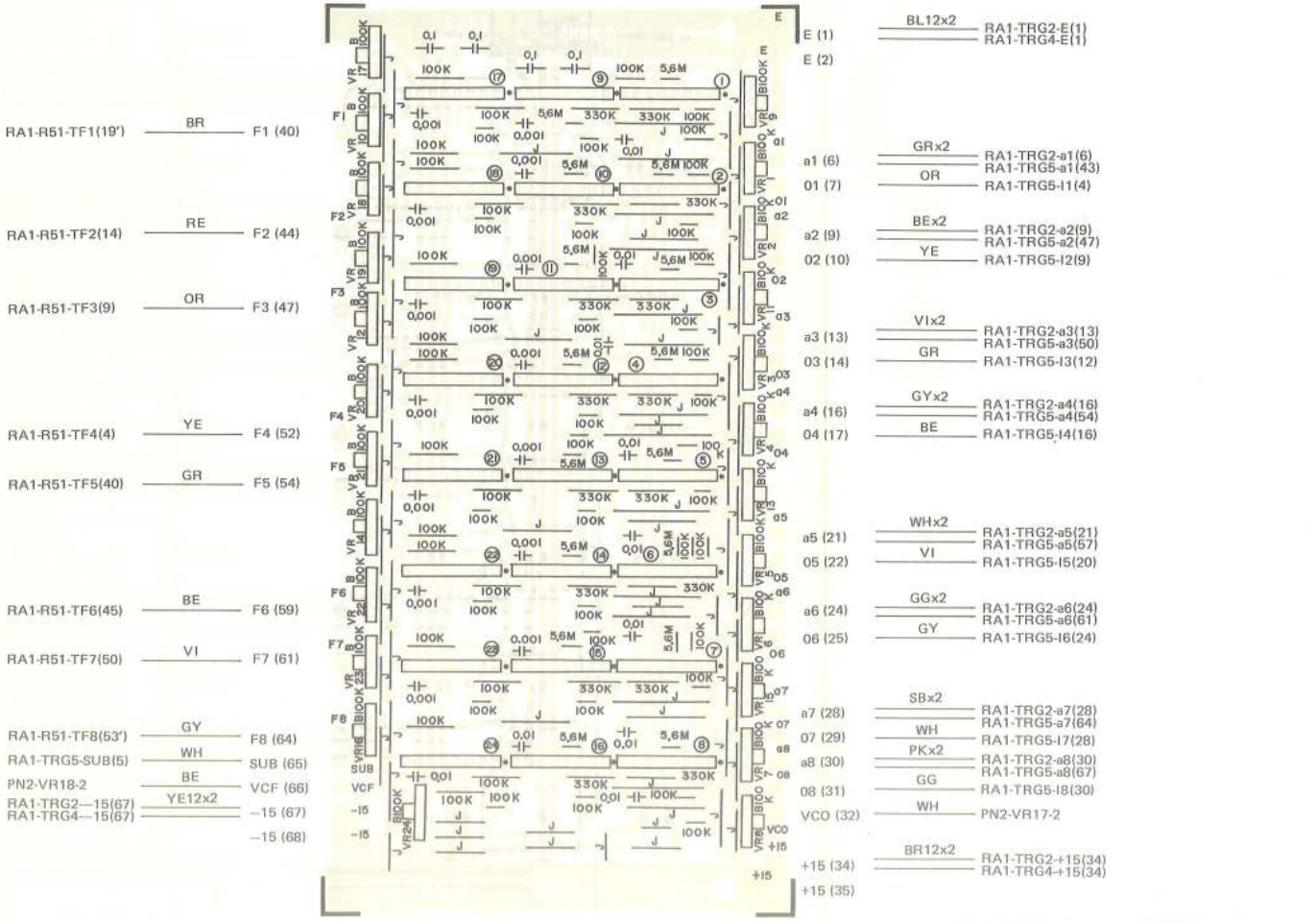


Note) 1. Print Board : LC21072
2. IC 1 ~ 24 : IG00151
3. Volume VR1~ 24 : 3 terminals (V10K8-1-2)

TRG 3 Circuit Diagram

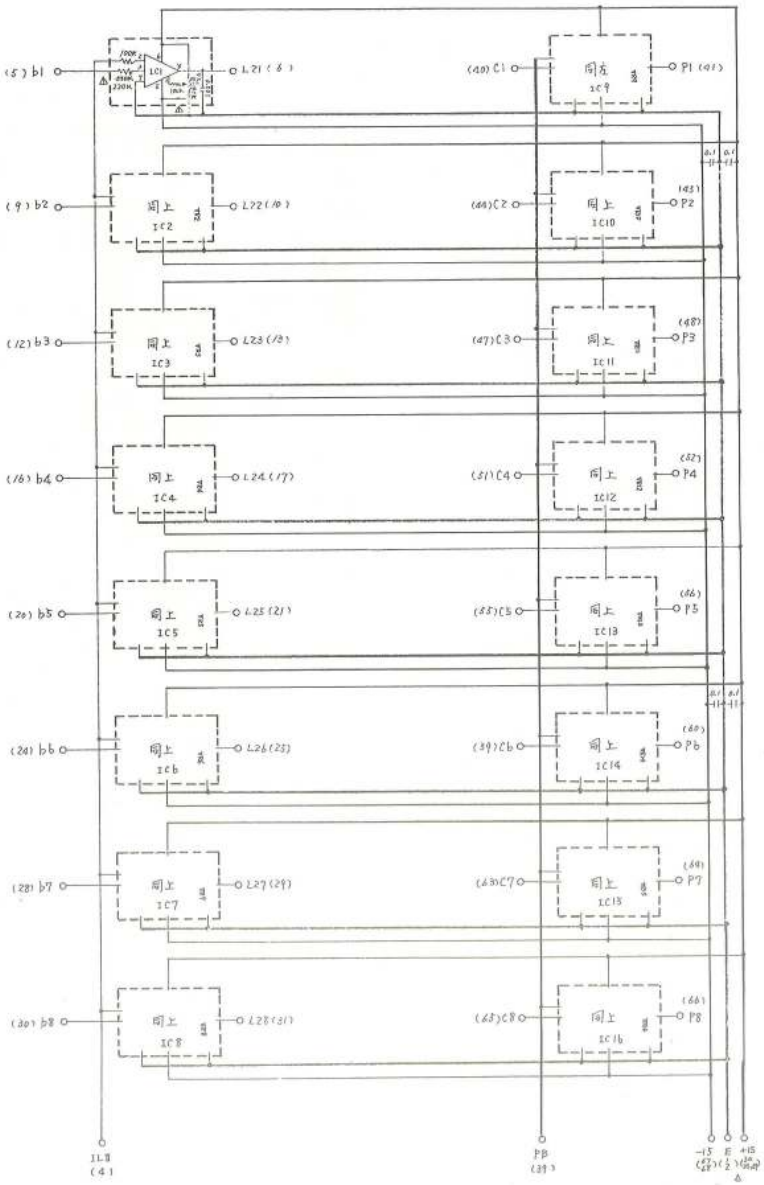


TRG 3 Circuit Board & Wiring

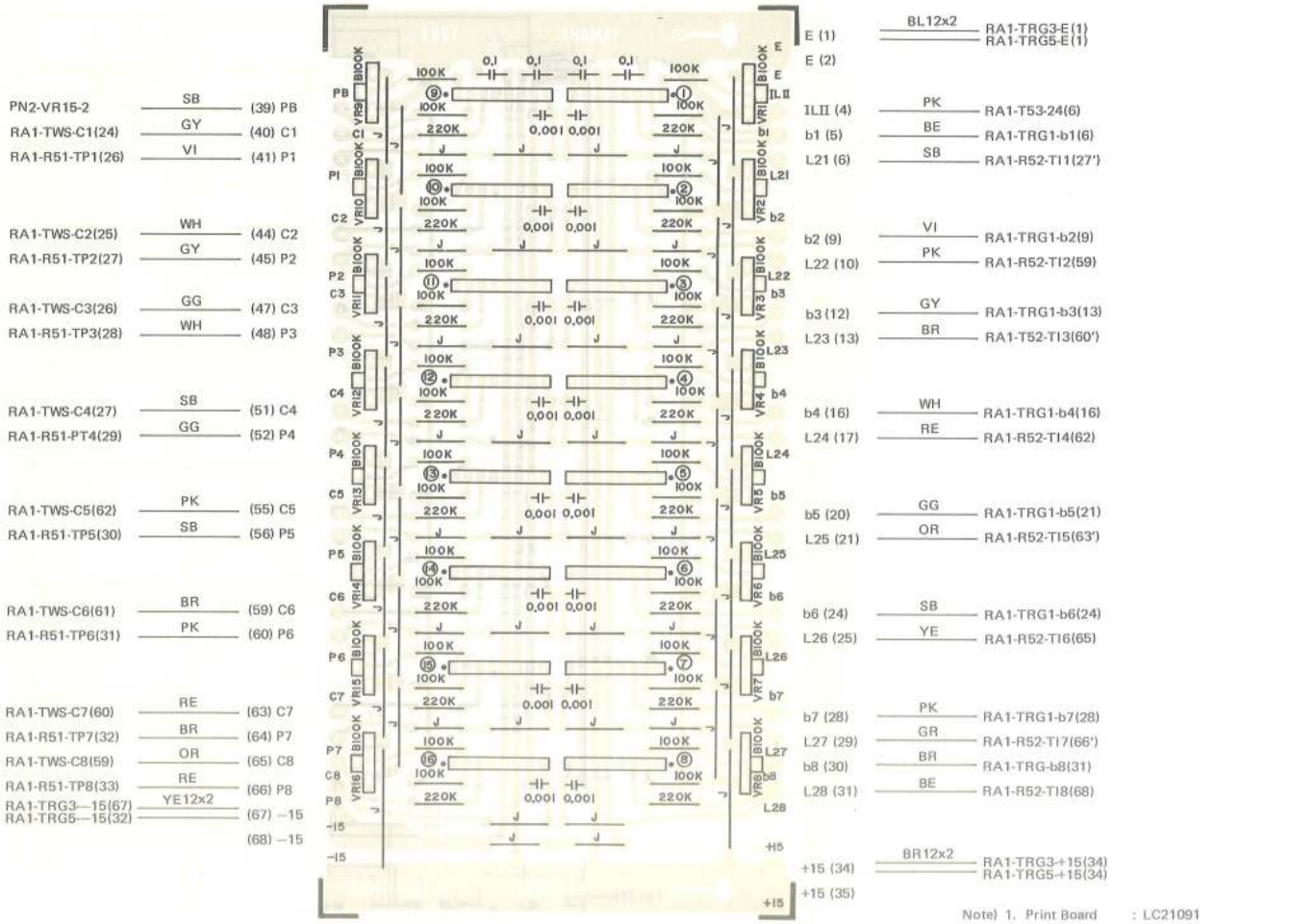


Note) 1. Print Board : LC21082
2. IC 1 ~ 24 : IG00151
3. Volume VR1 ~ 24 : 3 terminals (V10K8-1-2)

TRG 4 Circuit Diagram

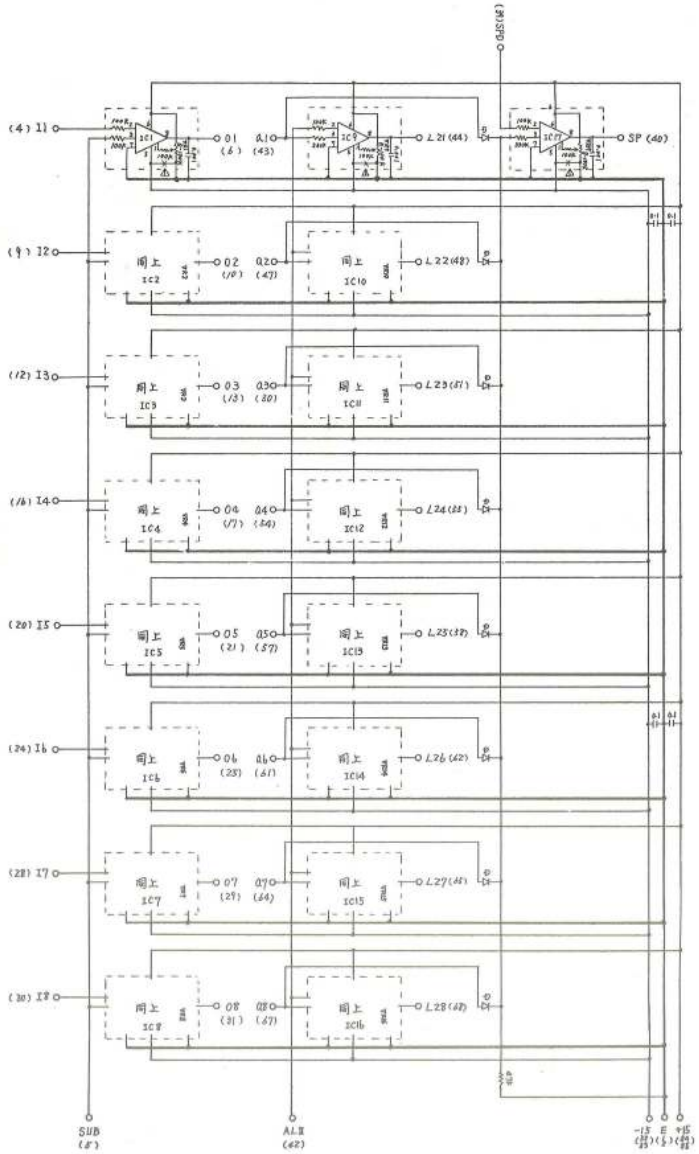


TRG 4 Circuit Board & Wiring

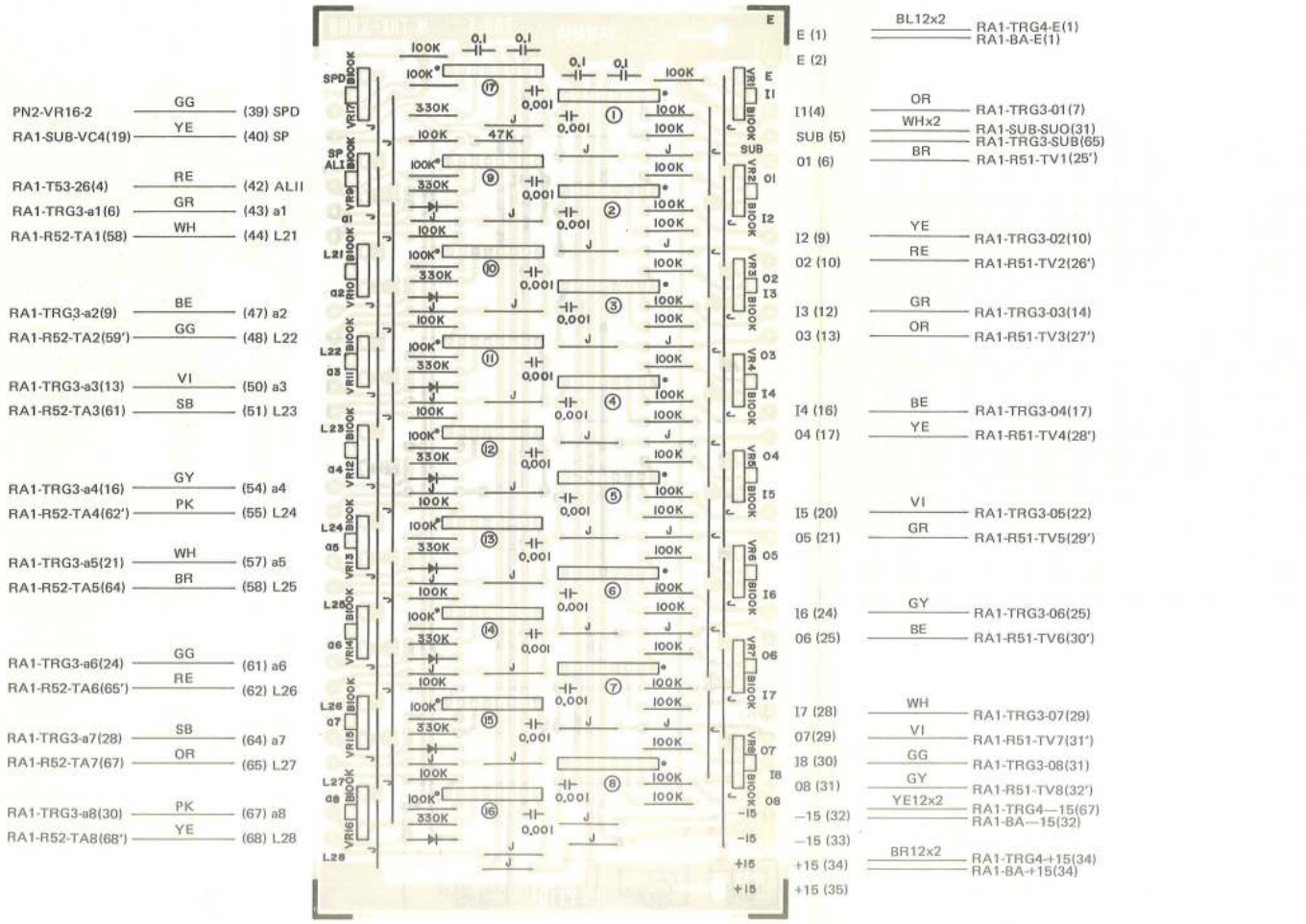


Note) 1. Print Board : LC21091
2. IC 1 ~ 16 : IG00151
3. Volume VR1 ~ 16 : 3 ternimals (V10K8-1-2)

TRG 5 Circuit Diagram

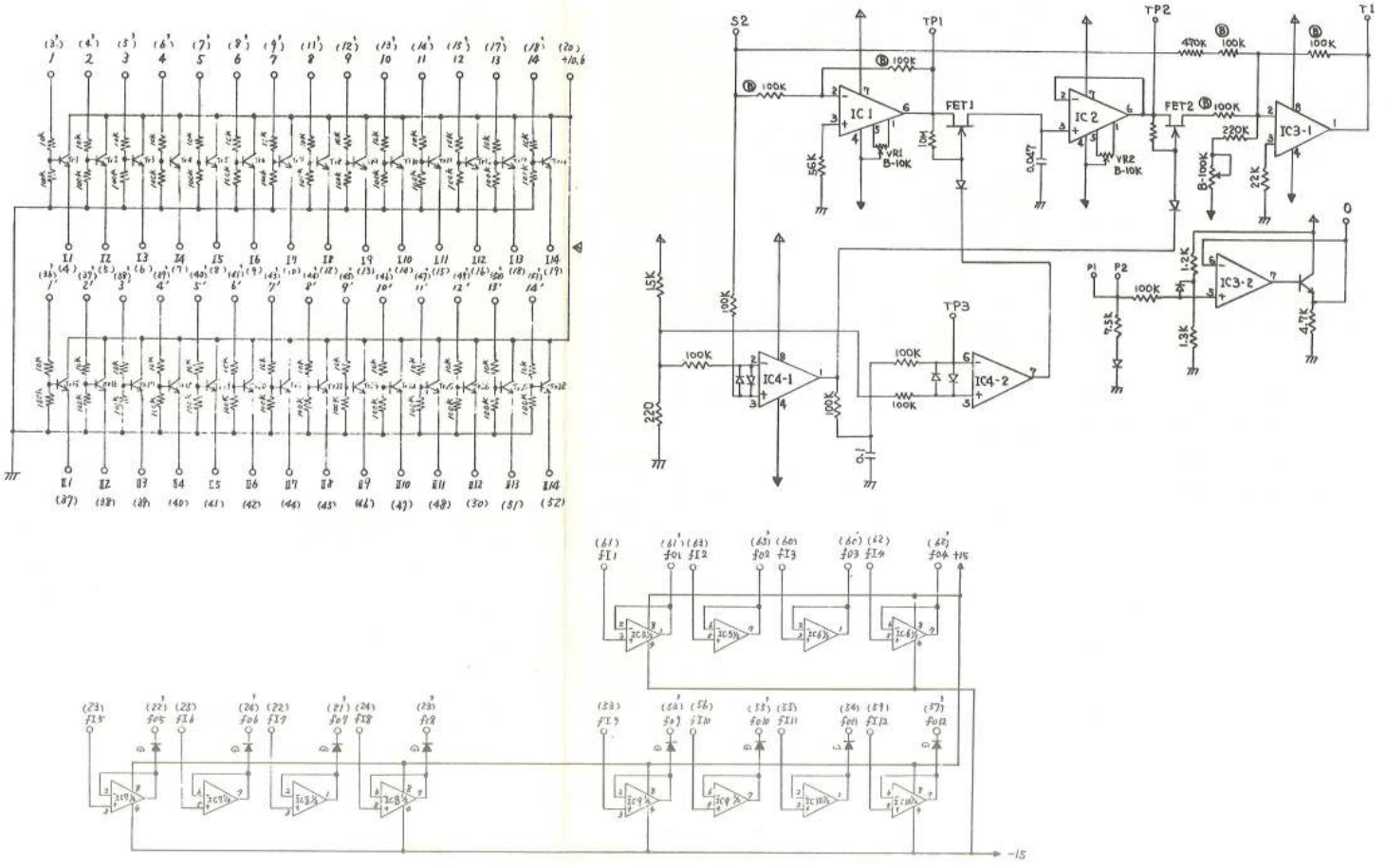


TRG 5 Circuit Board & Wiring



Note) 1. Print Board : LC21101
2. IC 1 ～ 17 : IG00151
3. Volume VR1 ～ 17 : 3 terminals (V10K8-1-2)
4. Diode : IS1555

BA Circuit Diagram



BA Circuit Board & Wiring

PN2-TS-1' — RE — (36') 1'
PN2-TS-2' — YE — (37') 2'
PN2-TS-3' — BE — (38') 3'
PN2-TS-4' — GY — (39') 4'
PN2-TS-5' — GG — (40') 5'
PN2-TS-6' — PK — (41') 6'

PN2-TS-7' — RE — (43') 7'
PN2-TS-8' — YE — (44') 8'
PN2-TS-9' — BE — (45') 9'
PN2-TS-10' — GY — (46') 10'
PN2-TS-11' — GG — (47') 11'

PN2-TS-12' — PK — (49') 12'
PN2-TS-13' — RE — (50') 13'
PN2-TS-14' — YE — (51') 14'

RA1-BA-f05(22') — OR — (53') f09

RA1-BA-f06(24') — YE — (55') fo10

RA1-BA-fo8(23') — RE — (57') fo12

RA1-T53-7'(26') — OR — (60') f03
RA1-T51-7'(26') — VI — (61') fo1
RA1-T53-9'(23') — BR — (62') fo4
RA1-T51-9'(23') — WH — (63') fo2

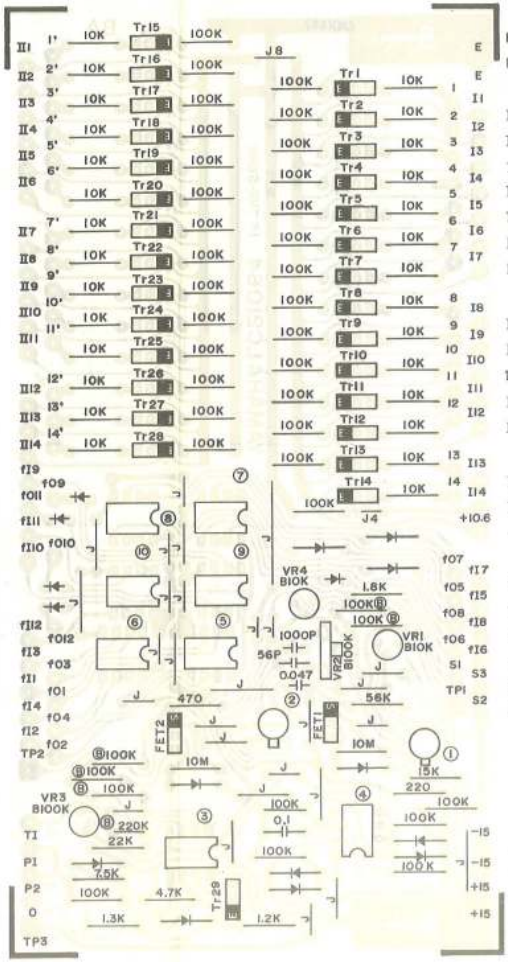
RA1-T53-A(91) — BR — (36) E / (37) I11
RA1-T53-B(92) — RE — (38) I12
RA1-T53-C(93) — OR — (39) I13
RA1-T53-D(94) — YE — (40) I14
RA1-T53-E(95) — GR — (41) I15
RA1-T53-F(96) — BE — (42) I16

RA1-T54-A(91) — BR — (44) I17
RA1-T54-B(92) — RE — (45) I18
RA1-T54-C(93) — OR — (46) I19
RA1-T54-D(94) — YE — (47) I110
RA1-T54-E(95) — GR — (48) I111

PN1-MBK-M2-VR24-3 — BE — (50) I112
PN1-MBK-M4-VR24-3 — VI — (51) I113
PN1-VR24'-3 — YE — (52) I114
PN1-MBK-M3-VR5-2 — VI — (53) fI9
RA1-BA-fo7(21') — BR — (54) fo11
PN1-MBK-M3-VR5-2 — GG — (55) fI11
PN1-MBK-M3-VR7-2 — WH — (56) fI10

PN1-MBK-M4-VR7-2 — SB — (59) fI12
PN1-VR5-2 — GG — (60) fI3
PN1-VR5-2 — VI — (61) fI1
PN1-VR7'-2 — SB — (62) fI4
PN1-VR7-2 — WH — (63) fI2
(64) TP2
(65) TP4

PN2-VRIA-3 — OR — (66) TI
PN2-VRIB-1 — BR — (67) PI
PN2-VRIB-2 — RE — (68) P2
RA2-KAS-TI(67) — GY — (69) 0
(70) TP3



E (1) — BL12x2 — RA1-TRG5-E(1) / PA1-SUB-E(1)
E (2)

I1 (4) — BR — RA1-T51-A(91)
I2 (5) — RE — RA1-T51-B(92)
I3 (6) — OR — RA1-T51-C(93)
I4 (7) — YE — RA1-T51-D(94)
I5 (8) — GR — RA1-T51-E(95)
I6 (9) — BE — RA1-T51-F(96)
I7 (10) — BR — RA1-T52-A(91)

I8 (12) — RE — RA1-T52-B(92)
I9 (13) — OR — RA1-T52-C(93)
I10 (14) — YE — RA1-T52-D(94)
I11 (15) — GR — RA1-T52-E(95)
I12 (16) — WH — PN1-MBK-M1-VR24-3

I13 (18) — GY — PN1-MBK-M3-VR24-3
I14 (19) — BE — PN1-VR24-3
+10.6 (20) — OR12x2 — PN3-VR1-1 / PN2-VR18-3

fI7 (22) — GG — PN1-MBK-M2-VR5-2
fI5 (23) — VI — PN1-MBK-M1-VR5-2
fI8 (24) — SB — PN1-MBK-M2-VR7-2
fI6 (25) — WH — PN1-MBK-M1-VR7-2
S3 (26) — OR — PN2-SK-3
S2 (27) — RE — PN2-SK-2

−15 (32) — YE12x2 — RA1-TRG5—15(32) / RA1-SUB—15(5)
−15 (33)
+15 (34) — BR12x2 — RA1-TRG5-+15(34) / RA1-SUB-+15(34)
+15 (35)

1 (3') — BR — PN2-TS-1
2 (4') — OR — PN2-TS-2
3 (5') — GR — PN2-TS-3
4 (6') — VI — PN2-TS-4
5 (7') — WH — PN2-TS-5
6 (8') — SB — PN2-TS-6
7 (9') — BR — PN2-TS-7

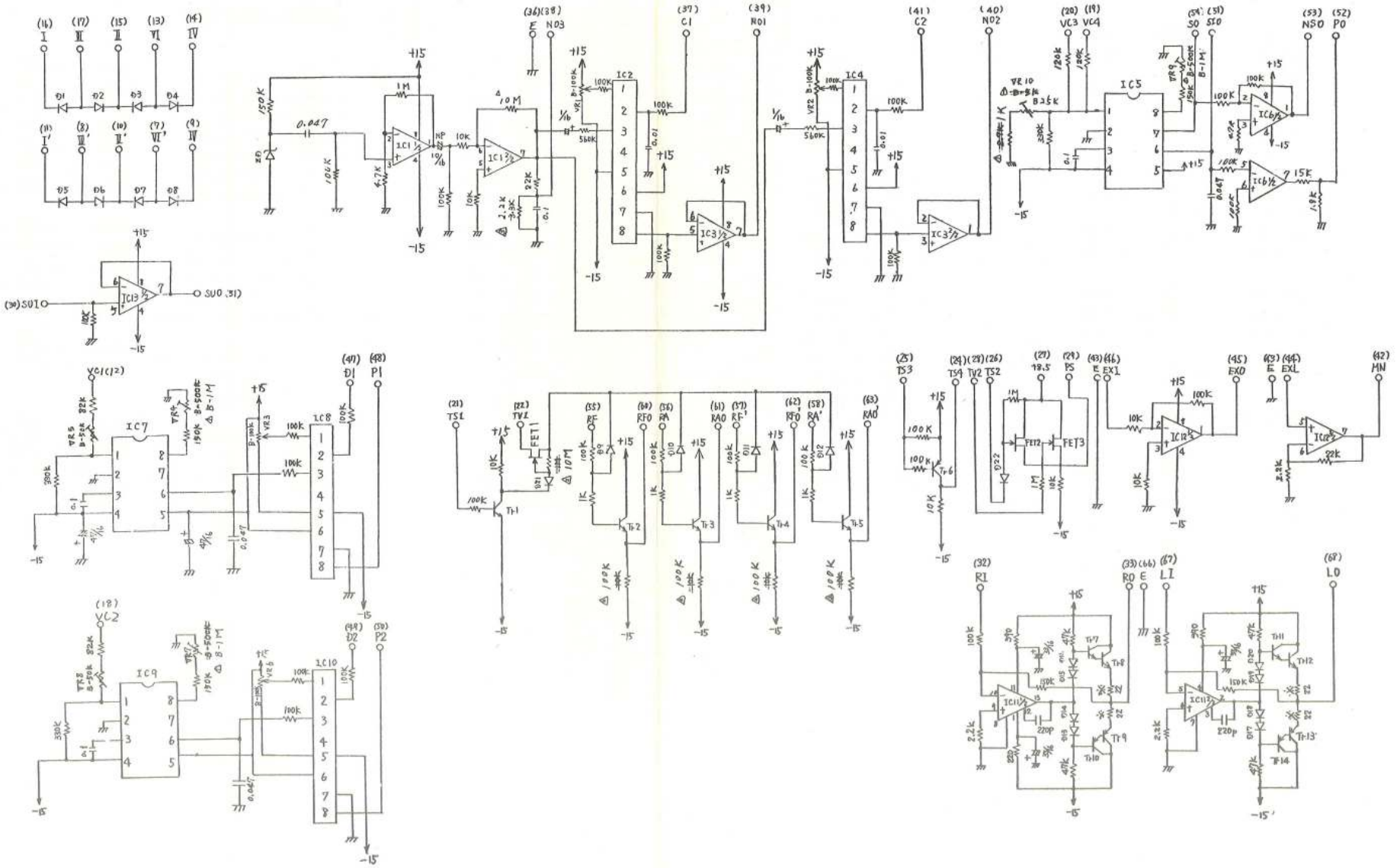
8 (11') — OR — PN2-TS-8
9 (12') — GR — PN2-TS-9
10 (13') — VI — PN2-TS-10
11 (14') — WH — PN2-TS-11
12 (15') — SB — PN2-TS-12

13 (17') — BR — PN2-TS-13
14 (18') — OR — PN2-TS-14

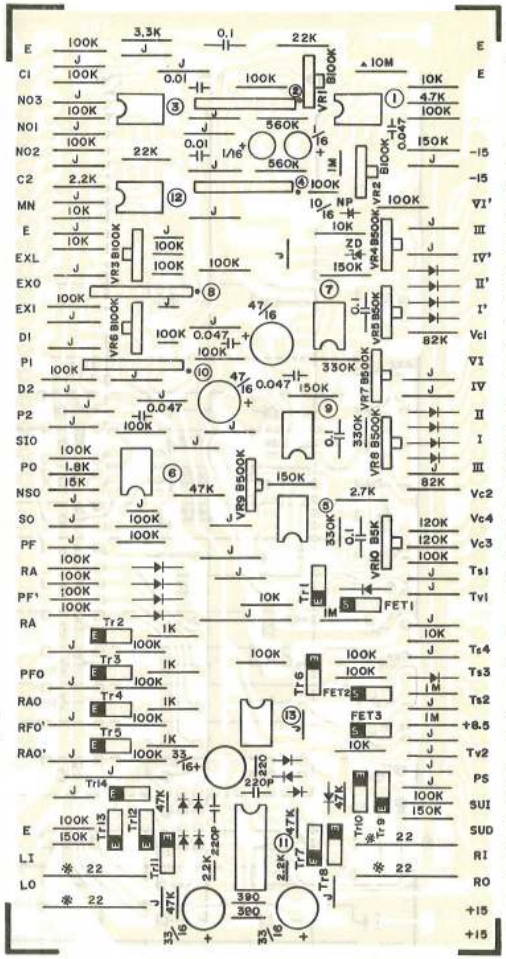
f07 (21') — BRx2 — RA1-BA-fo11(54)
f05 (22') — ORx2 — RA1-T53-7(27) / RA1-BA-f09(53)
f08 (23') — REx2 — RA1-T51-7(27) / RA1-BA-fo12(57')
f06 (24') — YEx2 — RA1-T53-9(24) / RA1-BA-fo10(55') / RA1-T51-9(24)
S1 (25') — BR — PN2-SK-1
TP1 (26')

Note) 1. Print Board : LC21063
2. Transistor
FET 1, 2 : 2SK30
Tr 1 ~ 29 : 2SC458
3. Diode : IS1555
4. Ⓑ Mark : Metal Film Resistor (0.1%)
5. Volume
VR1, 3, 4 : 3321H
VR2 : 3 terminals (V10K8-1-2)
6. IC
IC 1 : TA7504
IC 2 : CA3140T (RCA)
IC 3 ~ 10 : NJM4558

SUB Circuit Diagram



SUB Circuit Board & Wiring



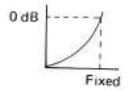
RA1-T51-6(28')	BE	(36) E
	PK	(37) C1
PN2-SW1-M5		(38) No3
RA1-M11-NI(43)	GG	(39) No1
RA1-M21-NI(43)	SB	(40) No2
RA1-T53-6(28)	BE	(41) C2
PN2-SW1-M6	GR	(42) MN
		(43) E
PN5-EXT-2	GG	(44) EXL
PN5-EXT-VR1-3	SB	(45) EXO
PN5-EXT-J6	BR	(46) EXI
RA1-T51-2(32)	RE	(47) D1
RA1-M11-PWM(41)	YE	(48) P1
RA1-T53-2(32)	RE	(49) D2
RA1-M21-PWM(41)	YE	(50) P2
PN2-SW1-M1	VI	(51) SI0
PN2-SW1-M4	WH	(52) P0
PN2-SW1-M3	SB	(53) NSO
PN2-SW1-M2	GY	(54) SO
RA1-T51-15(17)	OR	(55) RF
RA1-T51-21(10)	WH	(56) RA
RA1-T53-15(17)	OR	(57) RF'
RA1-T53-21(10)	WH	(58) RA'
RA1-M11-RF(60)	OR	(60) RFO
RA1-M11-RA(65)	WH	(61) RAO
RA1-M21-RP(60)	OR	(62) RFO'
RA1-M21-RA(65)	WH	(63) RAO'
EJ-1	BL12	(66) E
RA1-PRA-HL(40)	YE	(67) LI
EJ-2	WH12	(68) LO

E (1)	BL12x2	RA1-BA-E(1) RA1-T51-E(1)
E (2)		
−15 (5)	YE12x2	RA1-BA—15(32) RA1-M11—15(38)
−15 (6)		
VI' (7)	GR	PN2-SW3-M5
III' (8)	OR	PN2-SW3-M3
IV' (9)	YE	RA1-M21-IV(17)
II' (10)	RE	RA1-M21-II(16)
I' (11)	BR	RA1-M21-I(15)
VC1 (12)	BR	RA1-T51-1(33)
VI (13)	GR	PN2-SW2-M5
IV (14)	YE	RA1-M11-IV(17)
II (15)	RE	RA1-M11-II(16)
I (16)	BR	RA1-M11-I(15)
III (17)	OR	RN2-SW2-M3
VC2 (18)	BR	RA1-T53-1(33)
VC4 (19)	YE	RA1-TRG5-SP(40)
VC3 (20)	GG	PN2-VR8-2
TS1 (21)	YE	PN3-SW1-T1
TV1 (22)	GR	PN3-VR1-2
TS4 (24)	WH	PN3-SW2-M1
TS3 (25)	GY	PN5-J5-2
TS2 (26)	VI	PN3-SW2-T1
+8.5 (27)	PK12	RA1-KBC2-+8.5(5)
TV2 (28)	BE	PN3-VR2-2
PS (29)	SB	RA2-KAS-PS(93)
SUI (30)	GG	PN2-SW1-T
SUO (31)	WHx2	RA1-TRG5-SUB(5) PN2-VR11-3
RI (32)	GR	RA1-PRA—HR(48)
RO (33)	GY12	EJ-3
+15 (34)	BR12x2	RA1-BA-+15(34) RA1-M11-+15(34)
+15 (35)		

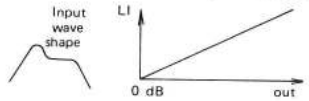
Note) 1. Print Board : LC20983
2. Transistor
Tr6, 10, 14 : 2SA561
Tr1 ~ 5, 7, 11 : 2SC828
Tr8, 12 : 2SD234
Tr9, 13 : 2SA490
FET1, 2, 3 : 2SK30
3. IC
IC 2, 4, 8, 10 : IG00151
IC 5, 7, 9 : IG00150
IC 11 : HA1452
IC 1, 3, 6, 12, 13 : NJM4558
4. * Mark : 2W Metal Oxide Resistor
5. Diode 1 ~ 22 : IS1555
6. ZD : IS1715P
7. Volume
VR 4, 5, 7 ~ 10 : 2 terminals (V10K4A-5-2)
VR 1 ~ 3, 6 : 3 terminals (V10K8-1-2)

VCA IC (IG00151)

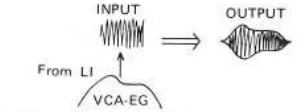
1. EI ..... Input voltage for level control.
Input of the control voltage is provided for changing the level expotenentially.



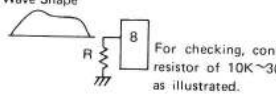
2. LI ..... Input of level control voltage.
Input of the control voltage is provided for linear change of the level.



3. +IN ..... Input
Input of the level modulated signal is provided.



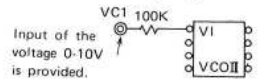
4. −IN ..... Negative feed back.
Normally unused.
5. Vee ..... −15V input power source.
6. Vcc ..... +15V input power source.
7. GND ..... Earth
8. OUT ..... Output
Output of the following wave shape is produced.



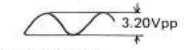
For checking, connect the resistor of 10K~30K ohm as illustrated.

VCOII IC (IG00150)

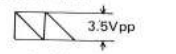
1. VI ..... Input of the control voltage.
The frequency is variable in accordance with the voltage supplied.



2. GND ..... Earth
3. C ..... Capacitor for determination of the frequency.
4. Vee ..... −15V input power source.
5. Vcc ..... +15V input power source.
6. SIO ..... Output of sine wave.

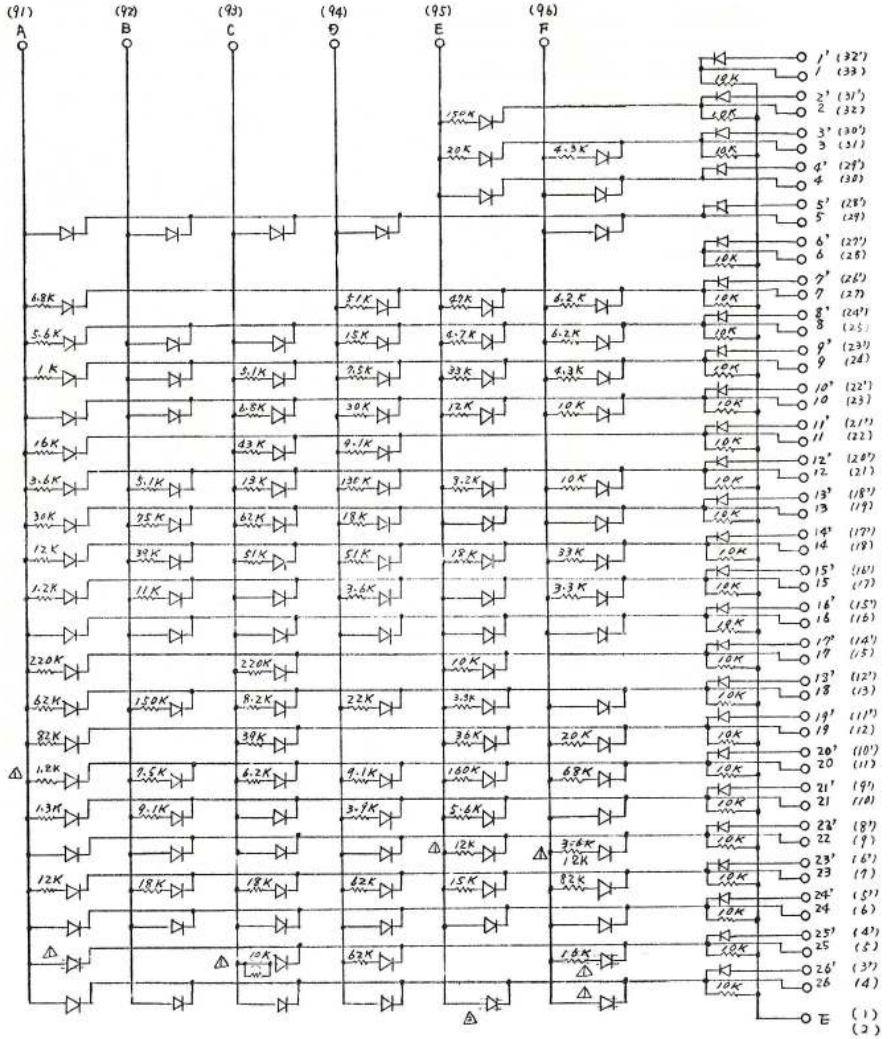


7. SO ..... Output of sawtooth wave

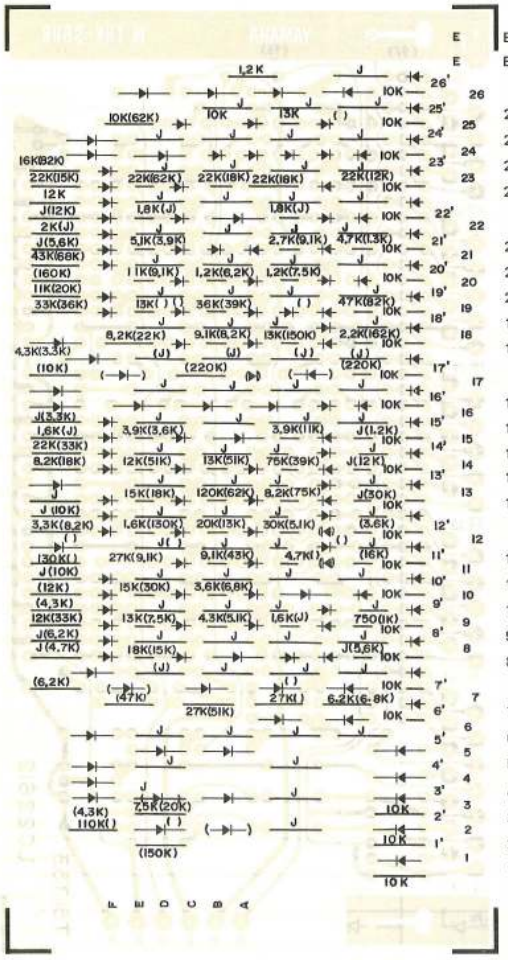


8. Iadj ..... Setting for standard electric current.
The standard electric current is set so as to be the output 200Hz when VC1 is 10V and VC2 is zero volt.

T51 Circuit Diagram



T51 Circuit Board & Wiring



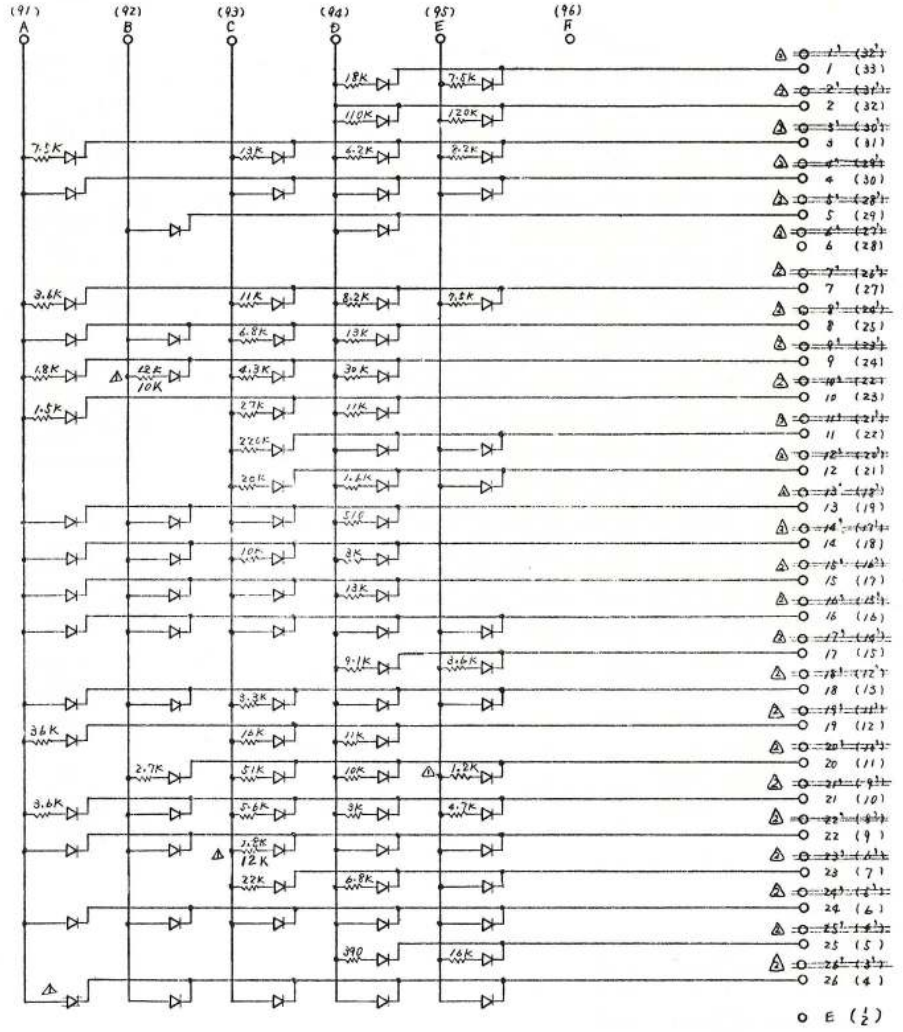
E (1)	BL12x2 — RA1-SUB-E(1) / RA1-T52-E(1)
E (2)	
26 (4)	REx2 — RA1-TRG2-ALI(65) / RA1-T52-26(4)
25 (5)	BRx2 — RA1-TRG2-ABI(33) / RA1-T52-25(5)
24 (6)	PKx2 — RA1-TRG1-ILI(65) / RA1-T52-24(6)
23 (7)	SBx2 — RA1-TRG1-IBI(33) / RA1-T52-23(7)
22 (9)	GG — RA1-T52-22(9)
21 (10)	WHx2 — RA1-SUB-RA(56) / RA1-T52-21(10)
20 (11)	GY — RA1-T52-20(11)
19 (12)	VI — RA1-T52-19(12)
18 (13)	BE — RA1-T52-18(13)
17 (15)	GR — RA1-T52-17(15)
16 (16)	YE — RA1-T52-16(16)
15(17)	ORx2 — RA1-SUB-RF(55) / RA1-T52-15(17)
14 (18)	RE — RA1-T52-14(18)
13 (19)	BR — RA1-T52-13(19)
12 (21)	PK — RA1-T52-12(21)
11(22)	SB — RA1-T52-11(22)
10 (23)	GG — RA1-T52-10(23)
9 (24)	YEx2 — RA1-BA-f06(24') / RA1-T52-9(24)
8 (25)	GY — RA1-T52-8(25)
7 (27)	ORx2 — RA1-BA-f05(22') / RA1-T52-7(27)
6 (28)	BEx2 — RA1-SUB-C1(37) / RA1-T52-6(28)
5 (29)	GR — RA1-T52-5(29)
4 (30)	YE — RA1-T52-4(30)
3 (31)	OR — RA1-T52-3(31)
2 (32)	REx2 — RA1-SUB-D1(47) / RA1-T52-2(32)
1 (33)	BRx2 — RA1-SUB-VC1(12) / RA1-T52-1(33)

26' (3')	RE — PN1-VR24-2
25' (4')	BR — PN1-VR23-2
24' (5')	PK — PN1-VR22-2
23' (6')	SB — PN1-VR21-2
22' (8')	GG — PN1-VR20-2
21' (9')	WH — PN1-VR19-2
20' (10')	GY — PN1-VR18-2
21' (11')	VI — PN1-VR17-2
20' (12')	BE — PN1-VR16-2
17' (14')	GR — PN1-VR15-2
16' (15')	YE — PN1-VR14-2
15' (16')	OR — PN1-VR13-2
14' (17')	RE — PN1-VR12-2
13' (18')	BR — PN1-VR11-2
12' (20')	PK — PN1-VR10-2
11' (21')	SB — PN1-VR9-2
10' (22')	GG — PN1-VR8-2
9' (23')	WH — PN1-BA-f02(63')
8' (24')	GY — PN1-VR6-2
7' (26')	VI — RA1-BA-f01(61')
6' (27')	BE — PN1-VR4-2
5' (28')	GR — PN1-SW2-T
4' (29')	YE — PN1-SW1-T
3' (30')	OR — PN1-VR3-2
2' (31')	RE — PN1-VR2-2
1' (32')	BR — PN1-VR1-2

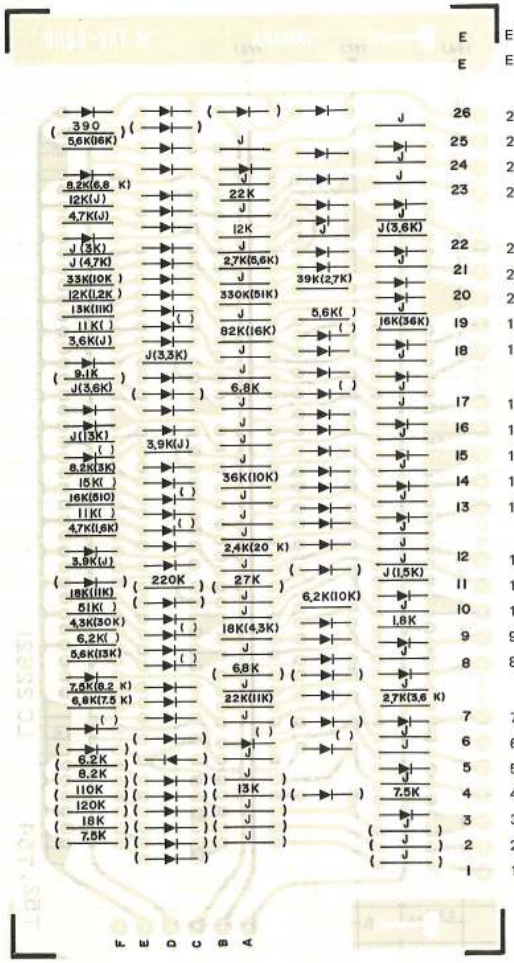
RA1-BA-I6(9) — BE	(96) F
RA1-BA-I5(8) — GR	(95) E
RA1-BA-I4(7) — YE	(94) D
RA1-BA-I3(6) — OR	(93) C
RA1-BA-I2(5) — RE	(92) B
RA1-BA-I1(4) — BR	(91) A

Note) 1. Print Board : LC22612
2. Diode : IS1555

T52 Circuit Diagram



T52 Circuit Board & Wiring

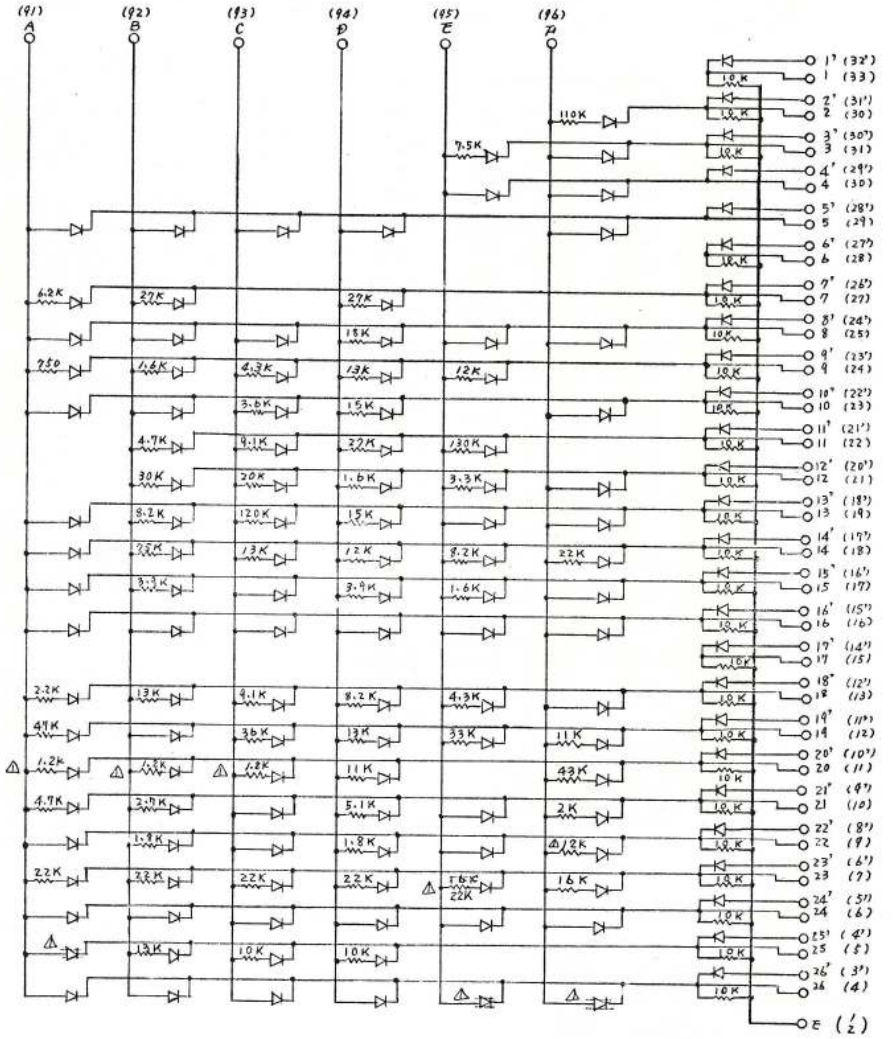


E	E (1)	BL12x2	RA1-T51-E(1)
			RA1-M11-E(1)
E	E (2)		
26	26 (4)	REx2	RA1-T51-26(4)
			PN1-MBK-M3-VR24
25	25 (5)	BRx2	RA1-T51-25(5)
			PN1-MBK-M3-VR23
24	24 (6)	PKx2	RA1-T51-24(6)
			PN1-MBK-M3-VR22
23	23 (7)	SBx2	RA1-T51-23(7)
			PN1-MBK-M3-VR21
22	22 (9)	GGx2	RA1-T51-22(9)
			RA1-R51-LV(93')
21	21 (10)	WHx2	RA1-T51-21(10)
			PN1-MBK-M3-VR19-4
20	20 (11)	GYx2	RA1-T51-20(11)
			RA1-M11-SL(64)
19	19 (12)	VIx2	RA1-T51-19(12)
			RA1-M11-1A(66)
18	18 (13)	BEx2	RA1-T51-18(13)
			RA1-M11-AA(67)
17	17 (15)	GRx2	RA1-T51-17(15)
			RA1-M11-LP2(24)
16	16 (16)	YEx2	RA1-T51-16(16)
			RA1-M11-LP1(27)
15	15 (17)	ORx2	RA1-T51-15(17)
			PN1-MBK-M3-VR13-4
14	14 (18)	REx2	RA1-T51-14 (18)
			RA1-M11-DF(61)
13	13 (19)	BRx2	RA1-T51-13(19)
			RA1-M11-AF(62)
12	12 (21)	PKx2	RA1-T51-12(21)
			RA1-M11-AL(59)
11	11 (22)	SBx2	RA1-T51-11(22)
			RA1-M11-IL(58)
10	10 (23)	GGx2	RA1-T51-10(23)
			RA1-R51-QL(15')
9	9 (24)	YEx2	RA1-R51-FL(55')
			RA1-T51-9(24)
8	8 (25)	GYx2	RA1-T51-8(25)
			RA1-R51-QH(57)
7	7 (27)	ORx2	RA1-R51-FH(55)
			RA1-T51-7(27)
6	6 (28)	BEx2	RA1-T51-6(28)
			PN1-MBK-M3-VR4
5	5 (29)	GRx2	RA1-T51-5(29)
			RA1-M11-G2(44)
4	4 (30)	YEx2	RA1-T51-4(30)
			RA1-M11-G1(45)
3	3 (31)	ORx2	RA1-T51-3(31)
			RA1-M11-PW(40)
2	2 (32)	REx2	RA1-T51-2(32)
			PN1-MBK-M3-VR2
1	1 (33)	BRx2	RA1-T51-1(33)
			PN1-MBK-M3-VR1

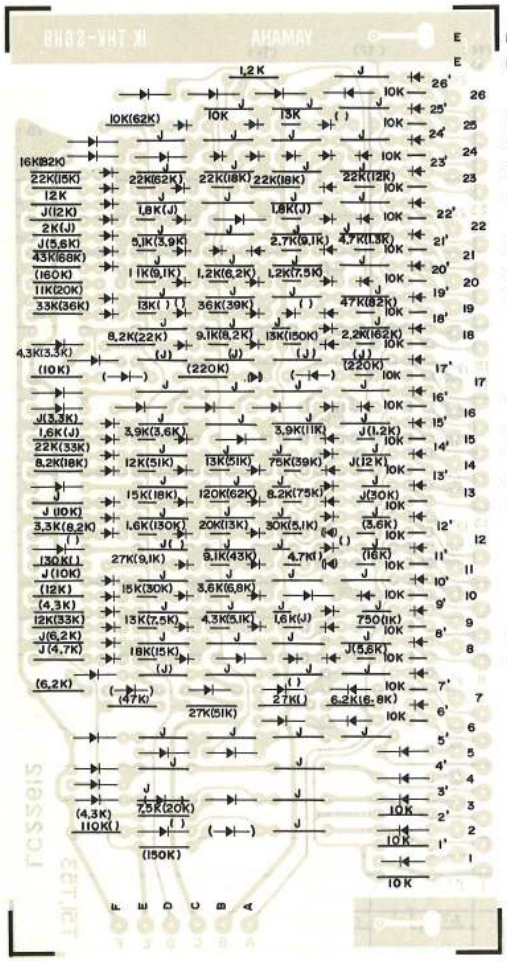
		(96) F
RA1-BA-I11(15)	GR	(95) E
RA1-BA-I10(14)	RE	(94) D
RA1-BA-I9(13)	OR	(93) C
RA1-BA-I8(12)	RE	(92) B
RA1-BA-I7(10)	BR	(91) A

Note) 1. Print Board : LC22621
2. Diode : IS1555
3. J : J10

T53 Circuit Diagram



T53 Circuit Board & Wiring



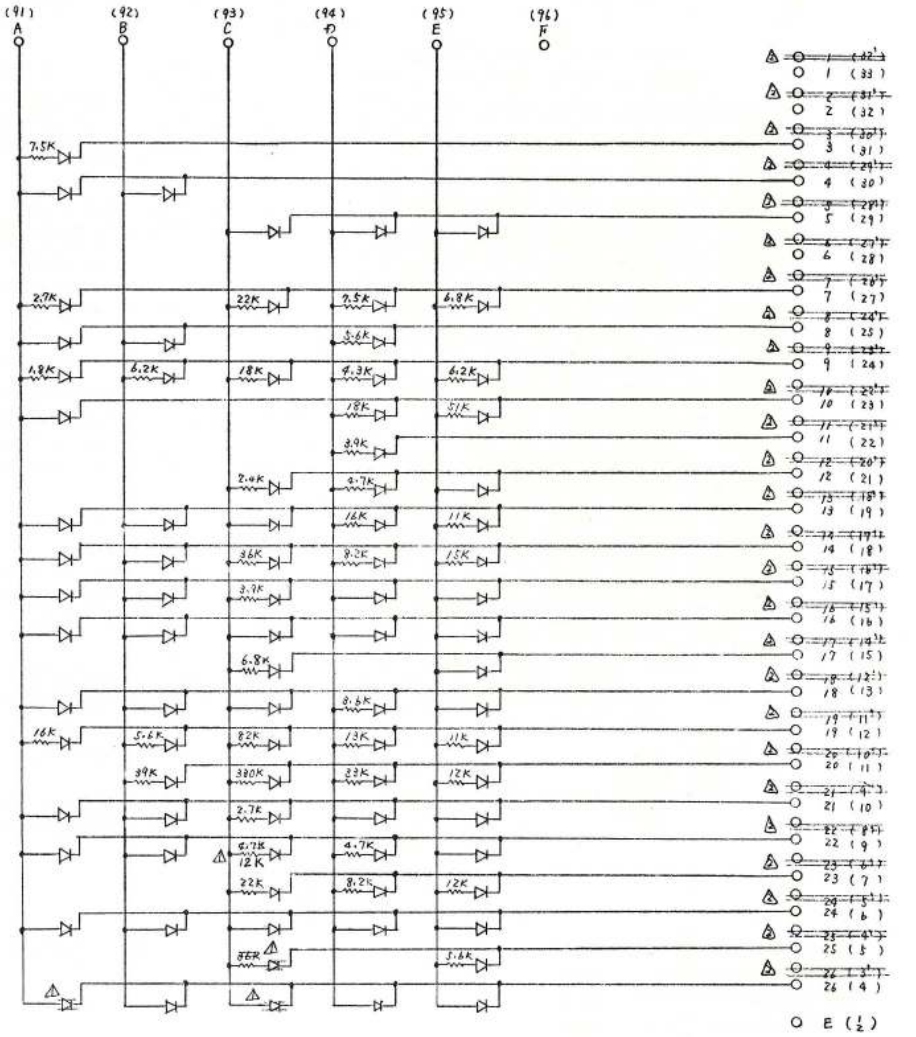
E (1)	BL12x2	RA1-M18-E(1)
		RA1-T54-E(1)
E (2)		
26 (4)	REx2	RA1-TRG5-ALII(42)
		RA1-T54-26(4)
25 (5)	BRx2	RA1-TRG2-ABII(66)
		RA1-T54-25(5)
24 (6)	PKx2	RA1-TRG4-ILII(4)
		RA1-T54-24(6)
23 (7)	SBx2	RA1-TRG1-IBII(66)
		RA1-T54-23(7)
22 (9)	GG	RA1-T54-22(9)
21 (10)	WHx2	RA1-SUB-RA'(58)
		RA1-T54-21(10)
20 (11)	GY	RA1-T54-20(11)
19 (12)	VI	RA1-T54-19(12)
18 (13)	BE	RA1-T54-18(13)
17 (15)	GR	RA1-T54-17(15)
16 (16)	YE	RA1-T54-16(16)
15 (17)	ORx2	RA1-SUB-RF'(57)
		RA1-T54-15(17)
14 (18)	RE	RA1-T54-14(18)
13 (19)	BR	RA1-T54-13(19)
12 (21)	PK	RA1-T54-12(21)
11 (22)	SB	RA1-T54-11(22)
10 (23)	GG	RA1-T54-10(23)
9 (24)	REx2	RA1-BA-f08(23')
		RA1-T54-9(24)
8 (25)	GY	RA1-T54-8(25)
7 (27)	BRx2	RA1-BA-f07(21')
		RA1-T54-7(27)
6 (28)	BEx2	RA1-SUB-C2(41)
		RA1-T54-6(28)
5 (29)	GR	RA1-T54-5(29)
4 (30)	YE	RA1-T54-4(30)
3 (31)	OR	RA1-T54-3(31)
2 (32)	REx2	RA1-SUB-D2(49)
		RA1-T54-2(32)
1 (33)	BRx2	RA1-SUB-VC2(18)
		RA1-T54-1(33)

26' (3')	RE	PN1-VR24'-2
25' (4')	BR	PN1-VR23'-2
24' (5')	PK	PN1-VR22'-2
23' (6')	SB	PN1-VR21'-2
22' (8')	GG	PN1-VR20'-2
21' (9')	WH	PN1-VR19'-2
20' (10')	GY	PN1-VR18'-2
19' (11')	VI	PN1-VR17'-2
18' (12')	BE	PN1-VR16'-2
17' (14')	GR	PN1-VR15'-2
16' (15')	YE	PN1-VR14'-2
15' (16')	OR	PN1-VR13'-2
14' (17')	RE	PN1-VR12'-2
13' (18')	BR	PN1-VR11'-2
12' (20')	PK	PN1-VR10'-2
11' (21')	SB	PN1-VR9'-2
10' (22')	GG	PN1-VR8'-2
9' (23')	BR	RA1-BA-f04(62')
8' (24')	GY	PN1-VR6'-2
7' (26')	OR	RA1-BA-f03(60')
6' (27')	BE	PN1-VR4'-2
5' (28')	GR	PN1-SW2'-T
4' (29')	YE	PN1-SW1'-T
3' (30')	OR	PN1-VR3'-2
2' (31')	RE	PN1-VR2'-2
1' (32')	BR	PN1-VR1'-2

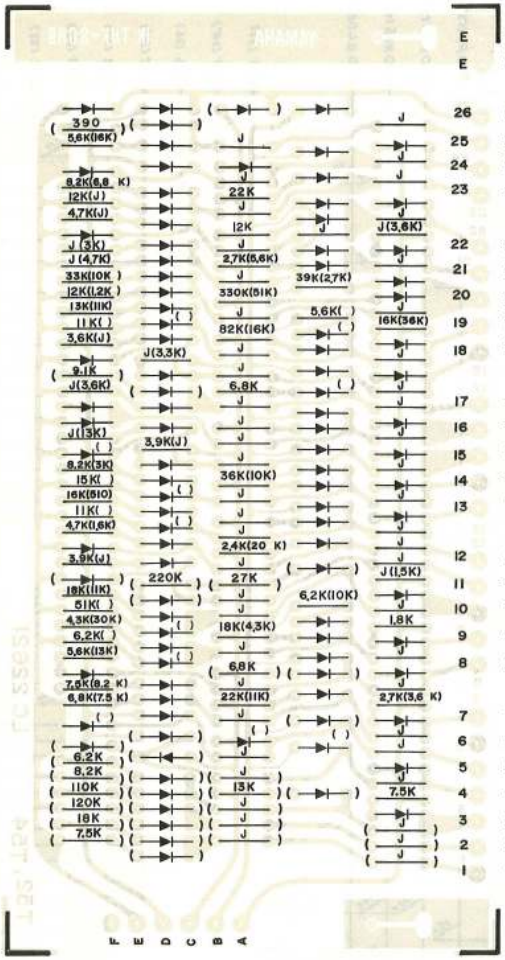
RA1-BA-I16(42)	BE	(96) F
RA1-BA-I15(41)	GR	(95) E
RA1-BA-I14(40)	YE	(94) D
RA1-BA-I13(39)	OR	(93) C
RA1-BA-I12(38)	RE	(92) B
RA1-BA-I11(37)	BR	(91) A

Note) 1. Print Board : LC22612
2. Diode : IS1555

T54 Circuit Diagram



T54 Circuit Board & Wiring

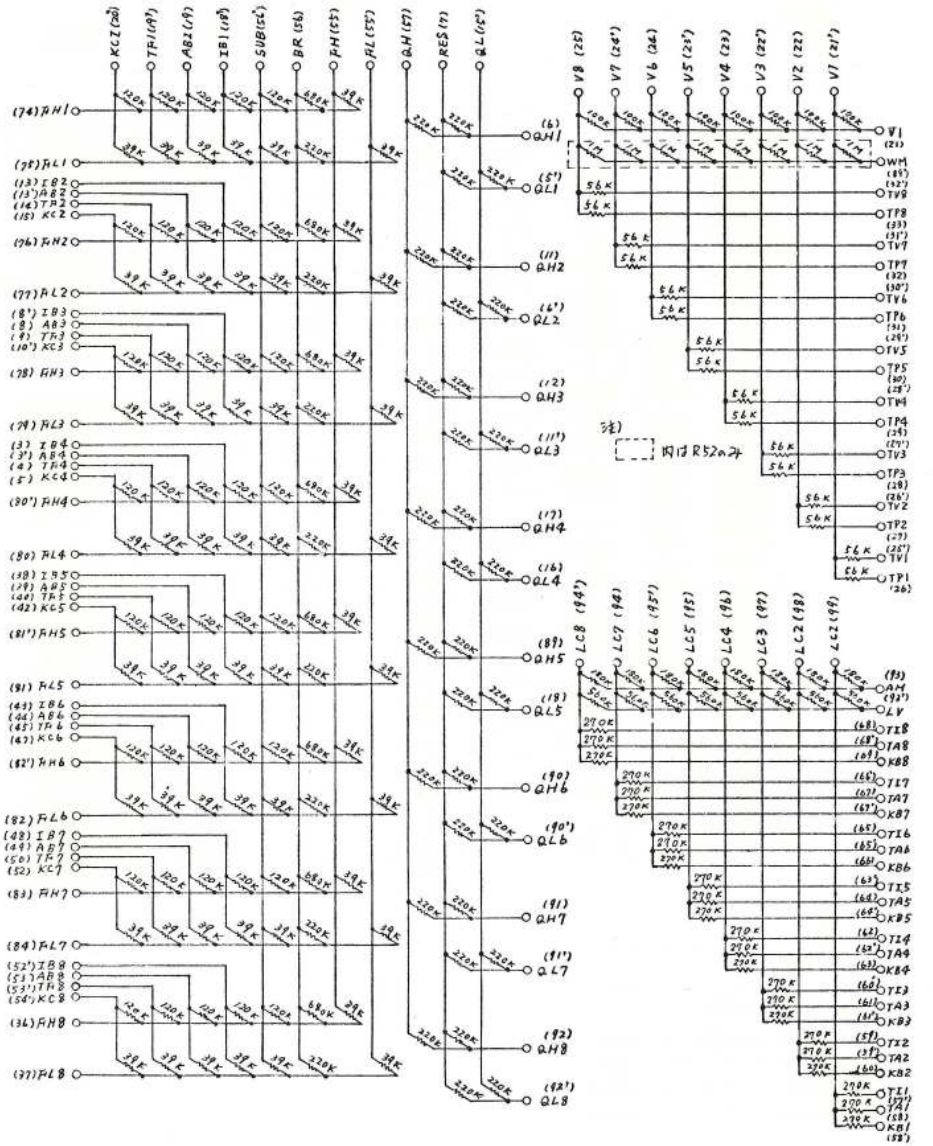


E	E (1)	BL12x2	RA1-T53-E(1) RA1-M21-E(1)
E	E (2)		
26	26 (4)	REx2	RA1-T53-26(4) PN1-MBK-M44-VR24'
25	25 (5)	BRx2	RA1-T53-25(5) PN1-MBK-M4-VR23'
24	24 (6)	PKx2	RA1-T53-24(6) PN1-MBK-M4-VR22'
23	23 (7)	SBx2	RA1-T53-23(7) PN1-MBK-M4-VR21'
22	22 (9)	GGx2	RA1-T53-22(9) RA1-R52-LV(93')
21	21 (10)	WHx2	RA1-T53-21(10) PN1-MBK-M4-VR19'-4
20	20 (11)	GYx2	RA1-T53-20(11) RA1-M21-SL(64)
19	19 (12)	VIx2	RA1-T53-19(12) RA1-M21-1A(66)
18	18 (13)	BEx2	RA1-T53-18(13) RA1-M21-AA(67)
17	17 (15)	GRx2	RA1-T53-17(15) RA1-M21-LP2(24)
16	16 (16)	YEx2	RA1-T53-16(16) RA1-M21-LP1(27)
15	15 (17)	ORx2	RA1-T53-15(17) PN1-MBK-M4-VR13'(4)
14	14 (18)	REx2	RA1-T53-14(18) RA1-M21-DF(61)
13	13 (19)	BRx2	RA1-T53-13(19) RA1-M21-AF(62)
12	12 (21)	PKx2	RA1-T53-12(21) RA1-M21-AL(59)
11	11 (22)	SBx2	RA1-T53-11(22) RA1-M21-IL(58)
10	10 (23)	GGx2	RA1-T53-10(23) RA1-R52-QL(15')
9	9 (24)	REx2	RA1-R52-FL(55') RA1-T53-9(24)
8	8 (25)	GYx2	RA1-T53-8(25) RA1-R52-QH(57)
7	7 (27)	BRx2	RA1-R52-FH(55) RA1-T53-7(27)
6	6 (28)	BEx2	RA1-T53-6(28) PN1-MBK-M4-VR4'
5	5 (29)	GRx2	RA1-T53-5(29) RA1-M21-G2(44)
4	4 (30)	YEx2	RA1-T53-4(30) RA1-M21-G1(45)
3	3 (31)	ORx2	RA1-T53-3(31) RA1-M21-PW(40)
2	2 (32)	REx2	RA1-T53-2(32) PN1-MBK-M4-VR2'
1	1 (33)	BRx2	RA1-T53-1(33) PN1-MBK-M4-VR1'

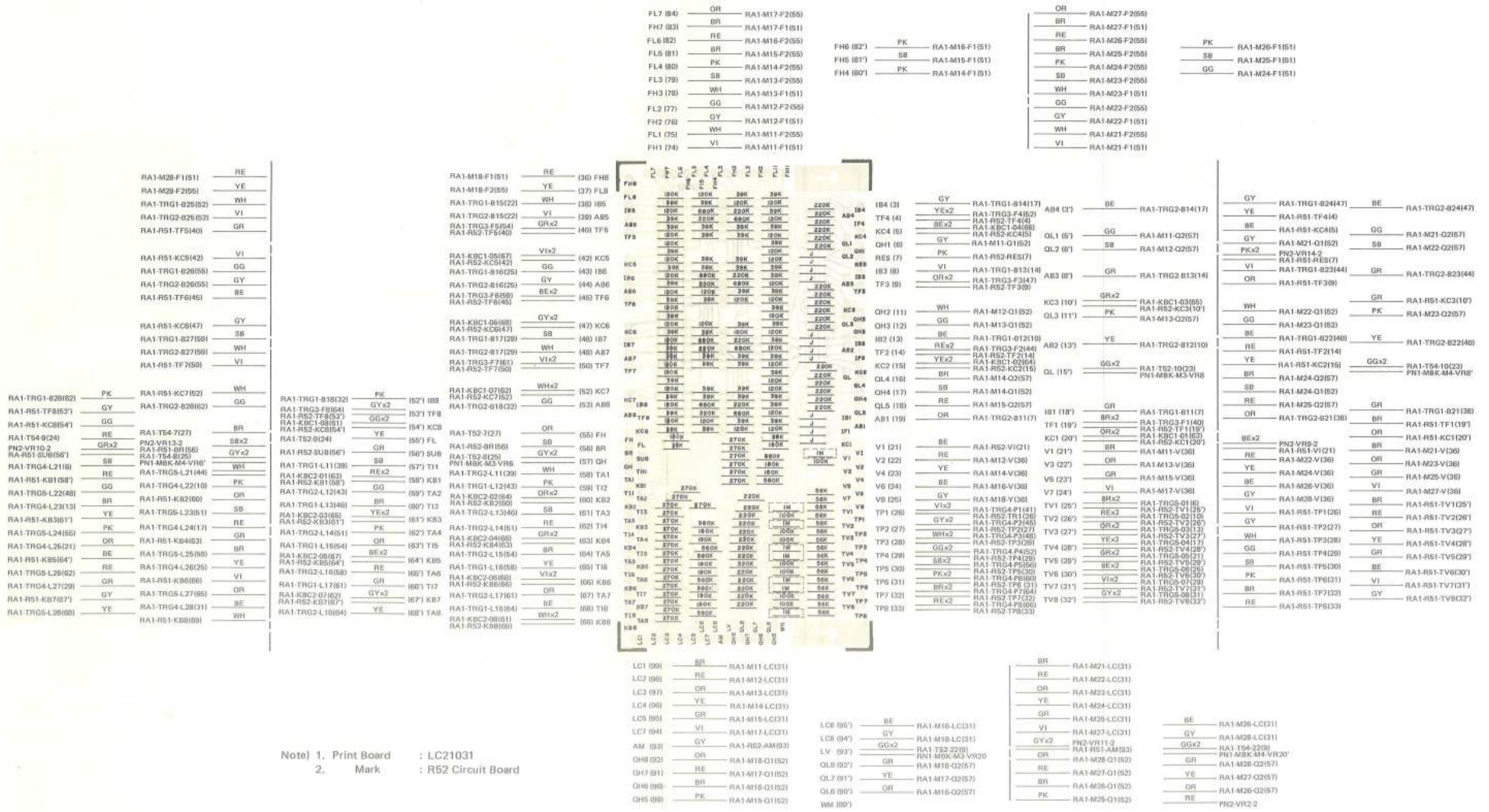
	GR	(96) F
RA1-BA-II11(48)		(95) E
RA1-BA-II10(47)	YE	(94) D
RA1-BA-II9(46)	OR	(93) C
RA1-BA-II8(45)	RE	(92) B
RA1-BA-II7(44)	BR	(91) A

Note) 1. Print Board :LC22621
2. Diode : IS1555
3. J : J10

R51-52 Circuit Diagram

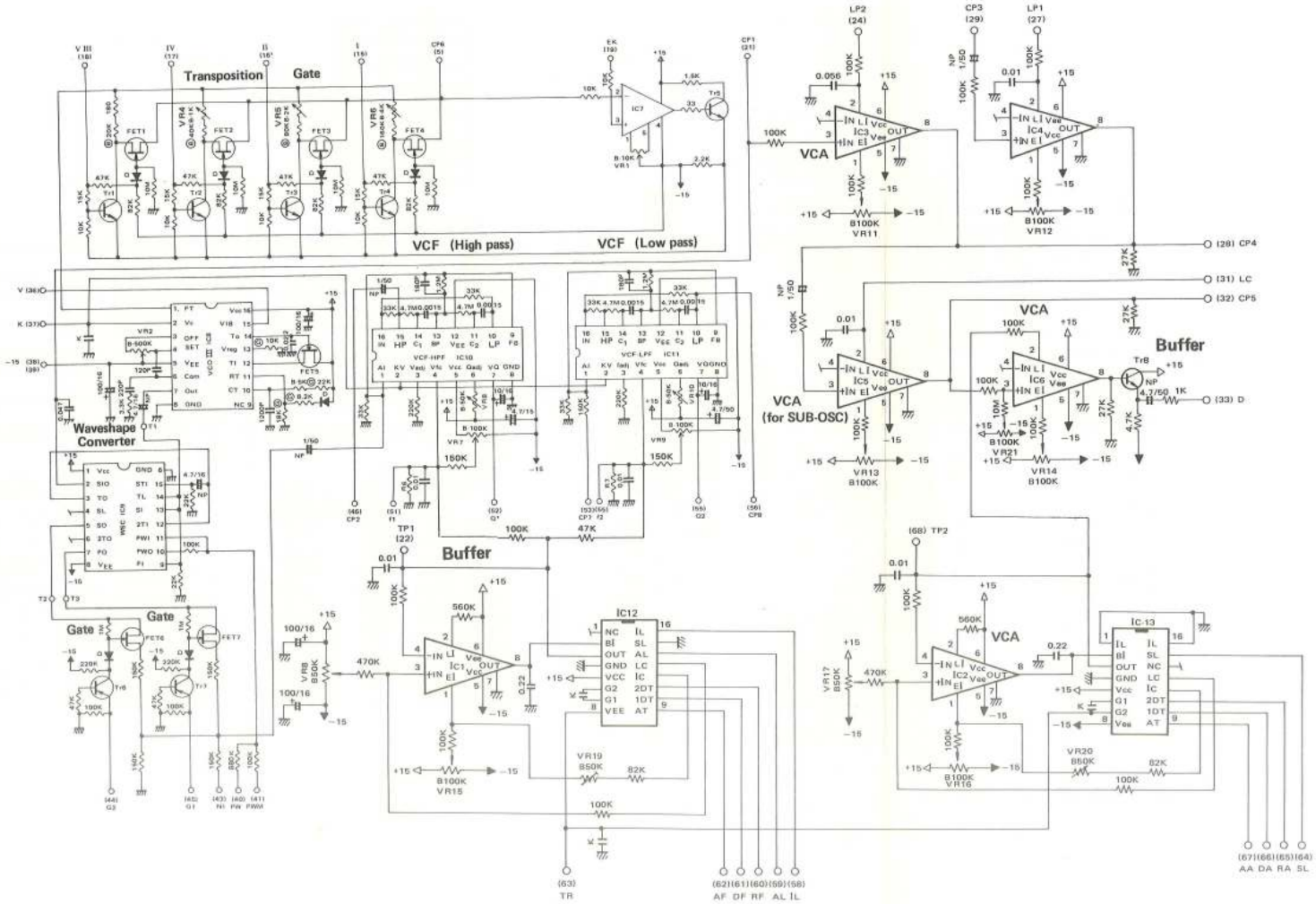


R51-52 Circuit Board & Wiring



Note) 1. Print Board : LC21031
2. Mark : R52 Circuit Board

M Circuit Diagram



	CS-80	CS-60,50
R1	Ⓑ 10K	Ⓑ 20K
R2	Ⓑ 20K	Ⓑ 40K
R3	Ⓑ 40K	Ⓑ 80K
R4	Ⓑ 80K	Ⓑ 160K
R5	100	180
VR4	B-500	B-1K
VR5	B-1K	B-2K
VR6	B-2K	B-5K

Note)

1. Tr1~Tr5,Tr8 : 2SC458
   Tr6, Tr7 : 2SA561
   FET1~7 : 2SK30
2. D : IS1555
3. VR1 : 3321H type
   VR3 : 3006 type
   Other VR : V10K8-1-2 (3 terminals)
   : V10K4A-5-2 (2 terminals)
4. Ⓖ Mark : Metal Film Resistor (2%)
   Ⓑ Mark : " (0.1%)
5. K Mark : Ceramic Capacitor (1000P)
6. IC
   IC views show the pin disposition looked from upper. (Opposite to Pattern)
7. IC
   IC1~6: IG00151 (VCA)
   IC7 : TA7504M
   IC8 : IG00153 (VCOIII)
   IC9 : IG00158
   IC10,11: IG00156
   IC12 : IG00152
   IC13 : IG00159

Constant value of R6, R7 in IC10,11 according to rank

	CS80	CS50,60	
A	2.7K	2.7K	R6
B	2.2K	2.2K	
C	1.8K	1.8K	
A	3.3K	3.0K	R7
B	2.7K	2.4K	
C	2.2K	2.0K	

VCO III IC (IG00153)

This IC is used for voltage controlled oscillator.
Many defferent frequencies are produced by the voltage supplied.

1. FT ..... Resistor for determination of the feet.
   The electric current is provided to the pin from transposition changing circuit so that the octave can be determined.

2. KV ..... Input of the key voltage
   The input of the voltage is provided to the pin in corporation with the keys held down.

   High voltage ..... High frequency
   Low voltage ..... Low frequency

Input Voltage	Output Frequency
0.250V	130.8Hz (C2)
0.500V	261.6Hz (C3)
1.000V	523.2Hz (C4)
2.000V	1046.0Hz (C5)
4.000V	2093.0Hz (C6)

Transposition "normal"

3. } OFF-SET .... Zero adjustment of input buffer circuit
4. }
5. Vee ..... −15V input power source.
6. Com .... Phase compensation for input buffer amplifier.
   Normally, the output (KV + 1V) is supplied to the pin.
7. OUT .... Output

As to the frequency, refer to the Pin No.2 (KV).

8. GND ..... Earth
9. Vref ..... Input of the standard voltage.
10. CT ..... Circuit for time constant.

The following wave shape is produced.

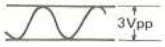
11. RT ..... Circuit for time constant.

Determines the discharging voltage level.

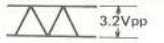
12. T1 ..... Input for the comparator.
    Input of the wave shape (⋀⋀) is provided. from the pin no. 14 (TO).
13. Iref ..... Input of the standard electronic current
14. TO ..... Output from time constant circuit.
    ⋀⋀ The following wave shape is produced.
15. VIB ..... Input for vibrato control wave.
    Input of the control wave is provided by VCO lever of SUB-OSC.
16. Vcc ..... +15V input power source.

WSC IC (IG00158)

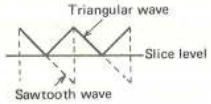
1. Vcc ..... +15V input power source
2. SIO ..... Output of the sine wave



3. TO ..... Output of triangular wave.



4. SL ..... Input of slice level.
   Input of the DC voltage is provided to the pin for determination of the inverting level which makes triangular wave from sawtooth wave.

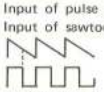


5. $\overline{SO}$ ..... Output of the inverter wave
   Output of inverted sawtooth wave is produced.

6. 2TO ..... Output of double triangle wave
   Double triangle wave is produced from triangle wave.

7. PO ..... Output of pulse wave.

8. Vee ..... −15V input power source.
9. PI ..... Input of pulse wave
   Input of sawtooth wave is provided.



10. PWO ..... Output of OP amplifier.
11. PWI ..... Input of OP amplifier.

Input of the voltage is provided for determination of the sensitivity of PWM lever so that the pulse width is changed.

12. 2TI ..... Input of triangular wave for producing double triangular wave shape.

Input of triangular wave (TO) is provided for making double triangular wave shape.

13. $\overline{SI}$ ..... Input of the pulse for producing inverted sawtooth wave.

Input of the sawtooth wave is provided from VCO III for producing inverted sawtooth wave shape.

14. TI ..... Input of the wave is provided for producing triangular wave shape.

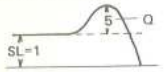
15. STI ..... Input of the wave for producing sine wave.

Input of the triangular wave (TO) is provided for producing sine wave.

16. GND ..... Earth

VCF IC (IG00156)

1. AI ..... Signal Input
   Input signals from VCO and WSC are provided to this pin.
2. KV ..... Key voltage input
   In order to change the tone color according to the tone range of keyboard, the degignated voltage of the key will supplied to the pin. (0.25-4.0V)
3. fc ..... Adjustment of the cut off frequency.
   Set the control currency of the cut off frequency.
4. Vf ..... Input of the cut off voltage.
   Input voltage of cut off frequency is supplied to this pin so that the tone color can be changed. The center point of the cut off frequency can be also set.
   When the VK is 0.25V and Vf is 5V, the cut off frequency is set to just 1KHz.
5. Vcc ..... +15V input power source
6. Q0 ..... Q adjustment.
   The Q control current sets the Q equal to 5, when VQ is 0 volt.



7. VQ ..... Input of the voltage for Q control.
   Q is variable according to the control voltage supplied.
   When the control voltage is
   0V (Max.), Q=5
   When the control voltage is
   10V (Min.), Q=0.5
8. GND .... Earth
9. FB ..... Q feed back
   This is the feed back output pin for the Q control by which the Q is determined.
10. LP ..... Low-pass output

The output of lower frequencies are produced.

11. C2 ..... C pin for determination of the cut off frequency.
12. Vee ..... −15V power source.
13. BP ..... Band-pass output.

The output of intermediate frequences are produced.

14. C1 ..... C pin for determination of the cut off frequency.
15. HP ..... Hi-pass output

The output of higher frequencies are produced.

16. IN ..... Input of feed back
    The input signal for determination of cut off frequency.

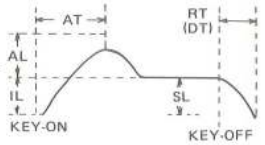
VCF-EG IC (IG00152)

This IC generates envelope wave shape which is supplied to VCF and control the tone color.

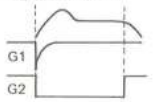
1. NC ..... Not connected
2. BI ..... Input of buffer amplifier.
3. OUT .... Output of buffer amprifier.

The buffer amplifier is built in for the purpose of matching inpedance.

Output wave shape



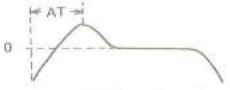
4. GND .... Earth
5. Vcc ..... +15V input power source.
6. G1 ..... Gate 1
7. G2 ..... Gate 2



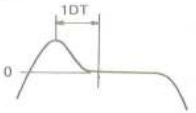
AT starting data

Key ON-OFF data

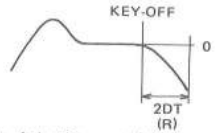
8. Vee ..... −15V input power source.
9. AT ..... Input of buffer voltage for determination of the attack time.
   Input of the voltage between zero V and 10V is provided and the attack time is controlled from 1 mS until 1S.



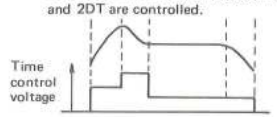
10. 1DT ..... Input of buffer voltage for determination of the decay time.
    Input of the voltage between zero V and 10V is provided and the first decay time is controlled from 10mS until 10 S.



11. 2DT ..... Input of buffer voltage for determination of the release time.
    Input of the voltage between zero V to 10V is provided and the time from KEY-ON until release is controlled from 10m second until 10 second.

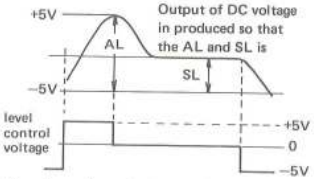


12. TC ..... Output of the time control.
    Output of DC voltage is produced so that the each time of attack, 1DT and 2DT are controlled.



The higher the voltage, the shorter the time and the lower the voltage the longer the time.

13. LC ..... Output of level control.



Output of DC voltage in produced so that the AL and SL is

The higher the voltage, the higher the level and the lower the voltage the lower the level.

14. AL..... Input of butter voltage for determination of attack level.
    Input of the voltage between (0V~10V is provided and the attack level is controlled from 0V until +5V.

15. SL ..... Input of buffer voltage for determination of the sustain level.
    Normally fixed to zero(0) volt.

16. IL ..... Input of buffer voltage for determination of the initial level.
    Input of the voltage between zero 0V and ten 10V is provided and the initial level is controlled from zero to minus 5 volt.

Pitch Adjustment

*Before carring out pitch adjustment, be sure to stabilize electrical circuits of your synthesizer more than 10 minutes after power switch is turned on.

1. Adjust the following variable resistors, when tuning knob on the panel is in the center position, so that the voltage of TU terminal is +4V ± 0.1% between the terminals TU (67) and E on KAS circuit board.

	Circuit Board	VR No.	
CS-50	SUB	VR11	B-200Ω
CS-60	SUB	VR14	B-100KΩ
CS-80	BA	VR3	B-100KΩ

*Make sure, all the levers on the panel are in "OFF" position.

2. Short circuit terminals EK (19) and E (1)(2) on M circuit board when tone selector is set to "FLUTE" and transposition lever is in OCT-UP (2' on CS-80) (+15V, terminal VIII on M circuit board) position. Then, adjust VR1 (B-10K) on M circuit board so that the voltage of terminal Cp (5) is within 0V ±120μV.

3. With checking the output frequency from the 9th pin of IC9 (WSC) on M circuit board, adjust the VR3 (B-5K) to have the certain highest note.

CS-50, 60 ..... C7 to be 4186 Hz ± 1 cent when C6 key is depressed.
CS-80 ..... C8 to be 8372 Hz ± 1 cent when C6 key is depressed.

In the same manner, set the lowest note by adjusting the VR2 (B-500) as follows.
CS-50 ..... C3 to be 261.6Hz ± 1 cent when C2 key is depressed.
CS-60 ..... C2 to be 130.8Hz ± 1 cent when C1 key is depressed.
CS-80 ..... C3 to be 261.6Hz ± 1 cent when C1 key is depressed.

Note:
(1) You can easily find out which M circuit board is corresponding to the key depressed by hearing the subtle change of the tone with hum modulation when you are adjusting VR2 on A M circuit board.
(2) The order the possible sounds when keys are depressed in turn is:
① → ② → ③ → ④ → ⑤ → ⑥ → ⑦ → ①
When keys are depressed at a time:
① → ② → ③ → ④ → ⑤ → ⑥ → ⑦ → ⑧ → ①
Therefore, when you adjust the M8 circuit board, depress the 8th key while 7th key is holding down, then release the 7th key. In this way, you can take out the sound of 8th key depressed.

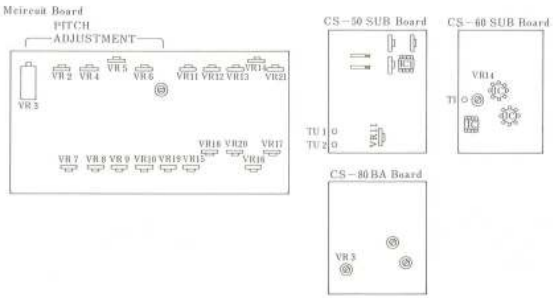
4. Adjust the following variable resisters, when transposition lever is in "NORMAL" ("4'" on CS-80) position (+15V at terminal IV) as below.
CS-50, 60 ..... VR4 (B-1K); C6 to be 2093Hz ± 1 cent when C6 key is depressed.
CS-80 ..... VR4 (B-500): C7 to be 4186Hz ± 1 cent when C6 key is depressed.

5. Adjust the following variable resistors, when transposition lever is in "1 OCT-DOWN" ("8'" on CS-80) position (+15V at terminal II) as below.
CS-50, 60 ..... VR5 (B-2K); C5 to be 1046Hz ± 1 cent when C6 key is depressed.
CS-80 ..... VR5 (B-1K); C6 to be 2093Hz ± 1 cent when C6 key is depressed.

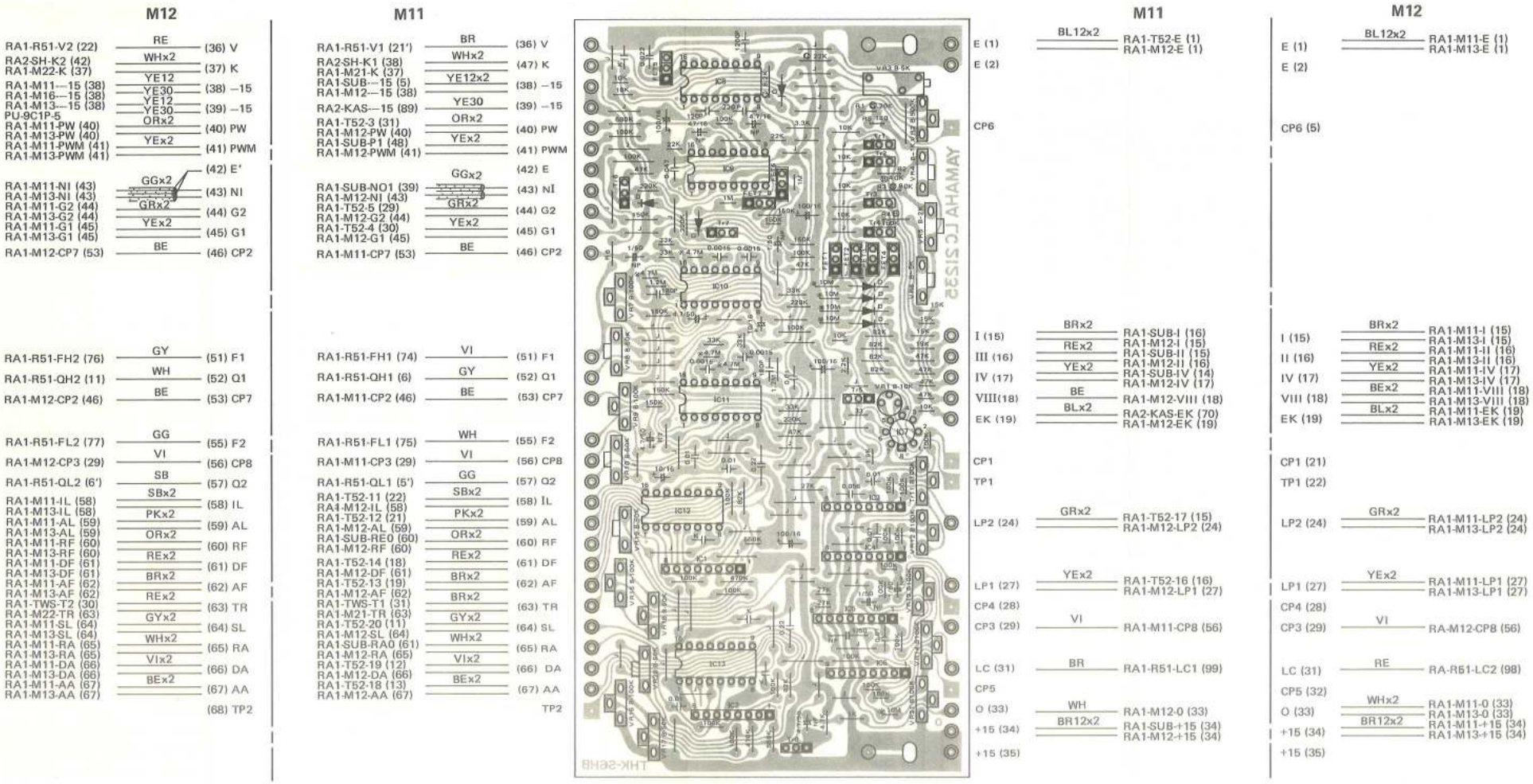
6. Likewise, adjust the followings, when transposition lever is in "2 OCT-DOWN" ("16'" on CS-80) position (+15V at terminal I) as below.
CS-50, 60 ..... VR6 (B-5K); C4 to be 523.2Hz ± 1 cent when C6 key is depressed.
CS-80 ..... VR6 (B-2K); C5 to be 1046.4Hz ± 1 cent when C6 key is depressed.

7. Finally, adjust the generating circuit for tuning standard voltage, when A3 key is depressed, so that the output is 443 Hz.

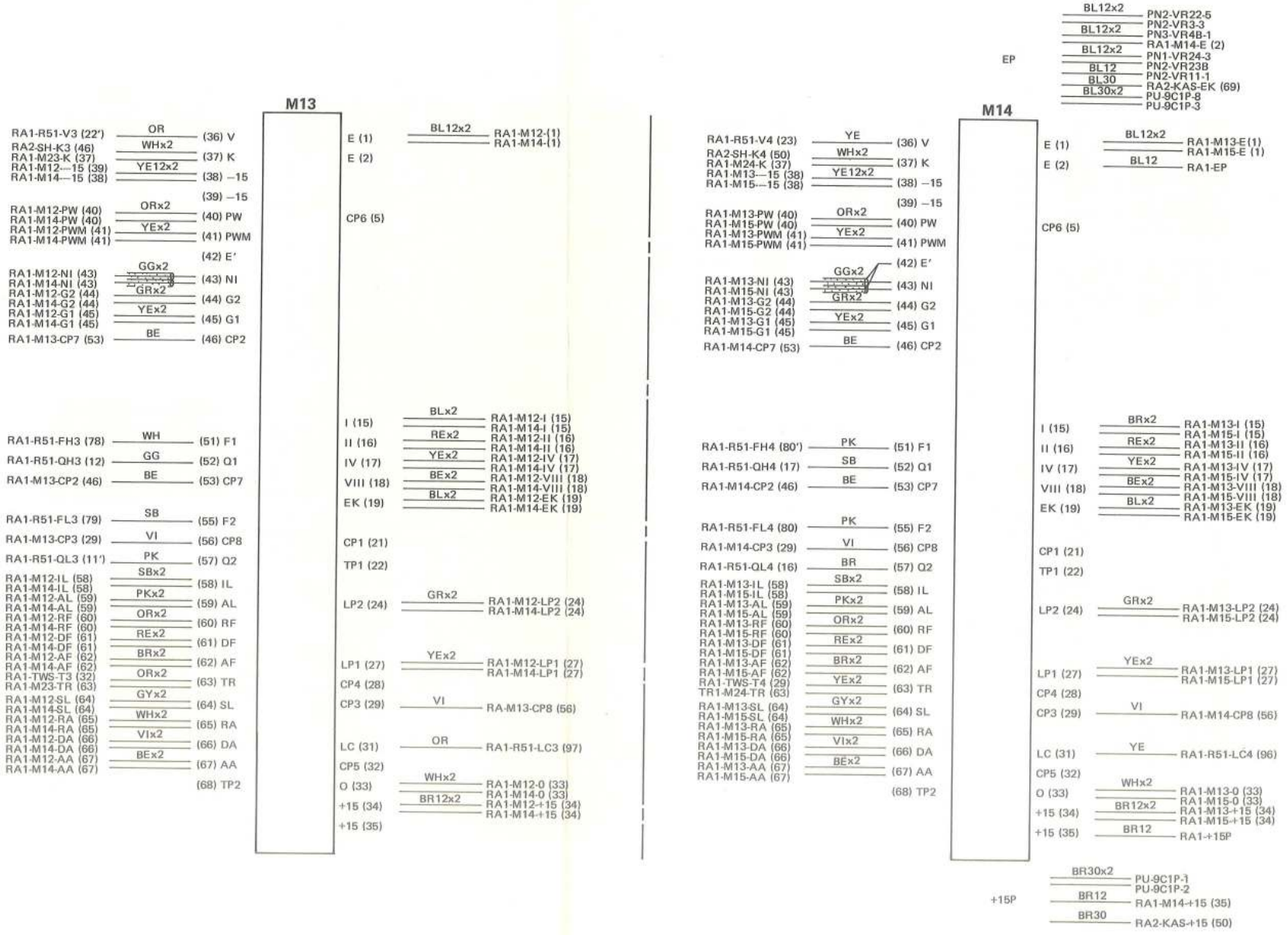
	Circuit Board	VR. No.	
CS-50	SUB	VR11	B-200Ω
CS-60	SUB	VR14	B-100Ω
CS-80	BA	VR3	B-100KΩ



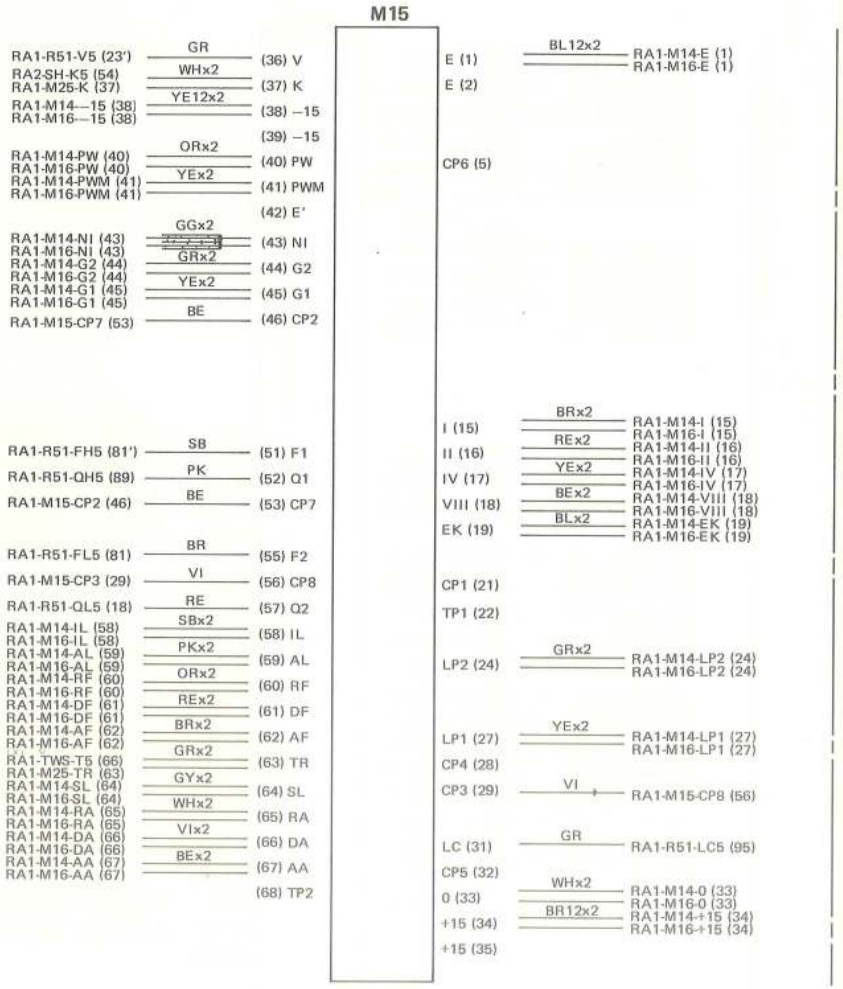
M11 ·12 Circuit Board & Wiring

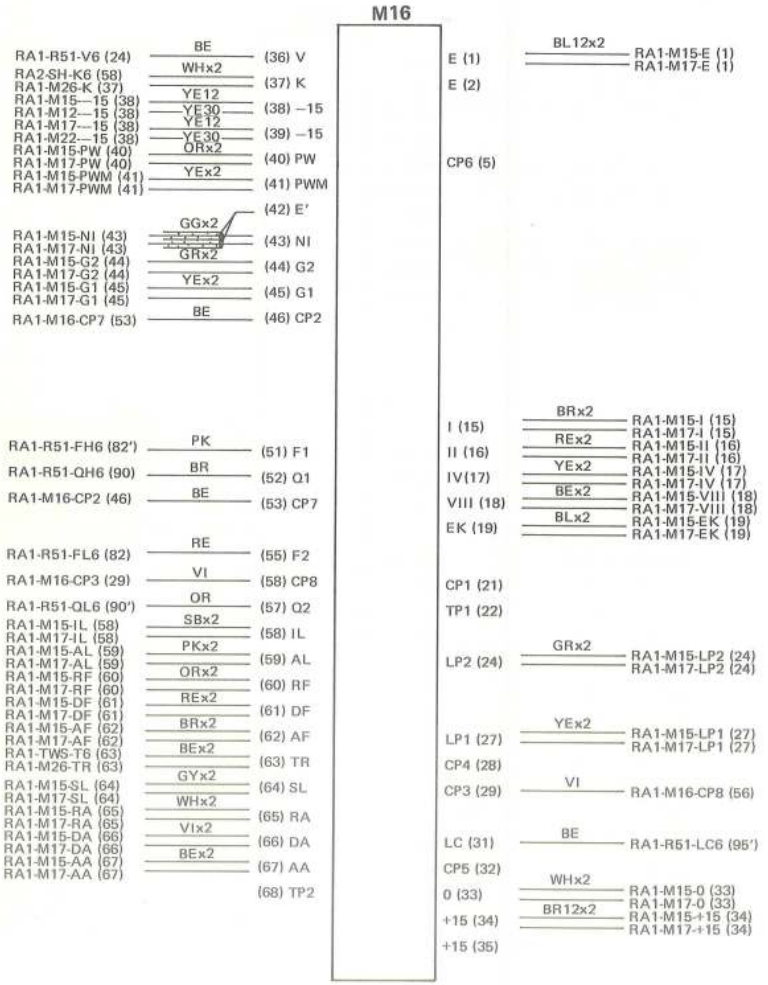


M13·14 Circuit Board & Wiring

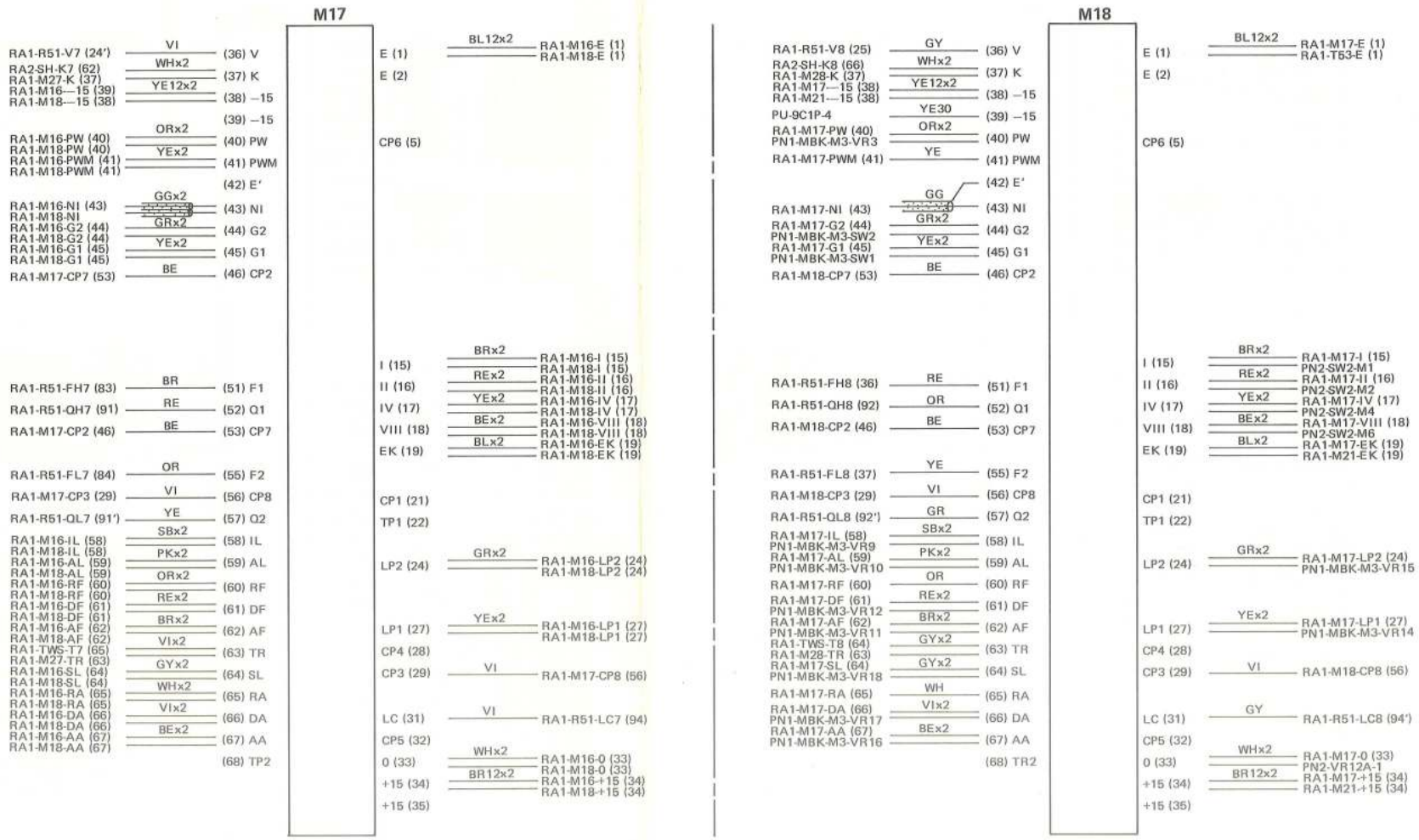


M15·16 Circuit Board & Wiring

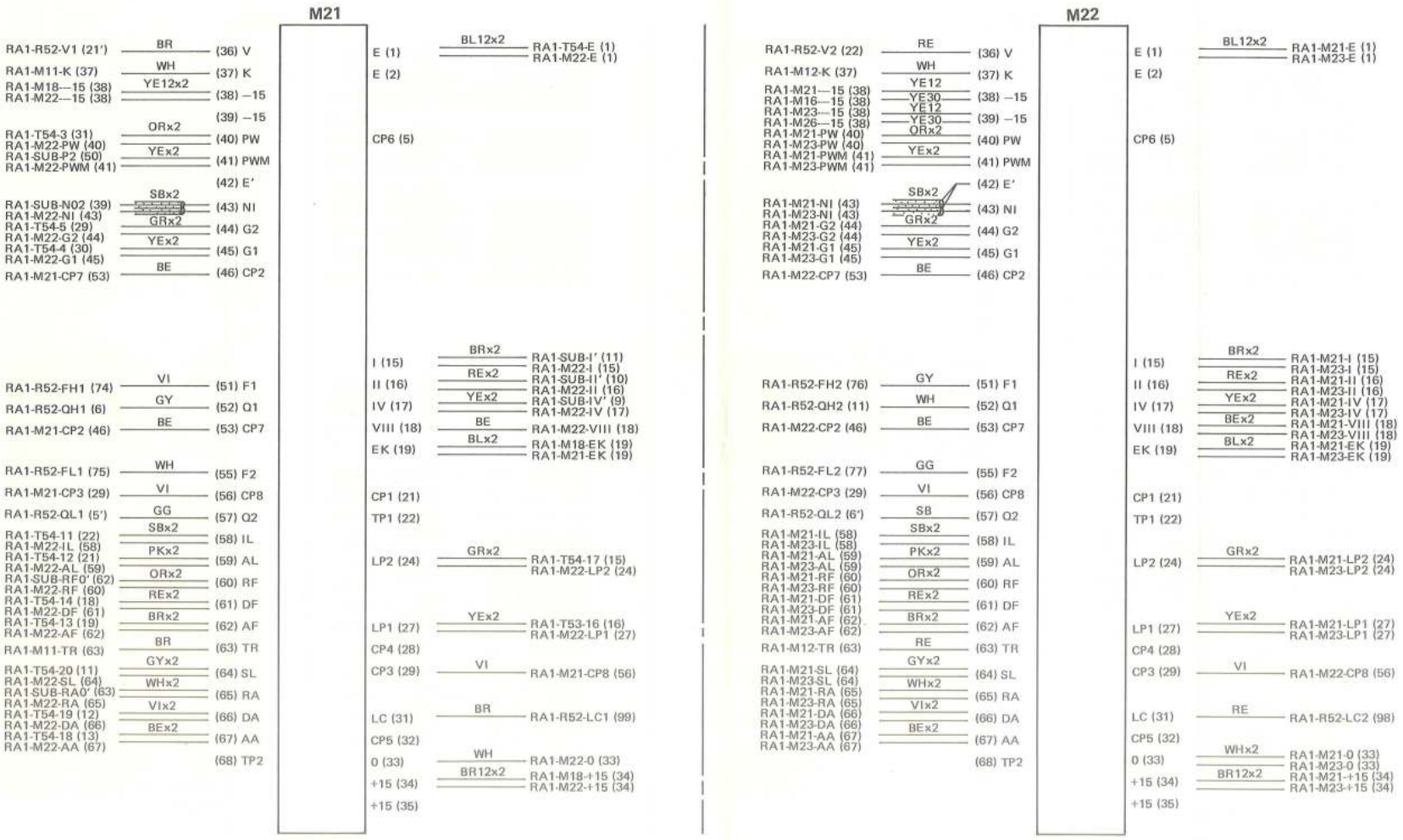




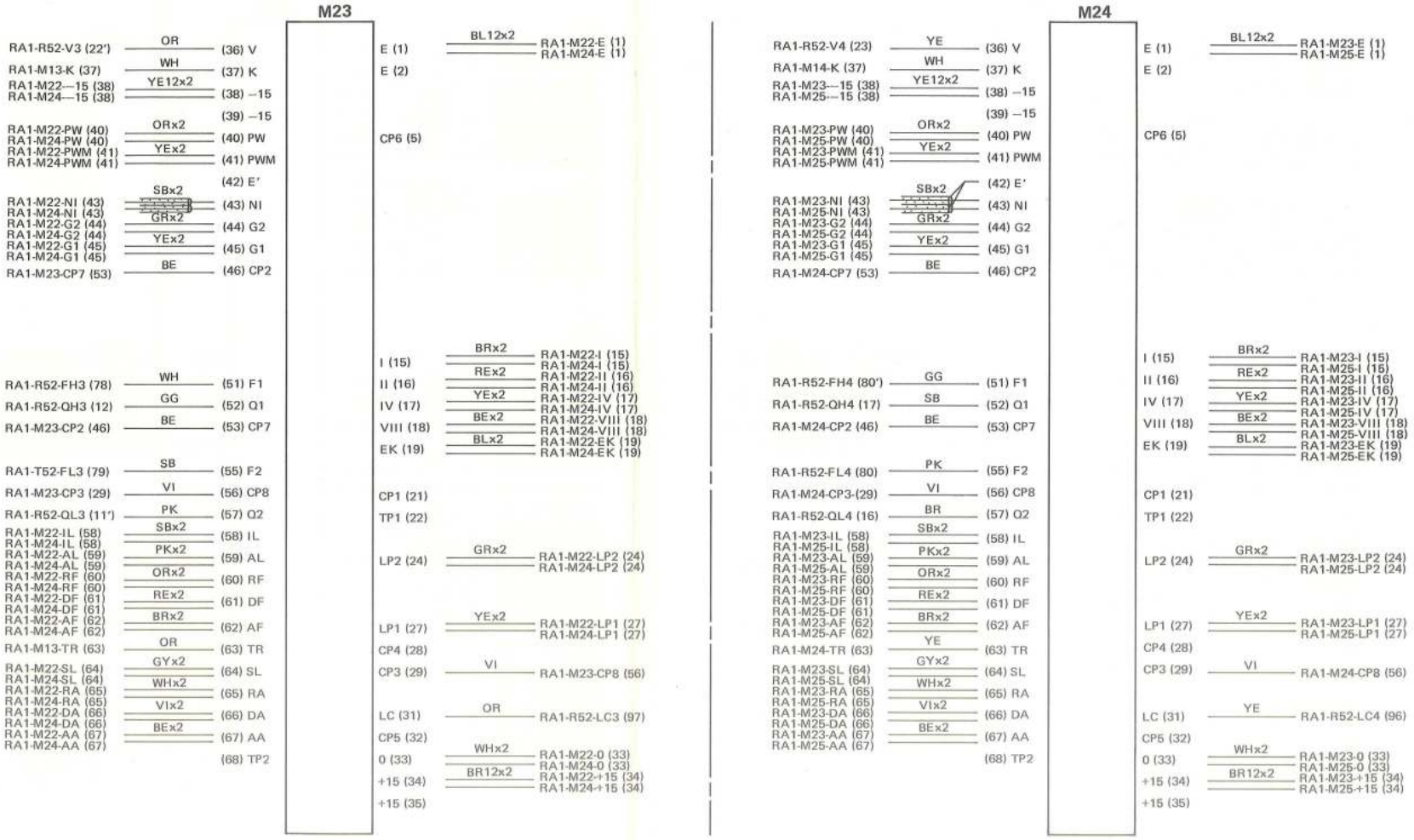
M17·18 Circuit Board & Wiring



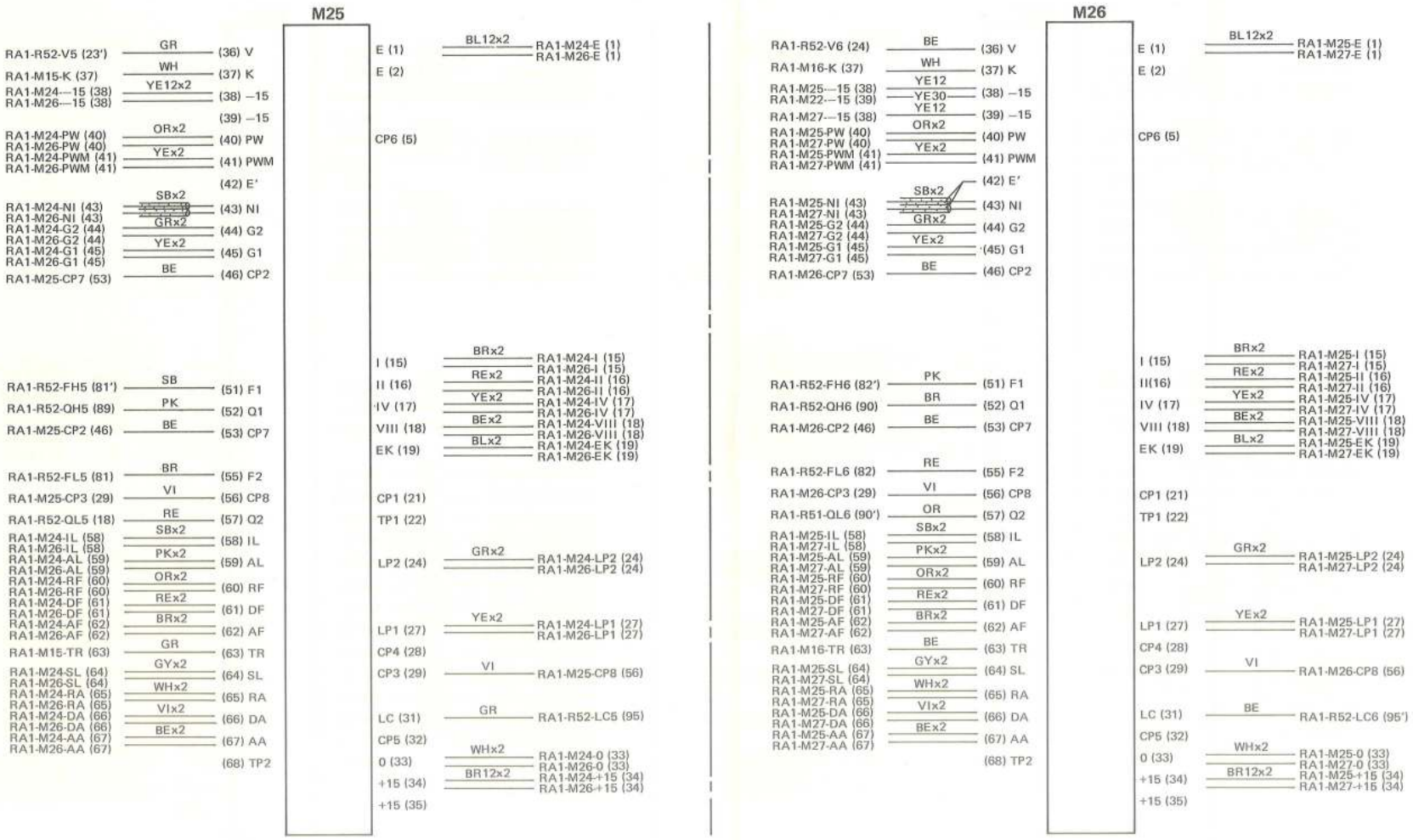
M21-22 Circuit Board & Wiring



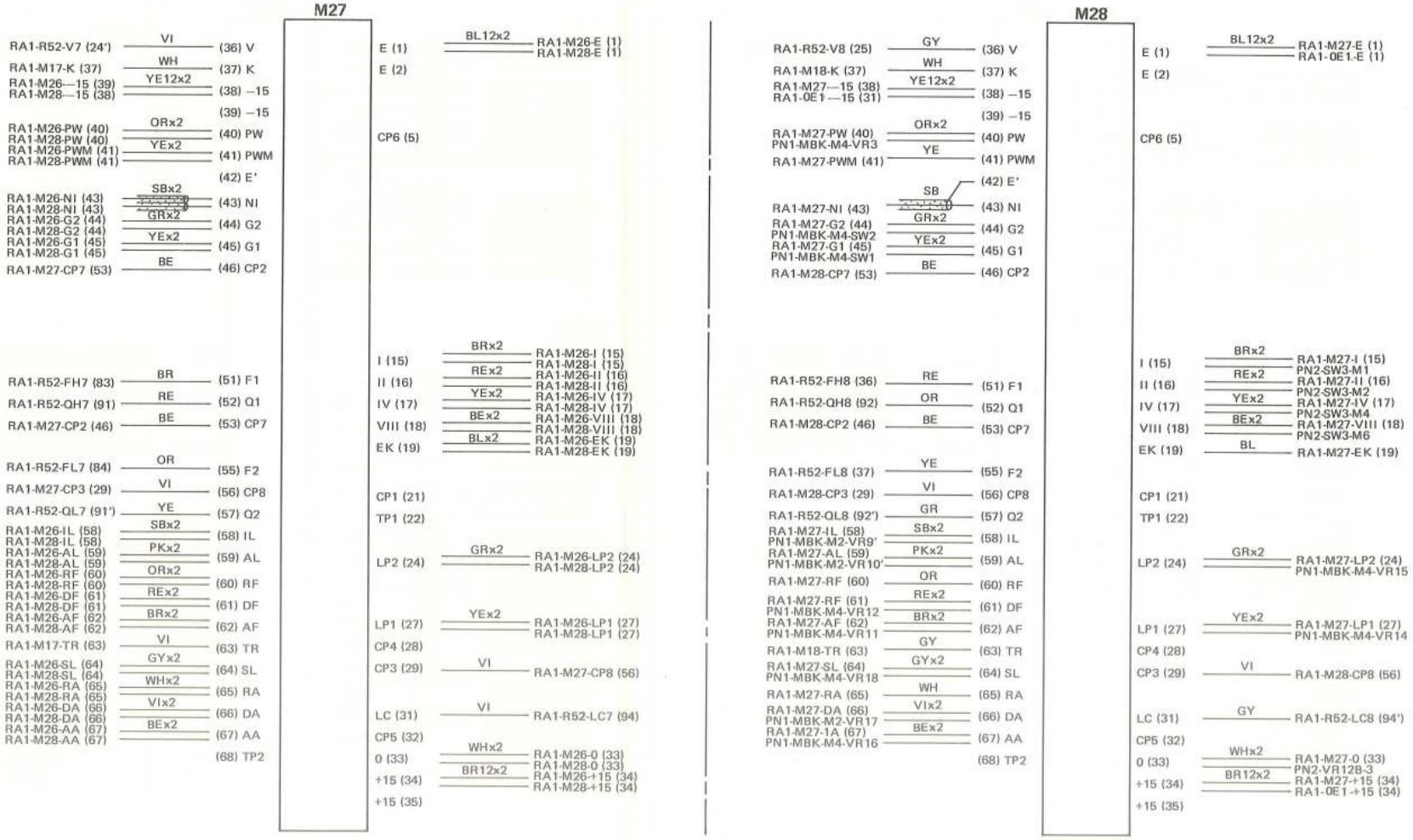
M 23·24 Circuit Board & Wiring



M25·26 Circuit Board & Wiring

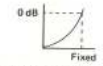


M 27·28 Circuit Board & Wiring

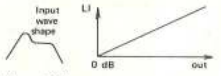


VCA IC (IG00151)

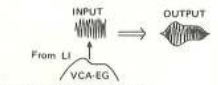
1. EI ..... Input voltage for level control.
   Input of the control voltage is provided for exponenti-ally changing the level expotenentially.



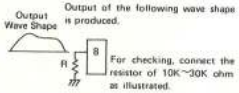
2. LI ..... Input of level control voltage.
   Input of the control voltage is provided for linear change of the level.



3. +IN ..... Input
   Input of the level modulated signal is provided.



4. −IN ..... Negative feed back.
   Normally unused.
5. Vee ..... −15V input power source.
6. Vcc ..... +15V input power source.
7. GND ..... Earth
8. OUT ..... Output
   Output of the following wave shape is produced.



For checking, connect the resistor of 10K~30K ohm as illustrated.

VCOII IC (IG00150)

1. VI ..... Input of the control voltage.
   The frequency is variable in accordance with the voltage supplied.

2. GND ..... Earth
3. C ..... Capacitor for determination of the frequency.
4. Vee ..... −15V input power source.
5. Vcc ..... +15V input power source.
6. SIO ..... Output of sine wave.

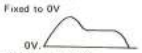
7. SO ..... Output of sawtooth wave

8. Iadj ..... Setting for standard electric currents.
   The standard electric current is set so as to be the output 200Hz when VC1 is 10V and VC2 is zero volt.

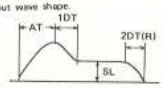
VCA-EG IC (IG00159)

This IC generates envelope wave shape which is supplied to VCA and control the tone volume.

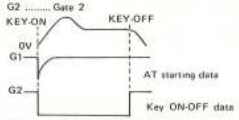
1. IL ..... Input of initial level.



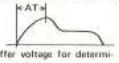
2. BI ..... Input of buffer amplifier.
3. OUT .... The buffer amplifier is built in for the purpose of matching inpedance.
   Output wave shape.



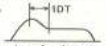
4. GND ..... Earth
5. Vcc ..... +15V input power source.
6. G1 ..... Gate 1
7. G2 ..... Gate 2



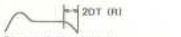
8. Vee ..... +15V input power source.
9. AT ..... Input of buffer voltage for determination of attack time.
   Input of the voltage between zero V and 10V is provided and the attack time is controlled from 1 mS until 15.



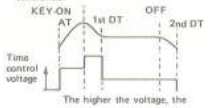
10. 1DT ..... Input of buffer voltage for determination of decay time.
    Input of the voltage between zero V and 10V is provided and the decay time is controlled from 10 m second until 10 second.



11. 2DT ..... Input of buffer voltage for determination of release time.
    Input of the voltage between zero V and 10V is provided and the time key-off until release is controlled from 10 mS until 10 S.

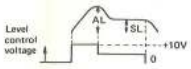


12. TC ..... Output of time control.
    Output of the DC voltage is produced so that the each time of Attack, 1st Decay and 2nd Decay are controlled.



The higher the voltage, the shorter the time and the lower the voltage, the longer the time.

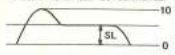
13. LC ..... Output of level control



Output of the DC voltage for AL and SL control is provided.

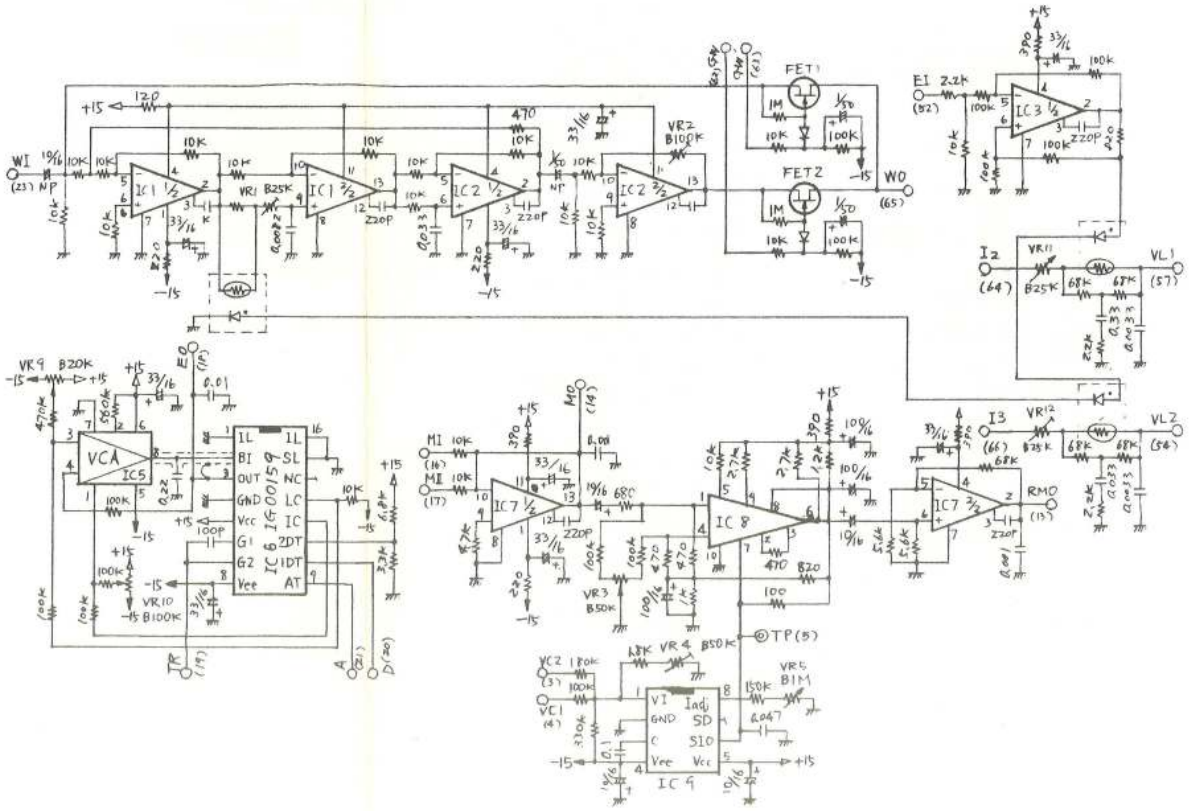
The higher the voltage, the higher the level and the lower the voltage, the lower the level.

14. NC..... Not connected.
15. SL ..... Input of buffer voltage for determination of the sustain level.
    Input of the voltage between zero V and 10V is provided so that the sustain level can be controlled.

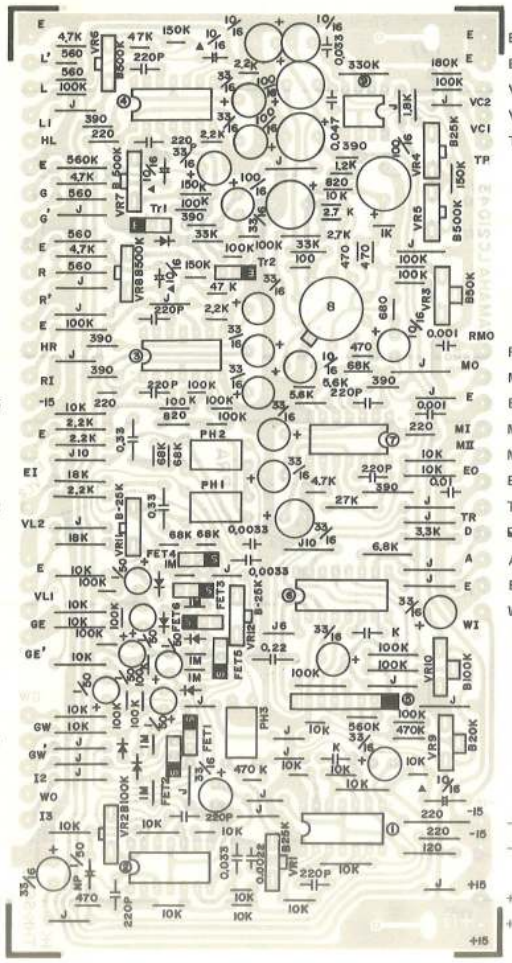


16. NC..... Not connected.

PRA Circuit Diagram



PRA Circuit Board & Wiring



PN5-J3-1 — RE — (36) E
PN5-J3-1 — VI — (37) L'
PN5-J3-1 — (38) L
PN2-VR23A-2 — BR — (39) LI
RA1-SUB-LI(67) — YE — (40) HL
(41) E
PN5-J2-1 — OR — (42) G
PN5-J2-1 — YE — (43) G'
(44) E
PN5-J1-1 — GR — (45) R
PN5-J1-1 — BE — (46) R'
(47) E
PA1-SUB-RI(32) — GR — (48) HR
PN2-VR23B-2 — SB — (49) RI
PN5-J4-3 — YE12 — (50) −15
PN5-J4-1 — BL12 — (51) E
PN5-J4-2 — VI — (52) EI
PN2-VR23B-3 — PK — (54) VL2
(56) E
PN2-VR23A-3 — RE — (57) VL1
PN3-SW9-M1 — GR — (62) GW
PN3-SW9-B1 — YE — (63) GW'
RA1-OE1-02(55) — OR — (64) I2
RA1-OE1-I1(38) — VI — (65) WO
RA1-OE1-03(60) — YE — (66) I3

E (1) — BL12x2 — RA1-OE1-E(1)
E (2)
VC2 (3) — YE — PN2-VR5-2
VC1 (4) — GR — PN2-VR6-2
TP (5)
RMO (13) — BE — PN2-VR7-3
MO(14) — GR — PN2-VR7-1
E (15)
MI (16) — GG — PN2-VR12A-2
MII (17) — PK — PN2-VR12B-2
EO (18) — BR — PN2-VR5-3
TR (19) — WH — RA2-KAS-TR9(35)
D (20) — RE — PN2-VR4-2
A (21) — OR — PN2-VR3-2
E (22)
WI (23) — VI — PN2-VR7-2
−15 (31) — YE12x2 — PN2-VR22-1 — RA1-OE1—15(69)
−15 (32)
+15 (34) — BR12x2 — PN2-VR22-3 — RA1-OE1-+15(34)
+15 (35) — BR12 — EJT

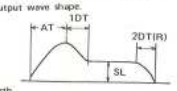
Note) 1. Print Board : LC21043
2. Transistor
Tr 1, 2 : 2SC828
FET 1 ~ 6 : 2SK30A
3. IC
IC 1 ~ 4, 7 : HA1452T
IC 5 : IG00151
IC 6 : IG00159
IC 8 : MA796HC
IC 9 : IG00150
4. Diode : IS1555
5. Volume
VR 3, 9, 10 : V10K8-1-2
VR 1, 2, 4 ~ 8, 11, 12 : V10K4A-5-2

VCA-EG IC (IG00159)

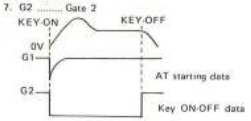
This IC generates envelope wave shape which is supplied to VCA and control the tone volume.

1. IL ..... Input of initial level.
Fixed to 0V

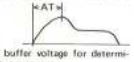
2. BI ..... Input of buffer amplifier.
3. OUT ... The buffer amplifier is built in for the purpose of matching inpedance.
Output wave shape.



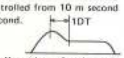
4. GND ..... Earth
5. Vcc ..... +15V input power source.
6. G1 ..... Gate 1
7. G2 ..... Gate 2



8. Vee ..... +15V input power source.
9. AT ..... Input of buffer voltage for determination of attack time.
Input of the voltage between zero V and 10V is provided and the attack time is controlled from 1 mS until 1S.

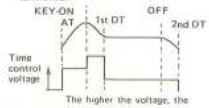


10. 1DT ..... Input of buffer voltage for determination of decay time.
Input of the voltage between zero V and 10V is provided and the decay time is controlled from 10 m second until 10 second.



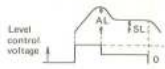
11. 2DT ..... Input of buffer voltage for determination of release time.
Input of the voltage between zero V and 10V is provided and the time kzy-off untill release is controlled from 10 mS until 10 S.

12. TC ..... Output of time control.
Output of the DC voltage is produced so that the each time of Attack, 1st Decay and 2nd Decay are controlled.



The higher the voltage, the shorter the time and the lower the voltage, the longer the time.

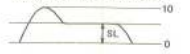
13. LC ..... Output of level control



Output of the DC voltage for AL and SL control is provided.

The higher the voltage , the higher the level and the lower the voltage, the lower the level.

14. NC ..... Not connected.
15. SL ..... Input of buffer voltage for determination of the sustain level.
Input of the voltage between zero V and 10V is provided so that the sustain level can be controlled.

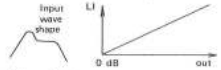


16. NC ..... Not connected.

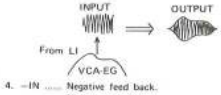
VCA IC (IG00151)

1. EI ..... Input voltage for level control.
Input of the control voltage is provided for c-snging the level expotenentially.

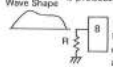
2. LI ..... Input of level control voltage.
Input of the control voltage is provided for linear change of the level.



3. +IN ..... Input
Input of the level modulated signal is provided.



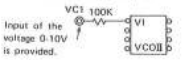
4. −IN ..... Negative feed back.
Normally unused.
5. Vee ..... −15V input power source.
6. Vcc ..... +15V input power source.
7. GND ..... Earth
8. OUT ..... Output
Output of the following wave shape is produced.



For checking, connect the resistor of 10K~30K ohm as illustrated.

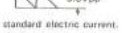
VCOII IC (IG00150)

1. VI ..... Input of the control voltage.
The frequency is variable in accordance with the voltage supplied.



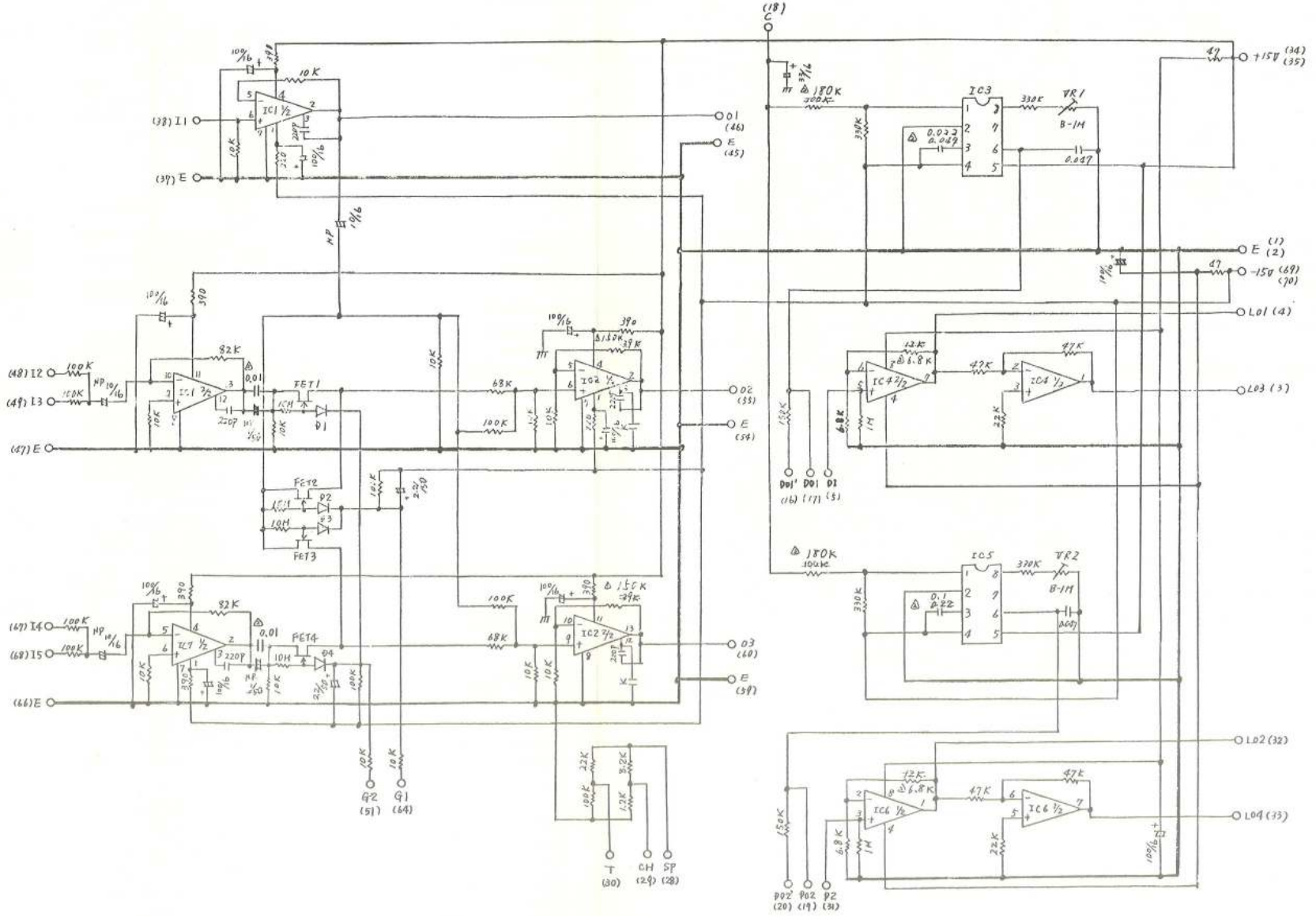
2. GND ..... Earth
3. C ..... Capacitor for determination of the frequency.
4. Vee ..... −15V input power source.
5. Vcc ..... +15V input power source.
6. SIO ..... Output of sine wave.

7. SO ..... Output of sawtooth wave

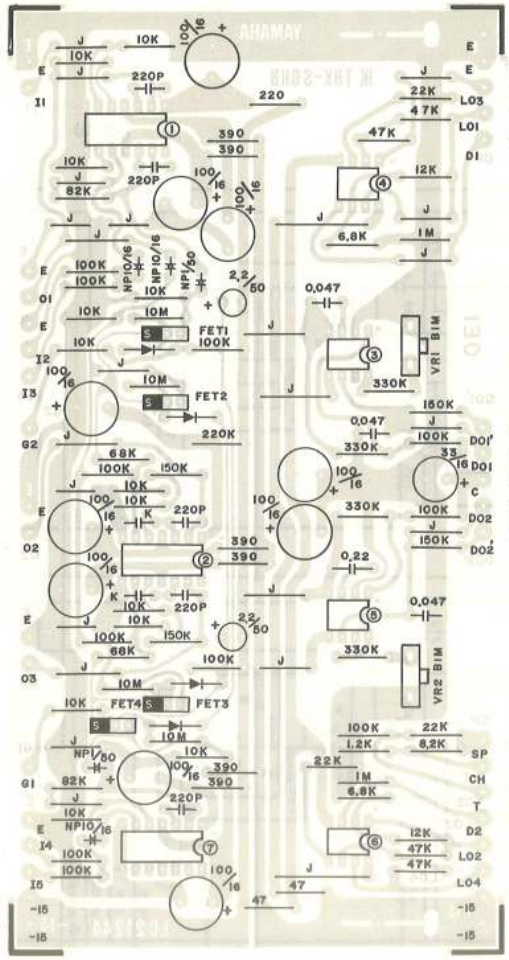


8. Iadj ..... Setting for standard electric current.
The standard electric current is set so as to be the output 200Hz when VC1 is 10V and VC2 is zero volt.

OE1 Circuit Diagram



OE1 Circuit Board & Wiring



RA1-PRA-WO(65) — GR — (37) E / (38) I1
RA1-OE2-I(53) — GR — (45) E / (46) O1
RA1-OE2-01(43) — BE — (47) E / (48) I2
RA1-OE2-02(38) — GY — (49) I3
PN3-SW7-M1 — WH — (51) G2
RA1-PRA-I2(64) — OR — (54) E / (55) O2
RA1-PRA-I3(66) — YE — (59) E / (60) O3
PN3-SW7-B1 — GY — (64) G1
RA1-OE2-03(8) — BR — (66) E
RA1-OE2-04(3) — RE — (67) I4 / (68) I5
RA1- M28—15(38) — YE12x2 — (69) −15
RA1-OE2—15(69) — (70) −15

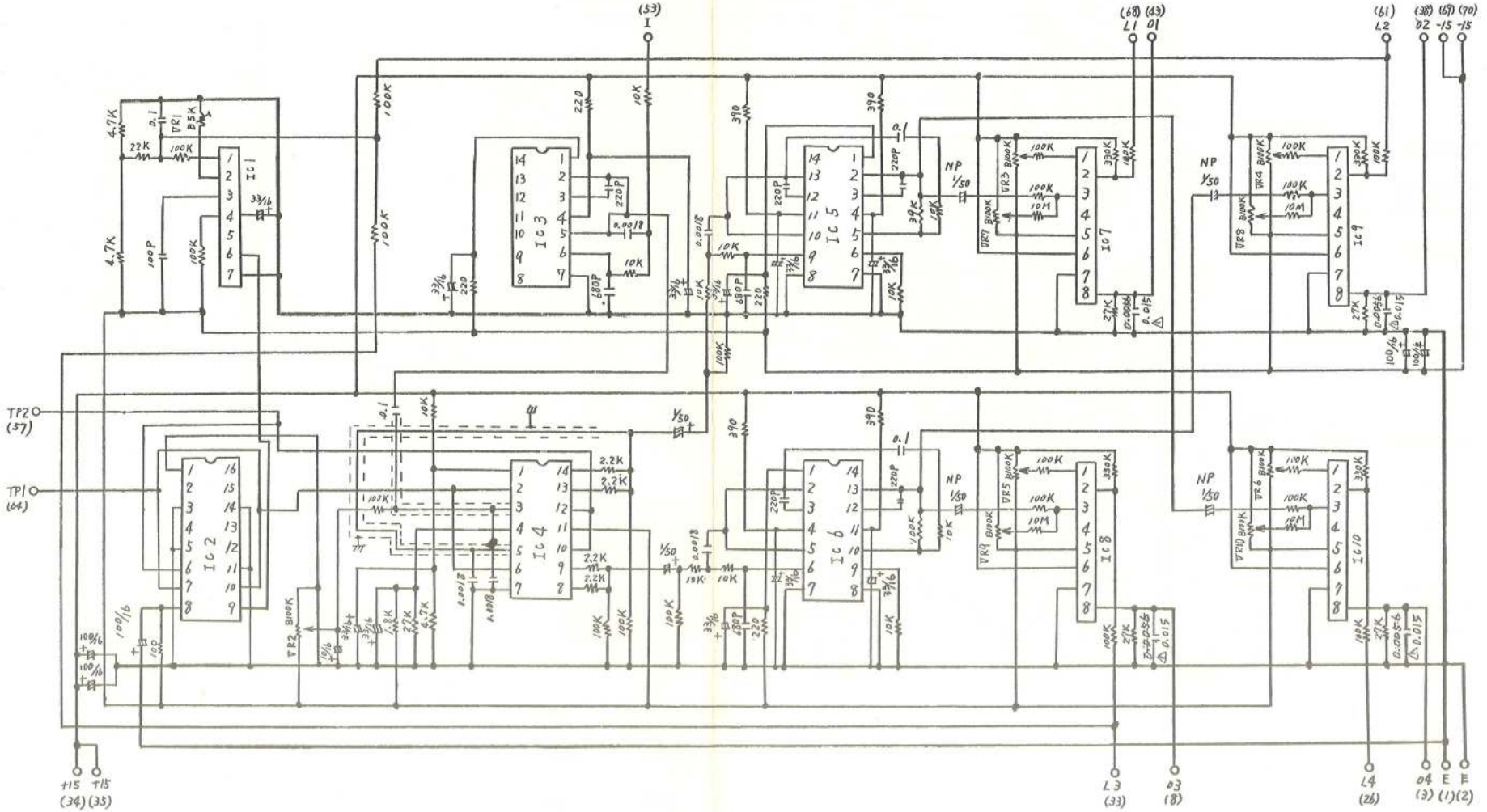
E (1) — BL12x2 — PA1- M28-E(1) / PA1-OE2-E(1)
E (2)
L03 (3) — OR — RA1-OE2-L3(33)
L01 (4) — BR — RA1-OE2-L1(68)
D1 (5) — GR — PN3-VR4A-2

D01 (16) — RE — PN3-SW5-M2
D01 (17) — VI — PN3-SW6-M2
C (18) — YE — PN3-SW5-T1
D02 (19) — WH — PN3-SW6-M3
D02' (20) — PK — PN3-SW5-M3

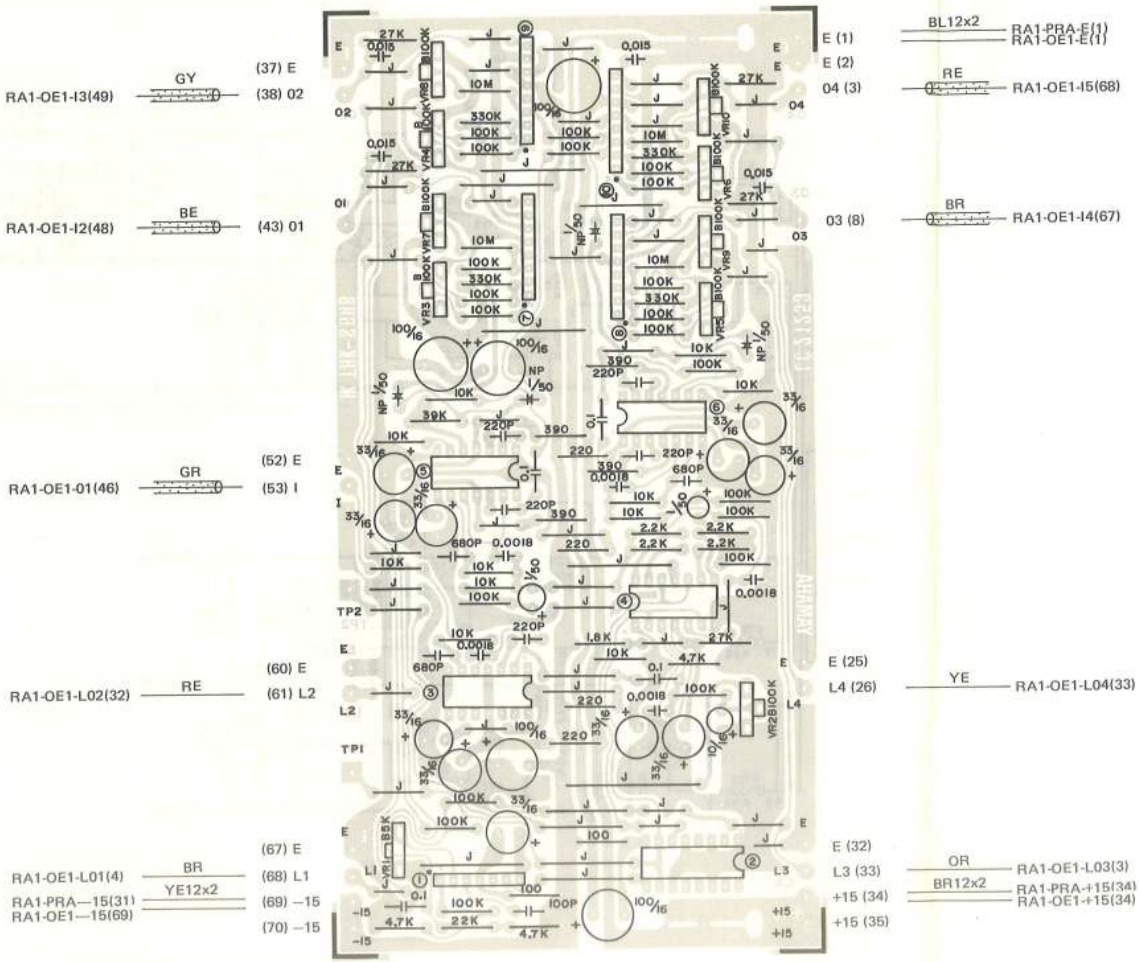
SP(28) — GR — PN3-VR3-2
CH (29) — GY — PN3-SW6-M1
T (30) — OR — PN3-SW5-M1
D2 (31) — VI — PN3-VR4B-2
L02 (32) — RE — RA1-OE2-L2(61)
L04 (33) — YE — RA1-OE2-L4(26)
+15 (34) — BR12x2 — RA1- M28-+15(34) / RA1-OE2+15(34)
+15 (35)

Note) 1. Print Board : LC21244
2. Transistor
FET : 2SK30Y
3. Diode : IS1555
4. IC
IC 1, 2, 7 : HA1452D
IC 3, 5 : IG00150
IC 4, 6 : NJM4558
5. VR : 2 terminals (V10K4A-5-2)

OE2 Circuit Diagram

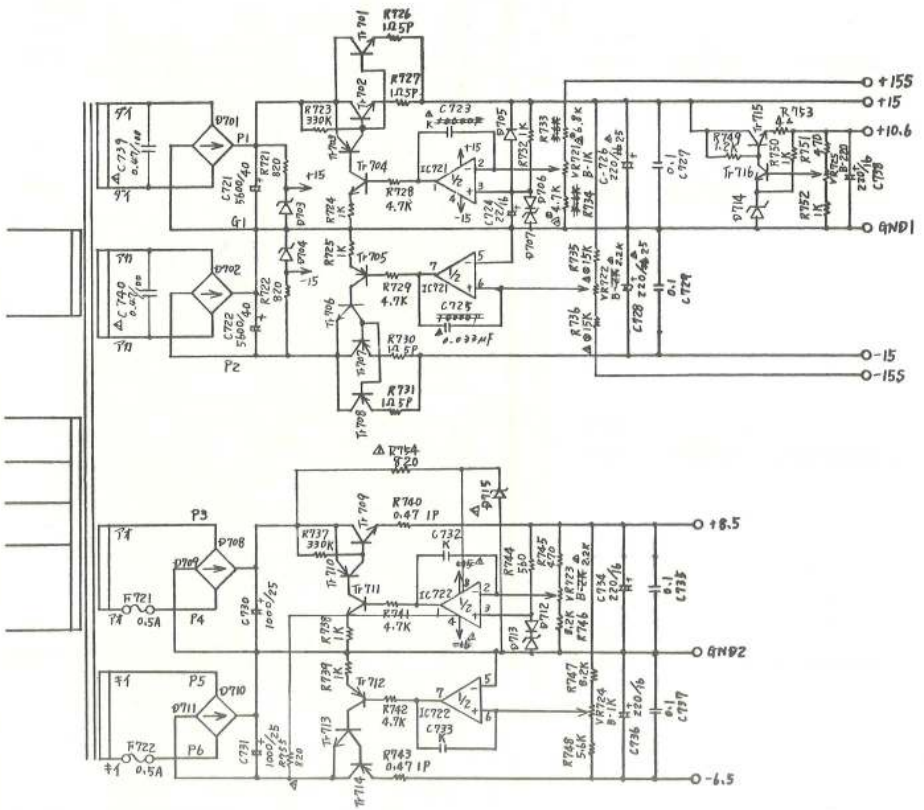


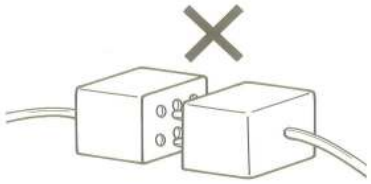
OE2 Circuit Board & Wiring



Note) 1. Print Board : LC21253
2. IC
IC 1 : BA617
IC 2 : TC4049
IC 3, 5, 6 : HA1452D
IC 4 : MN3001
IC 7 ~ 10 : IG00151
3. VR : 2 terminals (V10K4A-5-2)
: 3 terminals (V8K8A-1-2)

SVU Circuit Diagram





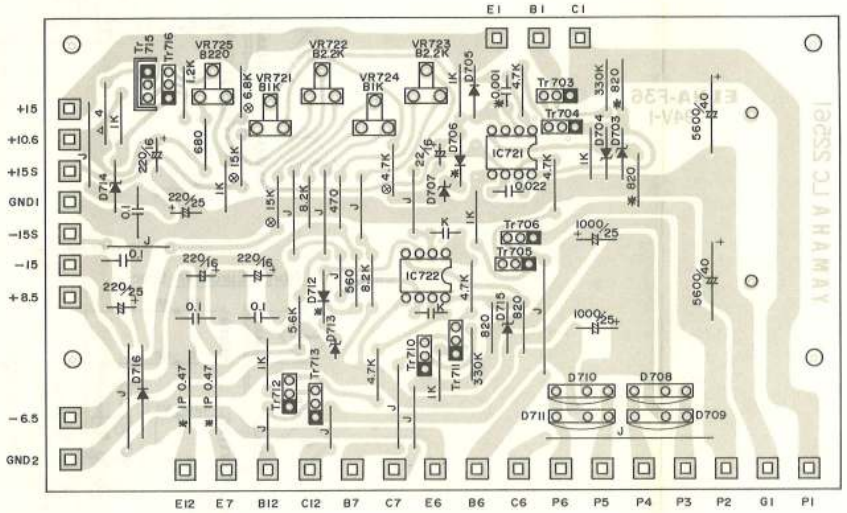
[Caution]

When you carry out inspection on Power Supply Unit, be sure to make complete connection of the connector or Short Circuit both +15 ↔ +15S and −15 ↔ −15S. Otherwise, the circuit can break with the power on.

〔注意〕

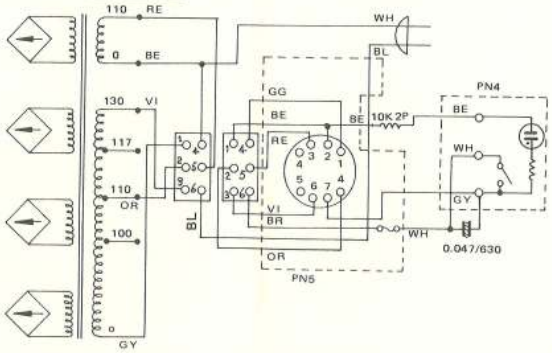
電源ユニットの点検などで，コネクターを取外したままで電源を入れますと回路を壊わす恐れがありますのでコネクターを取付けたまま，又は＋15↔＋15Ｓ，−15↔−15Ｓをショートさせて点検を行なう様お願いします。

SVU Circuit Board & Wiring

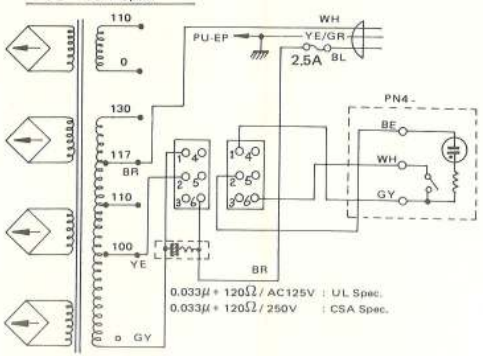


South African Spec.

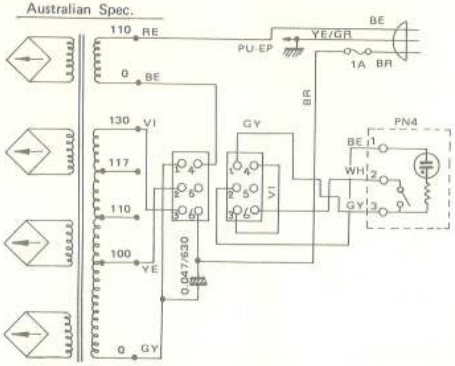
General · South African Spec.



UL · CSA Spec.

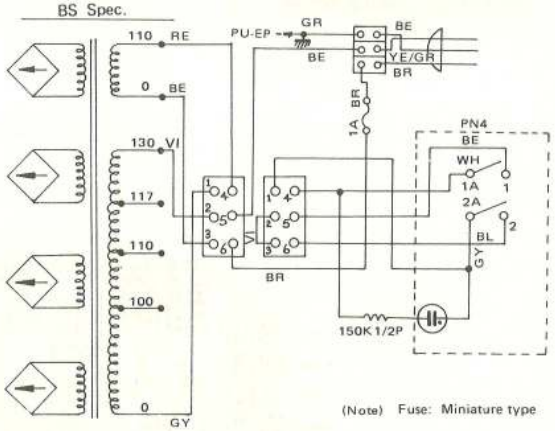


Australian Spec.



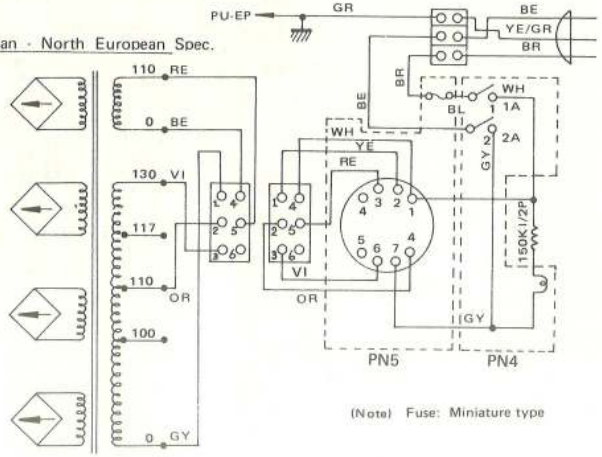
Power Supply NP0018Z (Primary) Circuit Diagram

BS Spec.



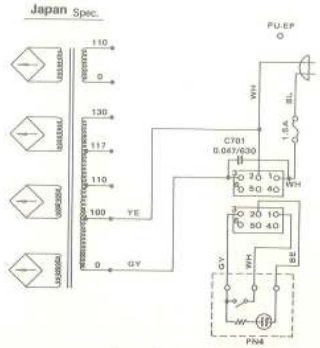
(Note) Fuse: Miniature type

European · North European Spec.

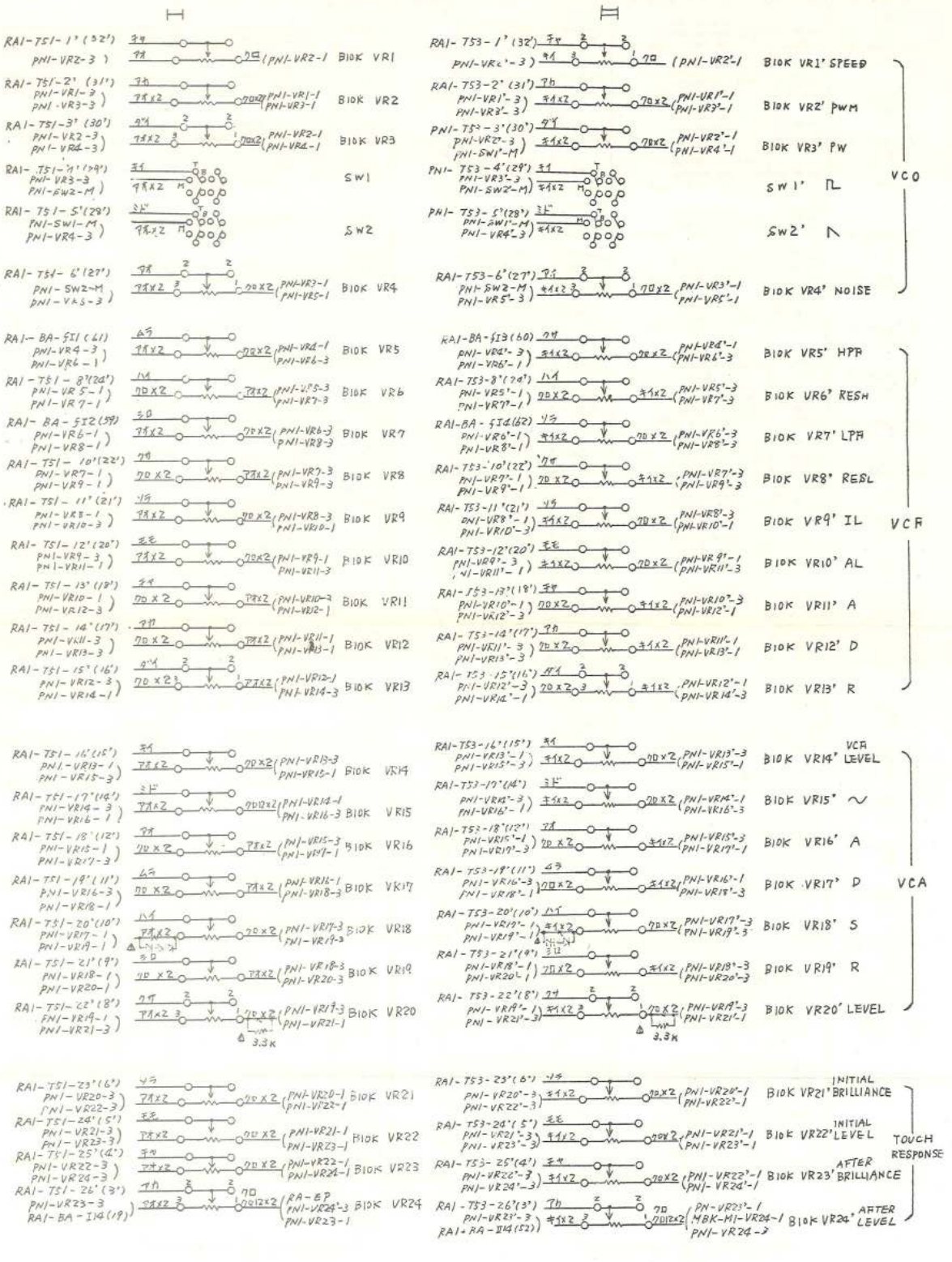


(Note) Fuse: Miniature type

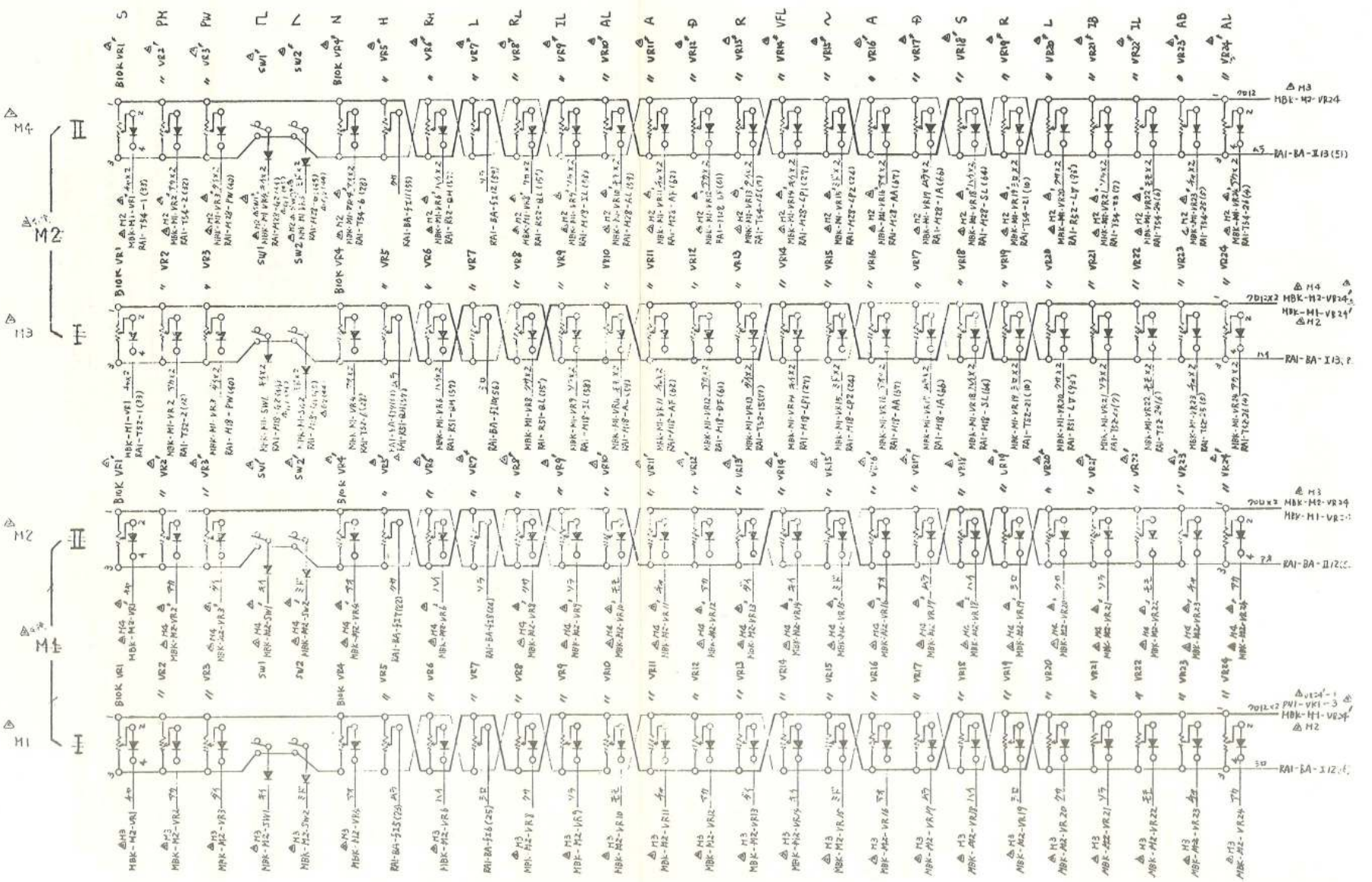
Japan Spec.

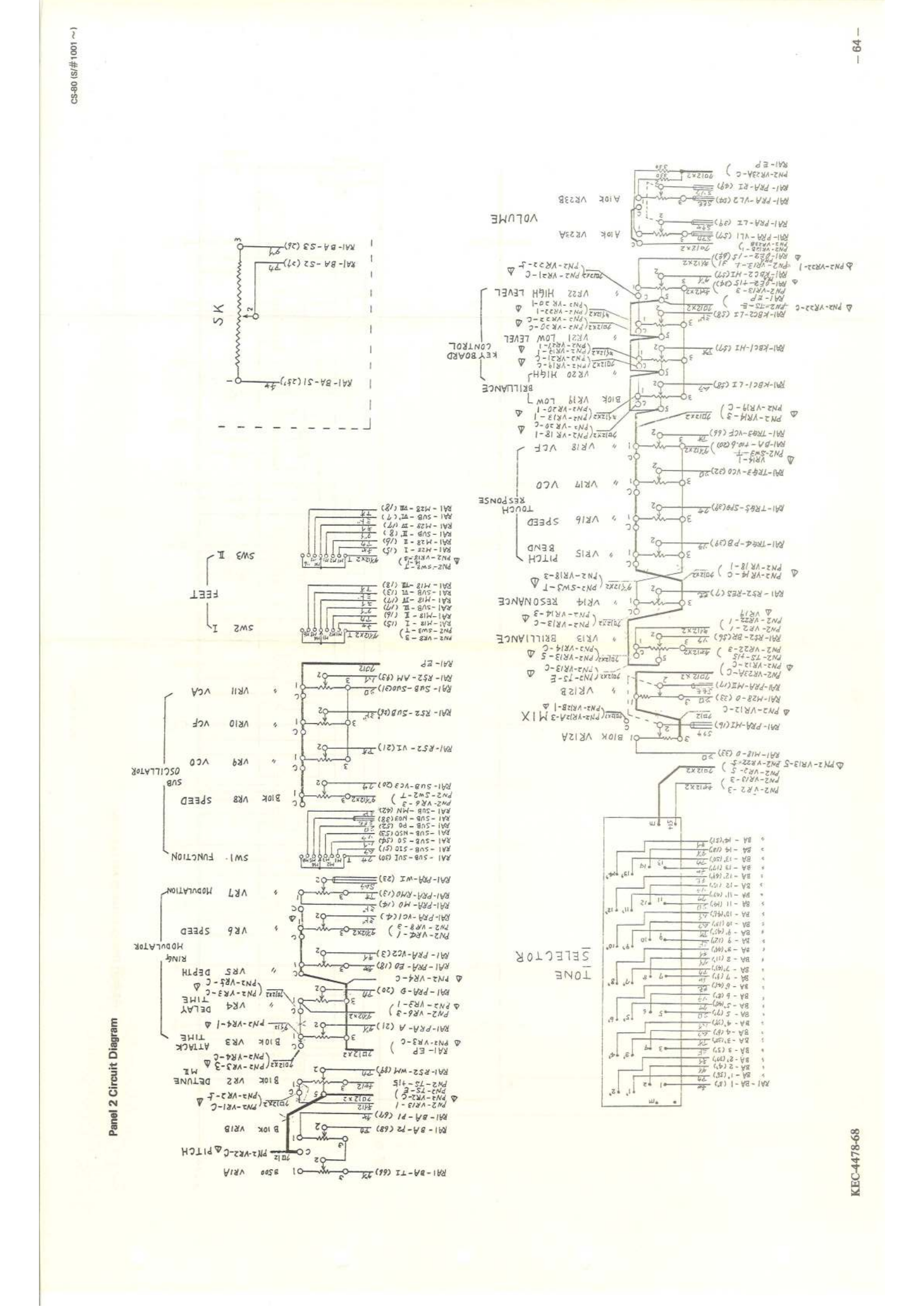


(Note) Fuse: Miniature type


I
II
B10K VR1
B10K VR2
B10K VR3
SW1
SW2
B10K VR4
B10K VR5
B10K VR6
B10K VR7
B10K VR8
B10K VR9
B10K VR10
B10K VR11
B10K VR12
B10K VR13
B10K VR14
B10K VR15
B10K VR16
B10K VR17
B10K VR18
B10K VR19
B10K VR20
B10K VR21
B10K VR22
B10K VR23
B10K VR24
B10K VR1' SPEED
B10K VR2' PWM
B10K VR3' PW
SW1' ⊓
SW2' ⌐
B10K VR4' NOISE
B10K VR5' HPF
B10K VR6' RESH
B10K VR7' LPF
B10K VR8' RESL
B10K VR9' IL
B10K VR10' AL
B10K VR11' A
B10K VR12' D
B10K VR13' R
B10K VR14' LEVEL
B10K VR15' ∼
B10K VR16' A
B10K VR17' D
B10K VR18' S
B10K VR19' R
B10K VR20' LEVEL
B10K VR21' INITIAL BRILLIANCE
B10K VR22' INITIAL LEVEL
B10K VR23' AFTER BRILLIANCE
B10K VR24' AFTER LEVEL
VCO
VCF
VCA
TOUCH RESPONSE
3.3K

Panel 1 (MBK) Circuit Diagram

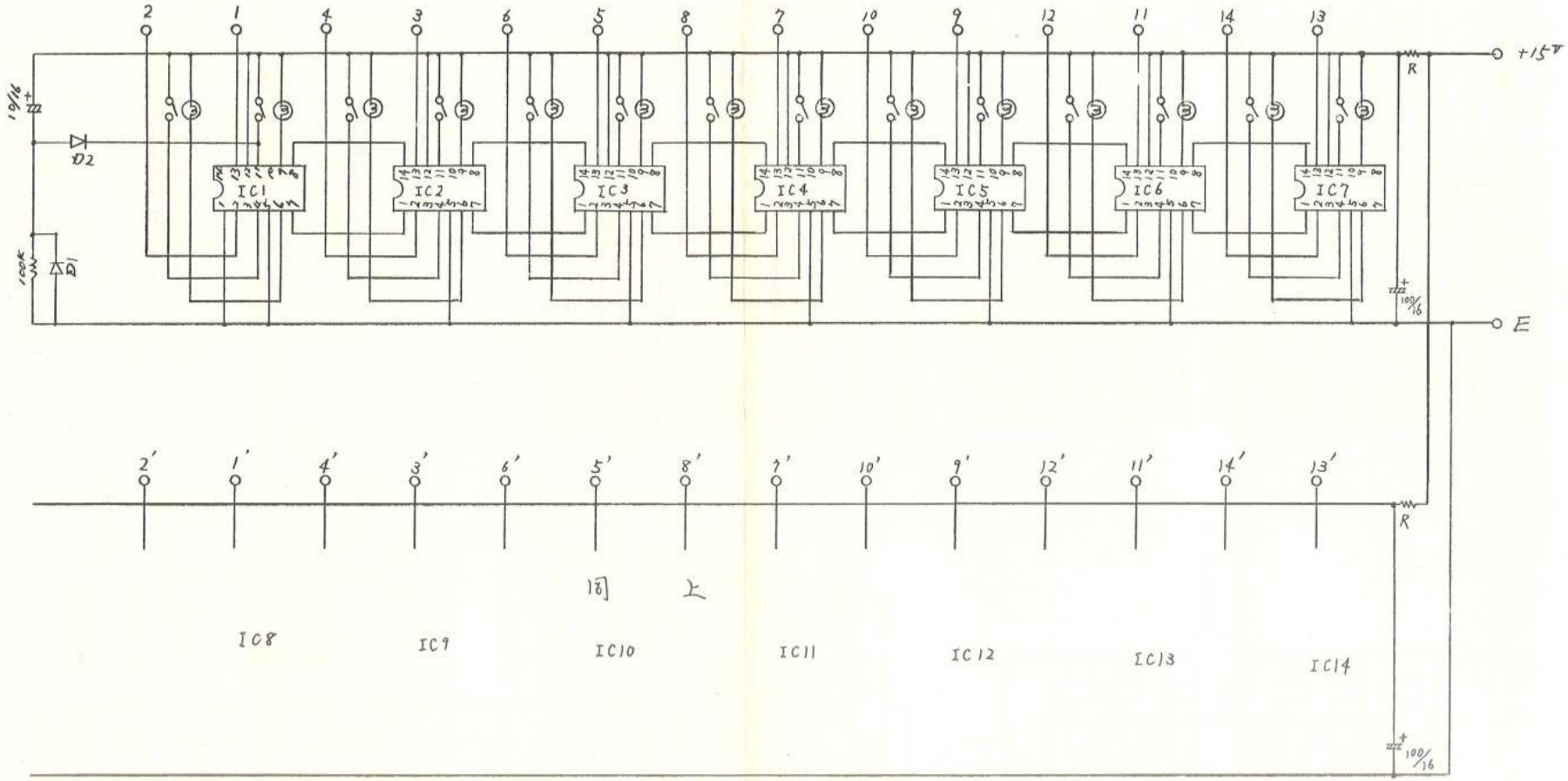




Panel 2 Circuit Diagram

KEC-4478-68

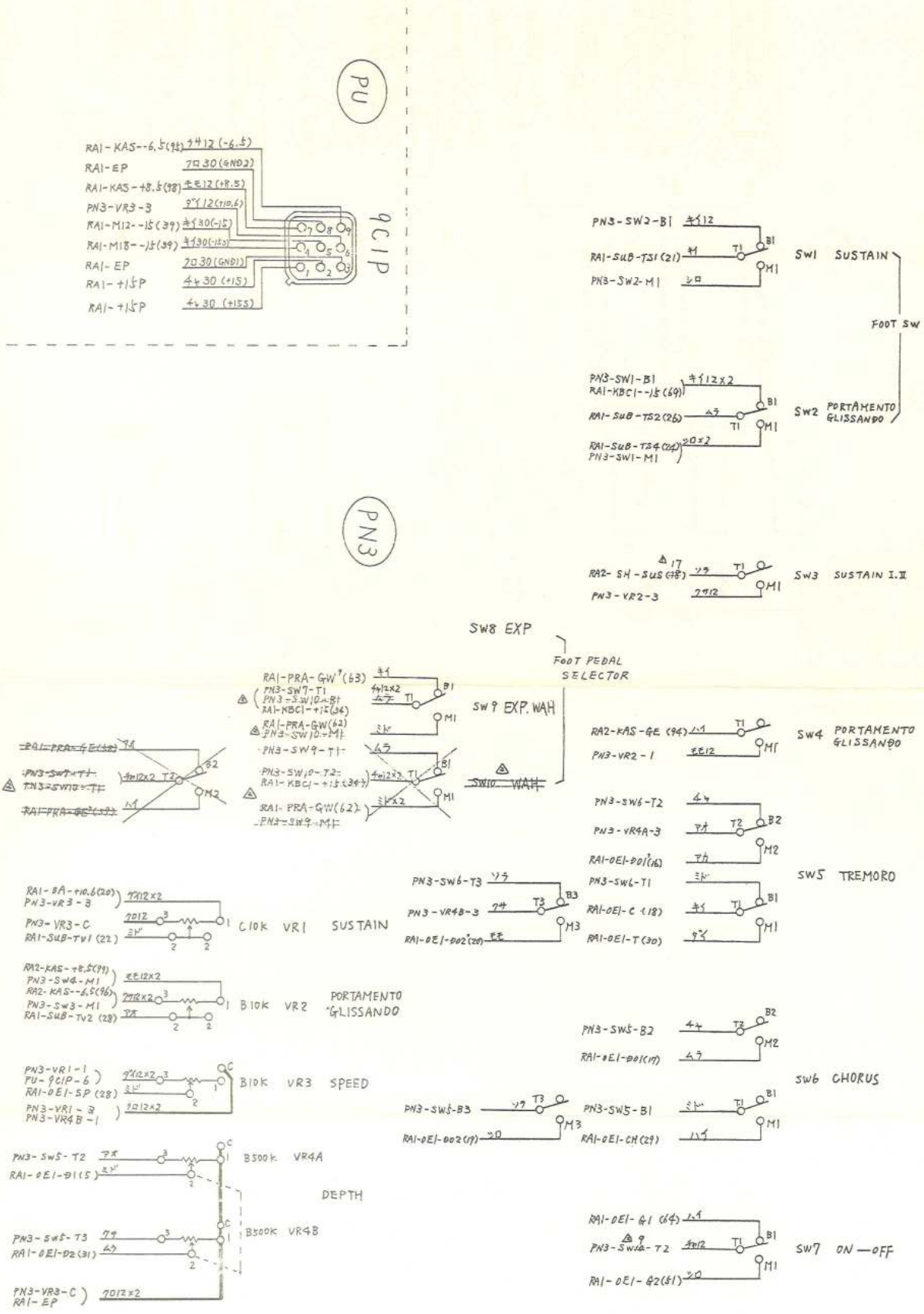
Panel 2 (Tone Selector) Circuit Diagram



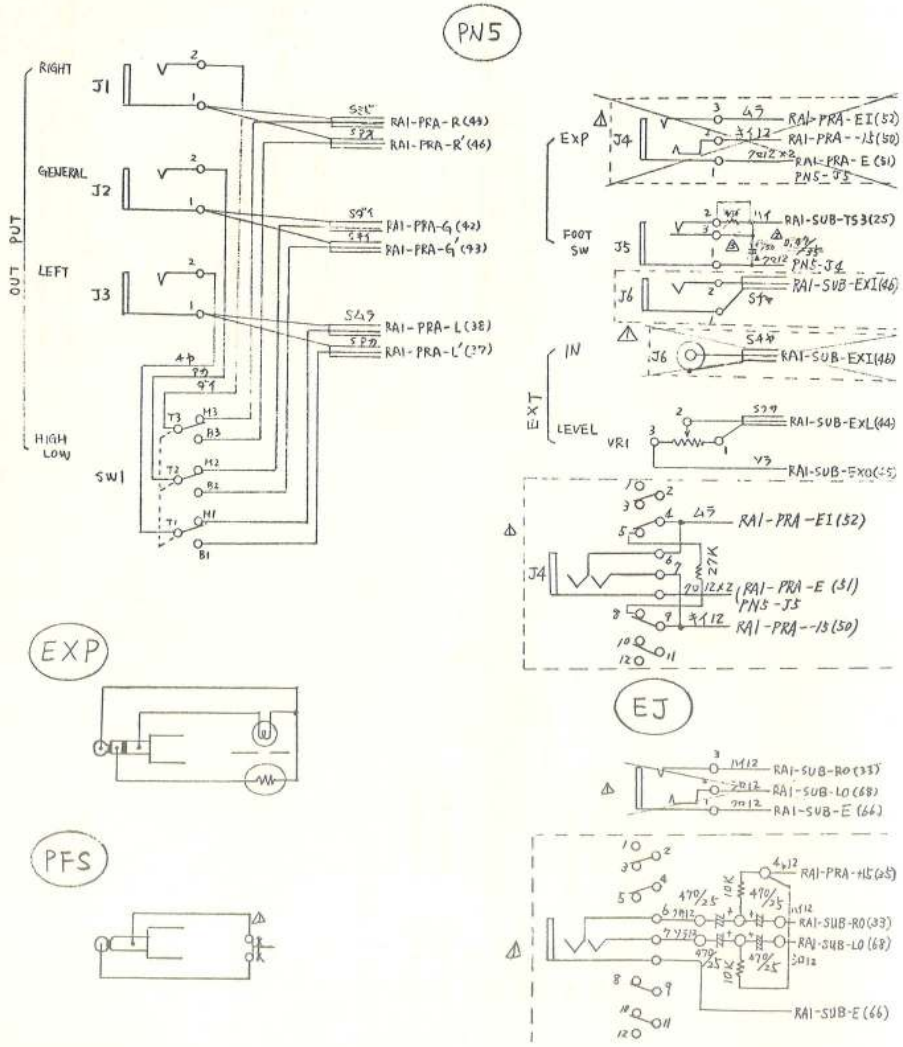
注) IC1~14 ; IG00157

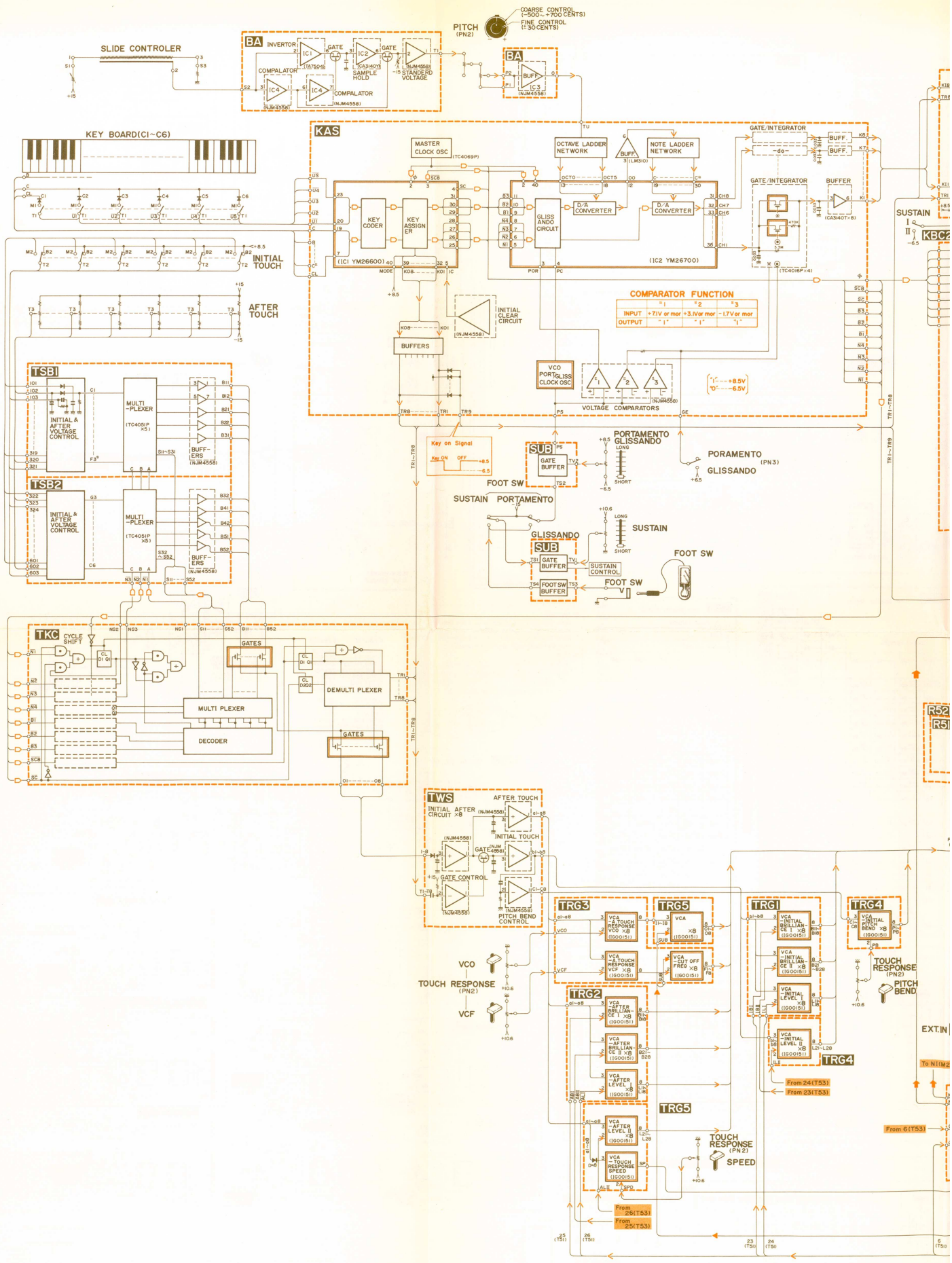
D ; 1S1555

R ; 12Ω/0.5W

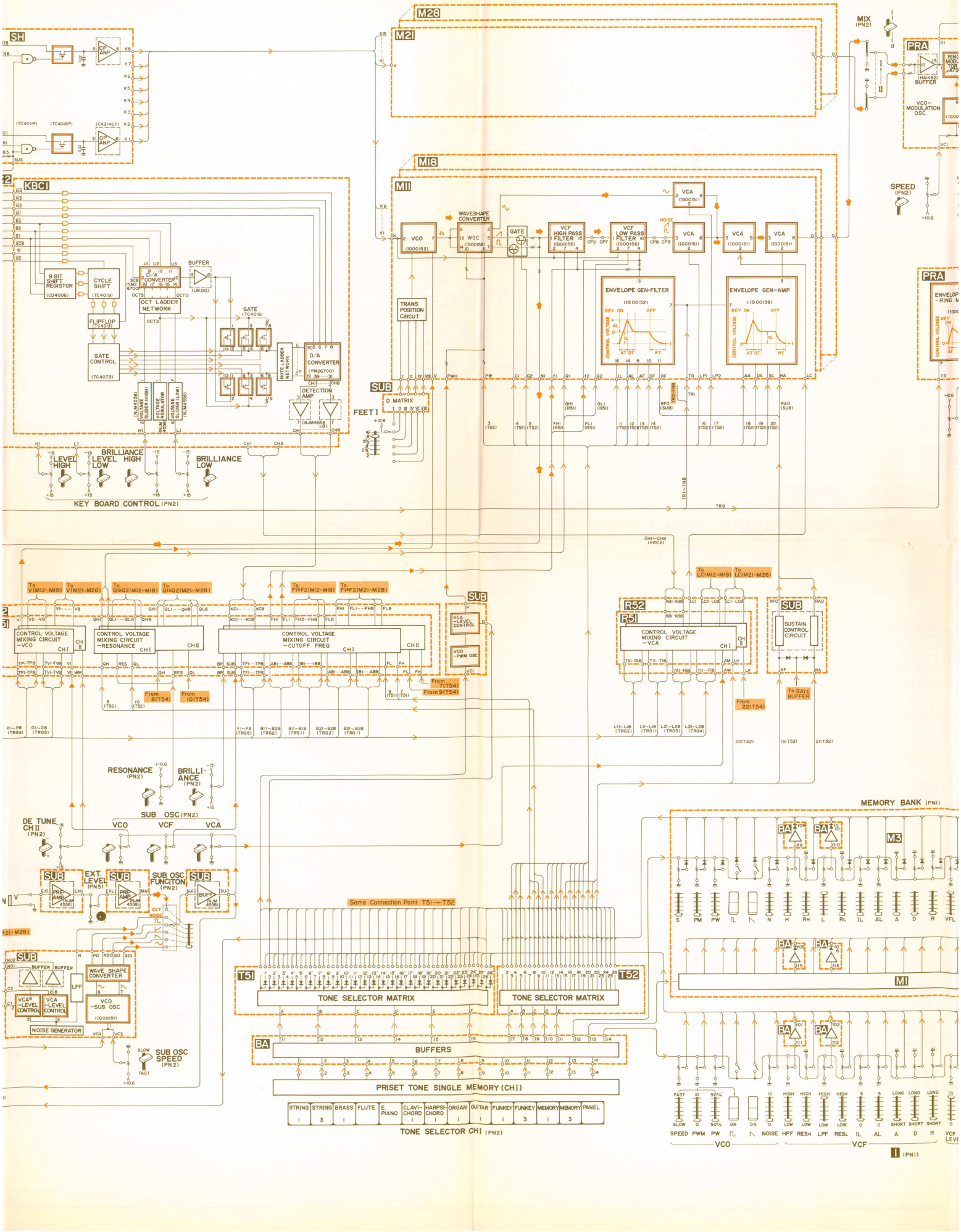

PU
9CIP
RA1-KAS--6.5(93)
RA1-EP
RA1-KAS-+8.5(98)
PN3-VR3-3
RA1-M12--15(39)
RA1-M18--15(39)
RA1-EP
RA1-+15P
RA1-+15P
PN3
PN3-SW2-B1
RA1-SUB-TS1(21)
PN3-SW2-M1
SW1 SUSTAIN
FOOT SW
PN3-SW1-B1
RA1-KBC1--15(69)
RA1-SUB-TS2(25)
RA1-SUB-TS4(24)
PN3-SW1-M1
SW2 PORTAMENTO GLISSANDO
RA2-SH-SUS(18)
PN3-VR2-3
SW3 SUSTAIN I.II
SW8 EXP
FOOT PEDAL SELECTOR
SW9 EXP. WAH
SW10 WAH
RA1-PRA-GW(63)
PN3-SW7-T1
PN3-SW10-B1
RA1-KBC1-+12(34)
RA1-PRA-GW(62)
PN3-SW10-M1
PN3-SW9-T1
PN3-SW10-T2
RA1-KBC1-+15(34)
RA1-PRA-GW(62)
PN3-SW9-M1
RA2-KAS-GE(94)
PN3-VR2-1
SW4 PORTAMENTO GLISSANDO
PN3-SW6-T2
PN3-VR4A-3
RA1-0E1-D01(16)
PN3-SW6-T3
PN3-VR4B-3
RA1-0E1-D02(20)
PN3-SW6-T1
RA1-0E1-C(18)
RA1-0E1-T(30)
SW5 TREMORO
RA1-BA-+10.6(20)
PN3-VR3-3
PN3-VR3-C
RA1-SUB-TV1(22)
C10K VR1 SUSTAIN
RA2-KAS-+8.5(91)
PN3-SW4-M1
RA2-KAS--6.5(96)
PN3-SW3-M1
RA1-SUB-TV2(28)
B10K VR2 PORTAMENTO GLISSANDO
PN3-VR1-1
PU-9CIP-6
RA1-0E1-SP(28)
PN3-VR1-3
PN3-VR4B-1
B10K VR3 SPEED
PN3-SW5-B2
RA1-0E1-D01(17)
PN3-SW5-B3
RA1-0E1-D02(19)
PN3-SW5-T2
RA1-0E1-D1(5)
B500K VR4A
DEPTH
PN3-SW5-T3
RA1-0E1-D2(31)
B500K VR4B
PN3-VR3-C
RA1-EP
SW6 CHORUS
PN3-SW5-B1
RA1-0E1-CH(29)
RA1-0E1-G1(64)
PN3-SW9-T2
RA1-0E1-G2(61)
SW7 ON—OFF

Panel 5 (EJ, EXP, PFS) Circuit Diagram

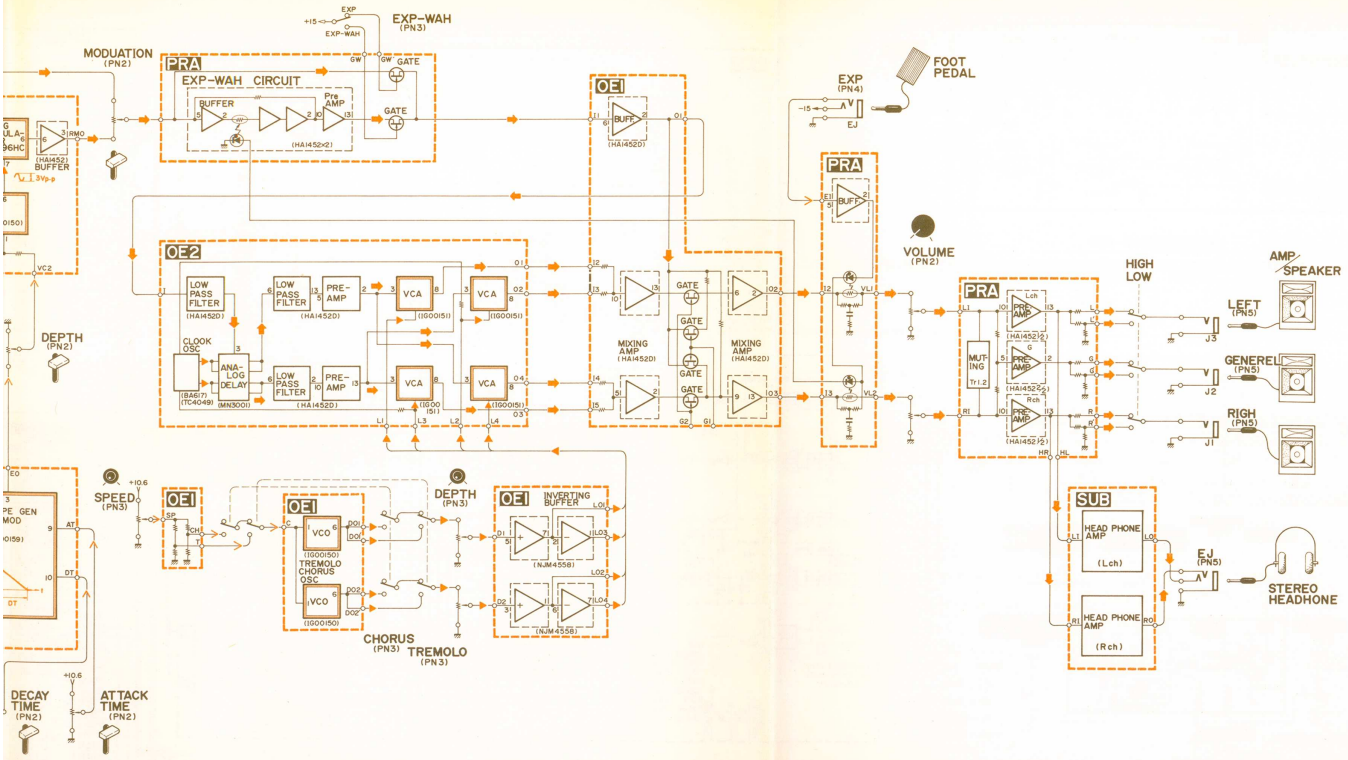



SLIDE CONTROLER
BA
INVERTOR
COMPALATOR
SAMPLE HOLD
STANDERD VOLTAGE
PITCH (PN2)
COARSE CONTROL (-500~+700 CENTS)
FINE CONTROL (±30CENTS)
KEY BOARD(C1~C6)
KAS
MASTER CLOCK OSC
OCTAVE LADDER NETWORK
NOTE LADDER NETWORK
GATE/INTEGRATOR
BUFFER
KEY CODER
KEY ASSIGNER
GLISS ANDO CIRCUIT
D/A CONVERTER
(IC1 YM26600)
(IC2 YM26700)
SUSTAIN
INITIAL TOUCH
AFTER TOUCH
COMPARATOR FUNCTION
INPUT
OUTPUT
INITIAL CLEAR CIRCUIT
BUFFERS
VCO PORT/GLISS CLOCK OSC
VOLTAGE COMPARATORS
TSB1
TSB2
INITIAL & AFTER VOLTAGE CONTROL
MULTI-PLEXER
BUFFERS
Key on Signal
SUB
GATE BUFFER
FOOT SW
SUSTAIN PORTAMENTO
PORTAMENTO GLISSANDO
PORAMENTO (PN3) GLISSANDO
GLISSANDO
SUSTAIN CONTROL
FOOT SW BUFFER
TKC
CYCLE SHIFT
GATES
MULTI PLEXER
DEMULTI PLEXER
DECODER
TWS
INITIAL AFTER CIRCUIT ×8
GATE CONTROL
PITCH BEND CONTROL
TRG1
TRG2
TRG3
TRG4
TRG5
VCA -INITIAL BRILLIANCE I ×8
VCA -INITIAL BRILLIANCE II ×8
VCA -INITIAL LEVEL I ×8
VCA -INITIAL LEVEL II ×8
VCA -AFTER BRILLIANCE I ×8
VCA -AFTER BRILLIANCE II ×8
VCA -AFTER LEVEL I ×8
VCA -AFTER LEVEL II ×8
VCA A.TOUCH RESPONSE VCO ×8
VCA A.TOUCH RESPONSE VCF ×8
VCA -CUT OFF FREQ ×8
VCA -TOUCH RESPONSE SPEED
VCO
VCF
TOUCH RESPONSE (PN2)
PITCH BEND
SPEED
EXT.IN
From 24(T53)
From 23(T53)
From 26(T53)
From 25(T53)
From 6(T53)

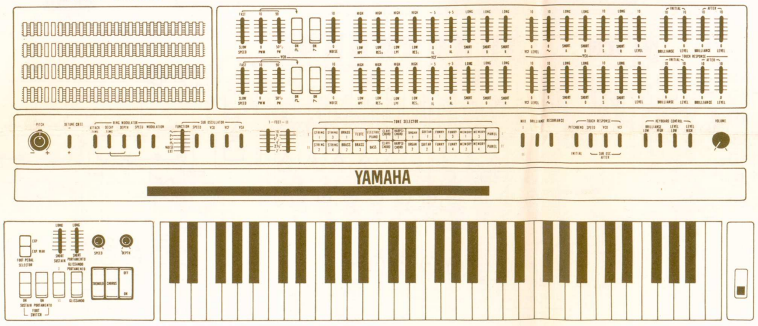
CS-80 SYNTHESIZER BLOCK DIAGRAM



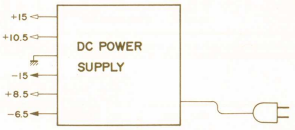
CS-80 SYNTHESIZER BLOCK DIAGRAM

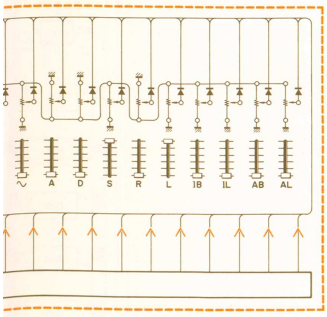


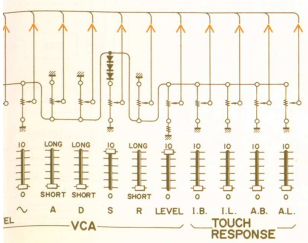
PANEL LAYOUT

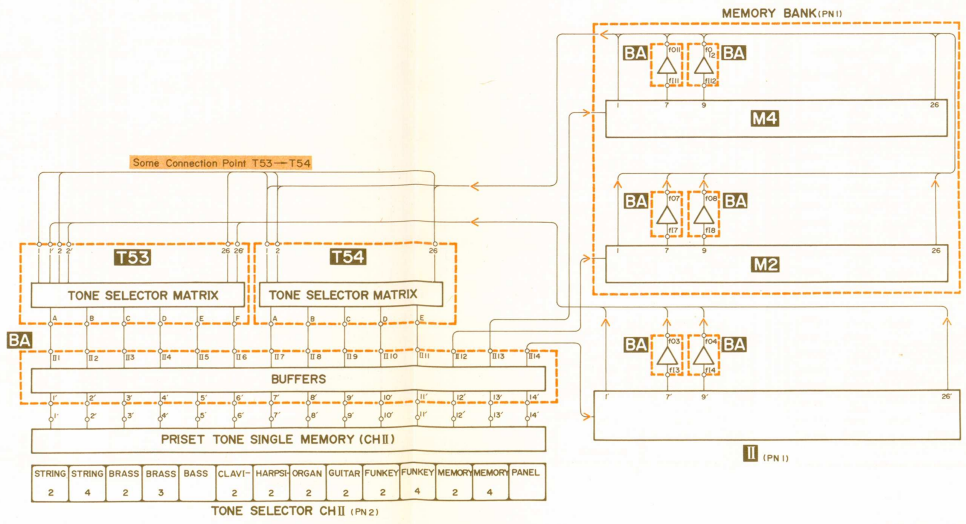


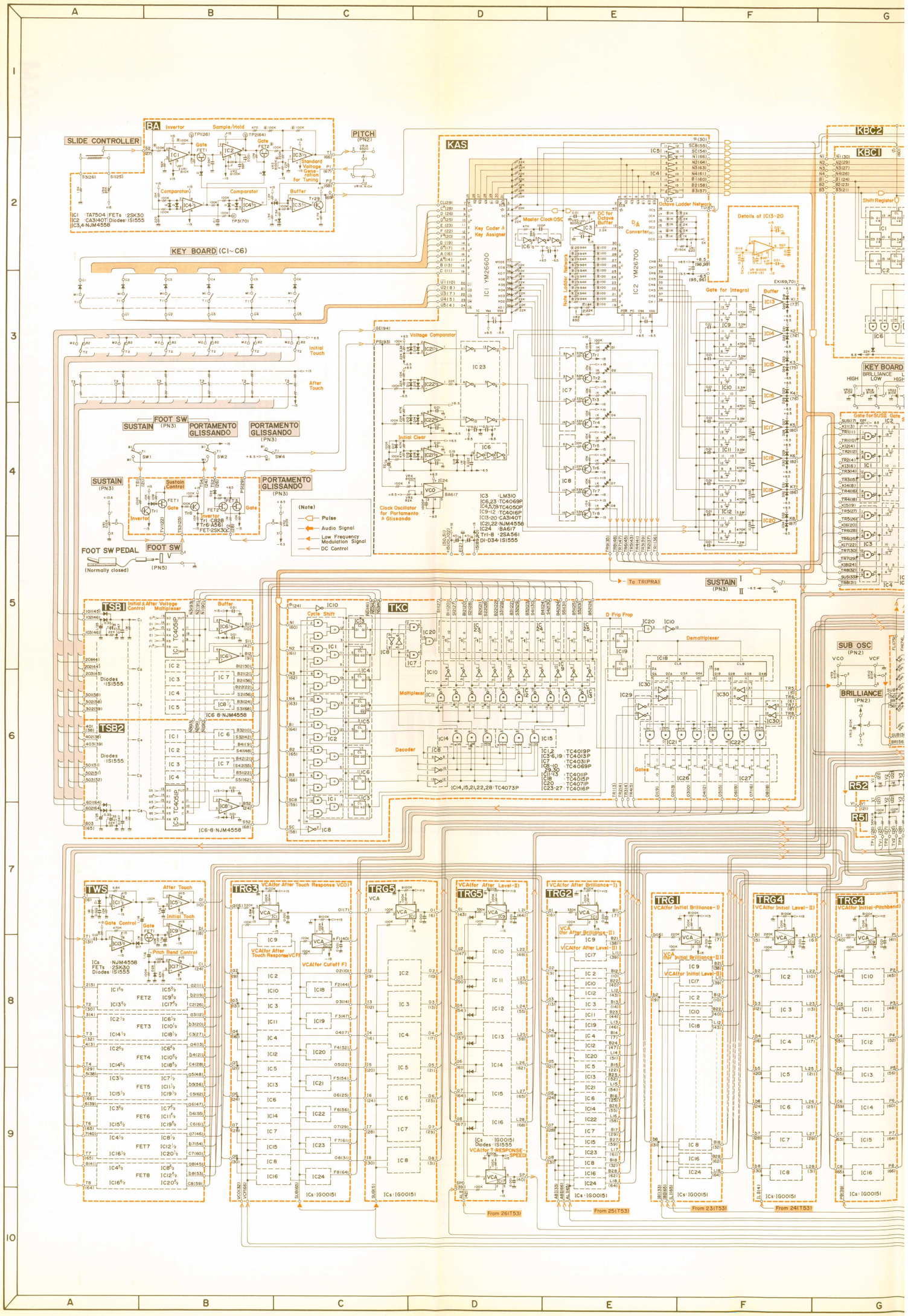
—□— PULSE —◀— AUDIO SIGNAL —◀— LOW FREQUECY MODULATION SIGNAL —◁— DC CONTROL SIGNAL










SLIDE CONTROLLER
BA
Inverter
Sample/Hold
PITCH (PN2)
IC1 : TA7504 FETs : 2SK30
IC2 : CA3140T Diodes : 1S1555
IC3,4 : NJM4558
KEY BOARD (C1~C6)
KAS
Key Coder & Key Assigner
Master Clock OSC
DC for Octave Buffer
Octave Ladder Network
Note Ladder Network
Gate for Integral
Buffer
KBC2
KBC1
Shift Register
KEY BOARD
BRILLIANCE
HIGH
LOW
FOOT SW (PN3)
SUSTAIN
PORTAMENTO GLISSANDO
Initial Touch
After Touch
Voltage Comparator
Initial Clear
Clock Oscillator for Portamento & Glissando
VCO
IC3 : LM310
IC6,23 : TC4069P
IC4,5,7,9-TC4050P
IC9~12 : TC4016P
IC13-20 : CA3140T
IC21,22 : NJM4556
IC24 : BA617
Tr1~8 : 2SA561
DI : 1S1555
(Note)
Pulse
Audio Signal
Low Frequency Modulation Signal
DC Control
FOOT SW PEDAL
(Normally closed)
(PN5)
To TR(PRA)
TSB1
Initial & After Voltage Multiplexer
TSB2
Diodes : 1S1555
IC6-8 : NJM4558
TKC
Cycle Shift
Multiplexer
Decoder
D Flip Flop
Demultiplexer
Gates
IC14,15,21,22,28 : TC4073P
IC23-27 : TC4016P
SUB OSC (PN2)
BRILLIANCE (PN2)
R52
R51
TWS
After Touch
Initial Toch
Gate Control
Pitch Bend Control
ICs : NJM4558
FETs : 2SK30
Diodes : 1S1555
TRG3
VCA(for After Touch Response VCO)
VCA(for Cutoff F)
TRG5
VCA
VCA(for After Level-II)
TRG2
VCA(for After Brilliance-I)
VCA(for After Brilliance-II)
VCA(for After Level-II)
TRG1
VCA(for Initial Brilliance-I)
VCA for Initial Brilliance-II
VCA(for Initial Level-II)
TRG4
VCA(for Initial Level-II)
TRG4
VCA(for Initial-Pitchbend)
ICs : IG00151
Diodes : 1S1555
VCA(for T-RESPONSE-SPEED)
From 26(T53)
From 25(T53)
From 23(T53)
From 24(T53)

CS-80 OVERALL CIRCUIT DIAGRAM

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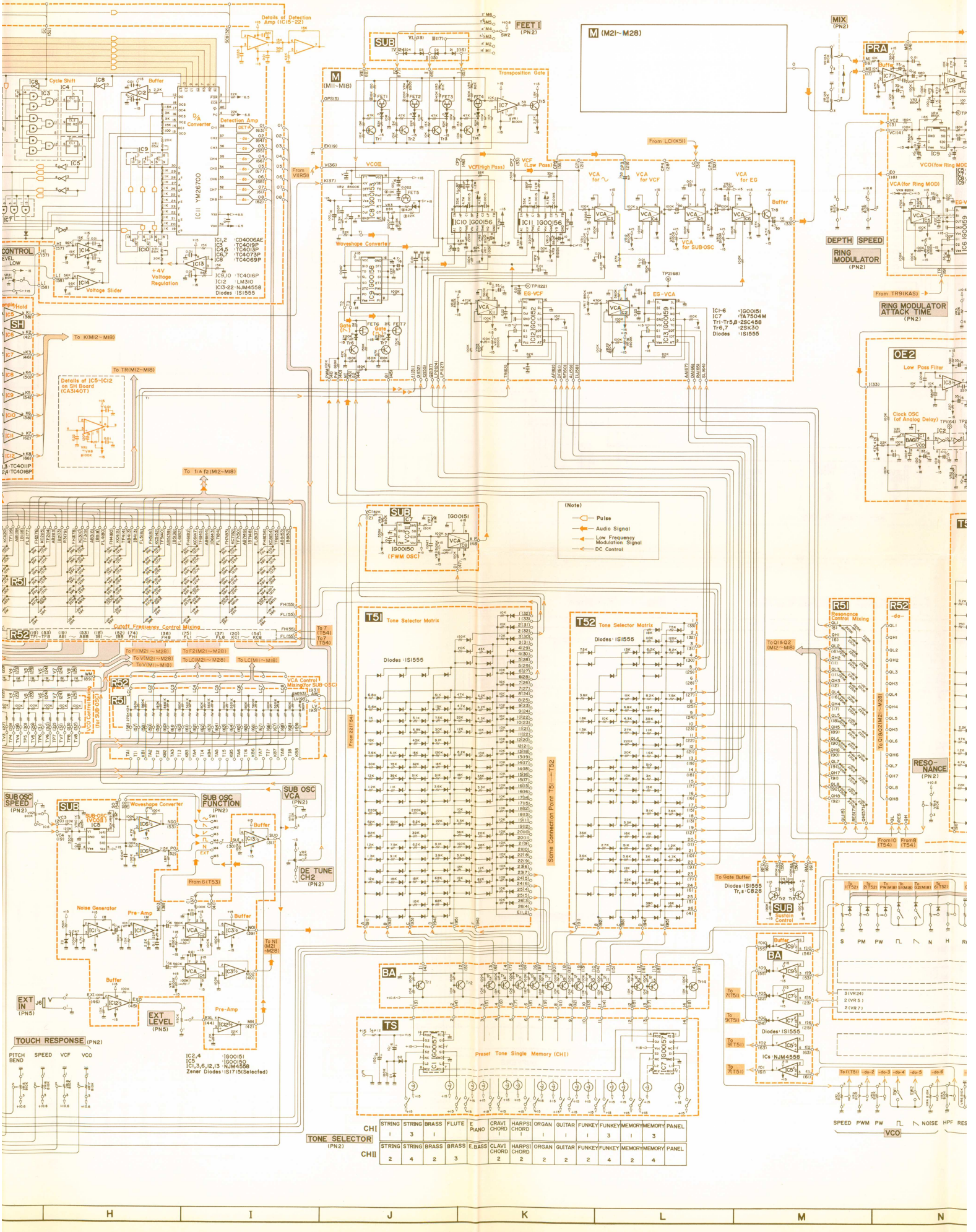

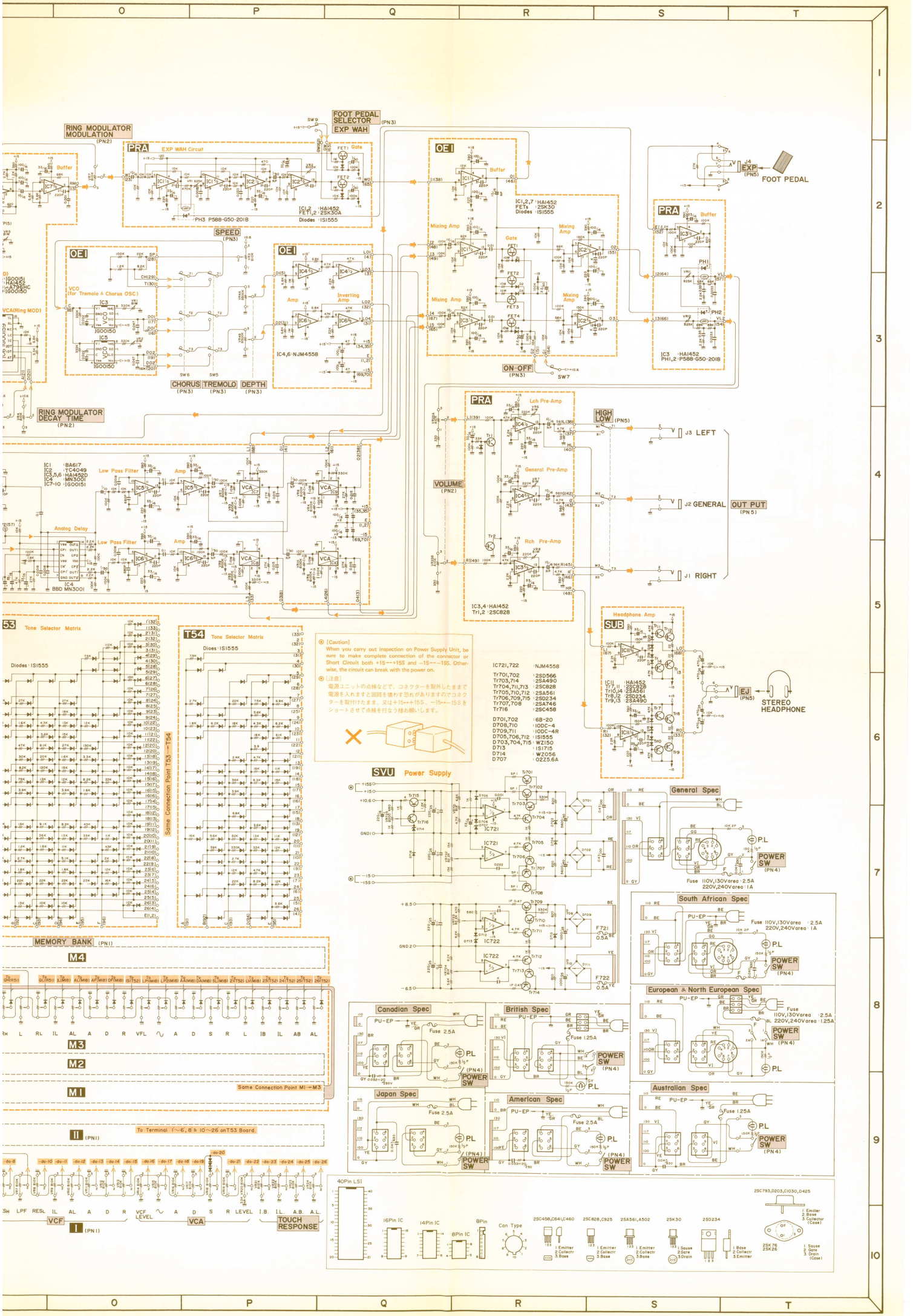
RING MODULATOR MODULATION (PN2)
PRA EXP WAH Circuit
PH3 P588-G50-201B
FOOT PEDAL SELECTOR (PN3)
EXP WAH
FOOT PEDAL
SPEED (PN3)
VCO (for Tremolo & Chorus OSC)
CHORUS (PN3)
TREMOLO (PN3)
DEPTH (PN3)
RING MODULATOR DECAY TIME (PN2)
OEI
Buffer
Mixing Amp
Gate
Inverting Amp
IC1,2,7 :HA1452
FETs :2SK30
Diodes :1S1555
IC4,6 :NJM4558
ON-OFF (PN3)
SW7
PRA
Buffer
PH1
PH2
IC3 :HA1452
PH1,2 :P588-G50-201B
J4 EXP (PN5)
IC1 :BA617
IC2 :TC4049
IC3,5,6 :HA14529
IC4 :MN3001
IC7-10 :IG00151
Low Pass Filter
Amp
VCA
Analog Delay
BBD MN3001
VOLUME (PN2)
Lch Pre-Amp
General Pre-Amp
Rch Pre-Amp
HIGH LOW (PN5)
J3 LEFT
J2 GENERAL OUT PUT (PN5)
J1 RIGHT
IC3,4 :HA1452
Tr1,2 :2SC828
T53 Tone Selector Matrix
T54 Tone Selector Matrix
Diodes :1S1555
Same Connection Point T53 ⟷ T54
[Caution]
When you carry out inspection on Power Supply Unit, be sure to make complete connection of the connector or Short Circuit both +15⟷+15S and −15⟷−15S. Otherwise, the circuit can break with the power on.
[注意]
電源ユニットの点検などで、コネクターを取外したままで電源を入れますと回路を壊す恐れがありますのでコネクターを取付けたまま、又は+15⟷+15S、−15⟷−15Sをショートさせて点検を行なう様お願いします。
IC721,722 :NJM4558
Tr701,702 :2SD566
Tr703,714 :2SA490
Tr704,711,713 :2SC828
Tr705,710,712 :2SA561
Tr706,709,715 :2SD234
Tr707,708 :2SA746
Tr716 :2SC458
D701,702 :6B-20
D708,710 :10DC-4
D709,711 :10DC-4R
D705,706,712 :1S1555
D703,704,715 :WZ150
D713 :1S1715
D714 :WZ056
D707 :02Z5.6A
SVU Power Supply
IC721
IC722
F721 0.5A
F722 0.5A
SUB
Headphone Amp
IC11 :HA1452
Tr7,11 :2SC828
Tr10,14 :2SA561
Tr8,12 :2SD234
Tr9,13 :2SA490
EJ (PN5)
STEREO HEADPHONE
MEMORY BANK (PN1)
M4
M3
M2
M1
Same Connection Point M1⟷M3
II (PN1)
To Terminal 1~6, 8 & 10~26 on T53 Board.
I (PN1)
VCF
VCA
TOUCH RESPONSE
General Spec
South African Spec
European & North European Spec
Canadian Spec
British Spec
Japan Spec
American Spec
Australian Spec
POWER SW (PN4)
P.L
PU-EP
Fuse 2.5A
Fuse 1.25A
Fuse 110V,130Varea :2.5A
220V,240Varea :1A
40Pin LSI
16Pin IC
14Pin IC
8Pin IC
8Pin Can Type
2SC458,C641,C460
2SC828,C925
2SA561,A502
2SK30
2SD234
2SC793,D203,C1030,D425
2SK76
2SK26